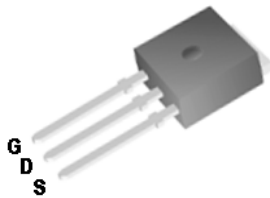


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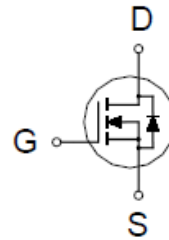
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
700V	$15\Omega @ V_{GS} = 10V$	1A



TO-251



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	700	V
Gate-Source Voltage		$V_{GS}$	$\pm 30$	
Continuous Drain Current <sup>2</sup>	$T_C = 25\text{ }^\circ\text{C}$	$I_D$	1	A
	$T_C = 100\text{ }^\circ\text{C}$		0.6	
Pulsed Drain Current <sup>1,2</sup>		$I_{DM}$	3	
Avalanche Current <sup>3</sup>		$I_{AS}$	0.9	
Avalanche Energy <sup>3</sup>	$L = 10\text{mH}$	$E_{AS}$	4	mJ
Power Dissipation <sup>A</sup>	$T_C = 25\text{ }^\circ\text{C}$	$P_D$	27.6	W
	$T_C = 100\text{ }^\circ\text{C}$		11	
Operating Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		4.53	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		110	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Limited only by maximum temperature allowed.

<sup>3</sup> $V_{DD} = 50V$ , Starting  $T_J = 25\text{ }^\circ\text{C}$

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## N-Channel Enhancement Mode MOSFET

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	700			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	3.6	4.5	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 30V$			$\pm 250$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 700V, V_{GS} = 0V, T_C = 25^\circ C$			1	$\mu A$
		$V_{DS} = 560V, V_{GS} = 0V, T_C = 100^\circ C$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 0.5A$		10.7	15	$\Omega$
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 10V, I_D = 0.5A$		1		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		166		pF
Output Capacitance	$C_{oss}$			57		
Reverse Transfer Capacitance	$C_{rss}$			3		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DD} = 560V, I_D = 1A, V_{GS} = 10V$		3.2		nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			1		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			1		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DD} = 350V, I_D = 1A, R_G = 25\Omega$		12		nS
Rise Time <sup>2</sup>	$t_r$			50		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			20		
Fall Time <sup>2</sup>	$t_f$			30		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>						
Continuous Current <sup>3</sup>	$I_S$				1	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 1A, V_{GS} = 0V$			1.4	V
Reverse Recovery Time	$t_{rr}$	$I_F = 1A, di_F/dt = 100A / \mu S, V_{GS} = 0V$		200		nS
Reverse Recovery Charge	$Q_{rr}$			0.31		nC

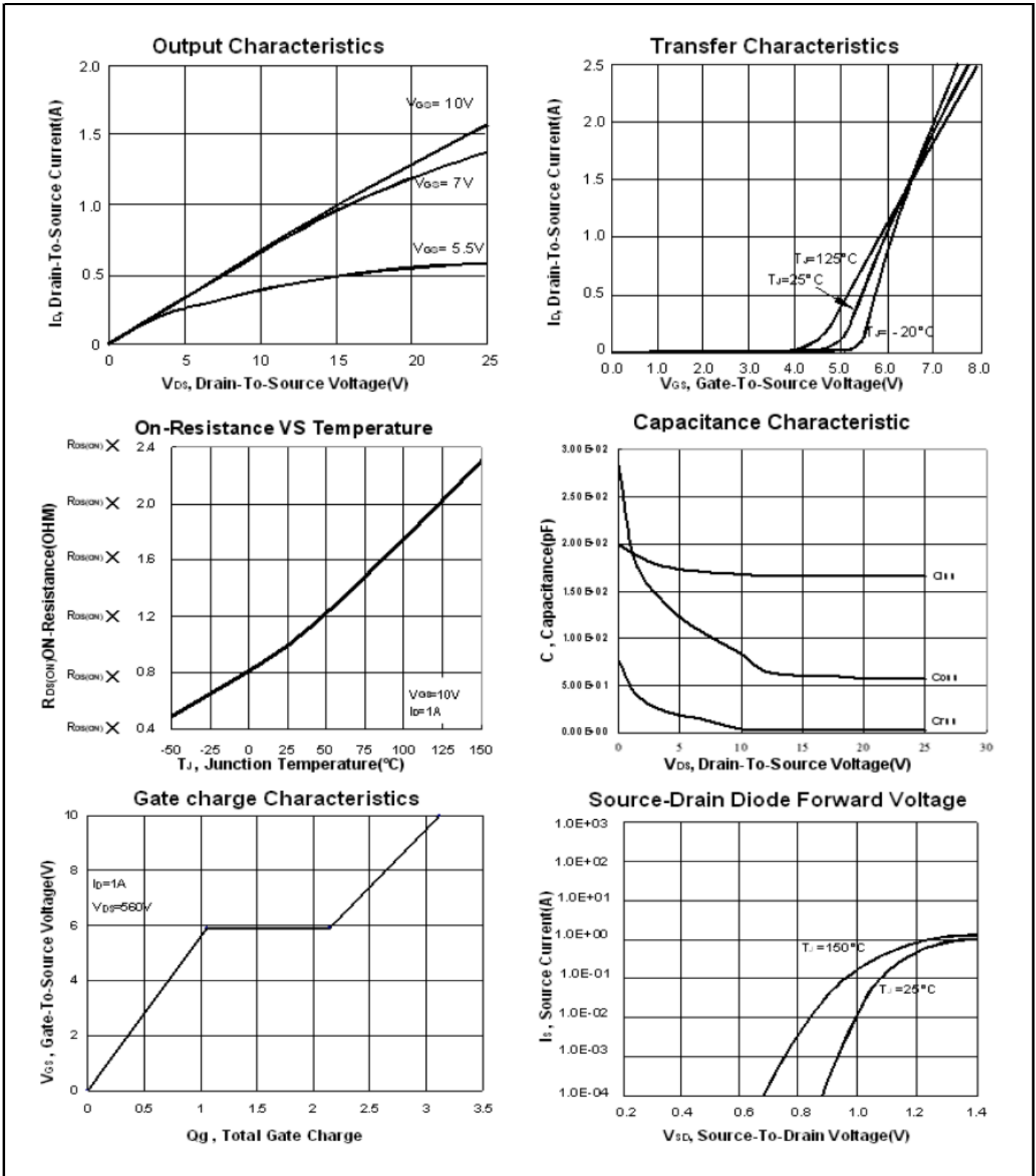
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

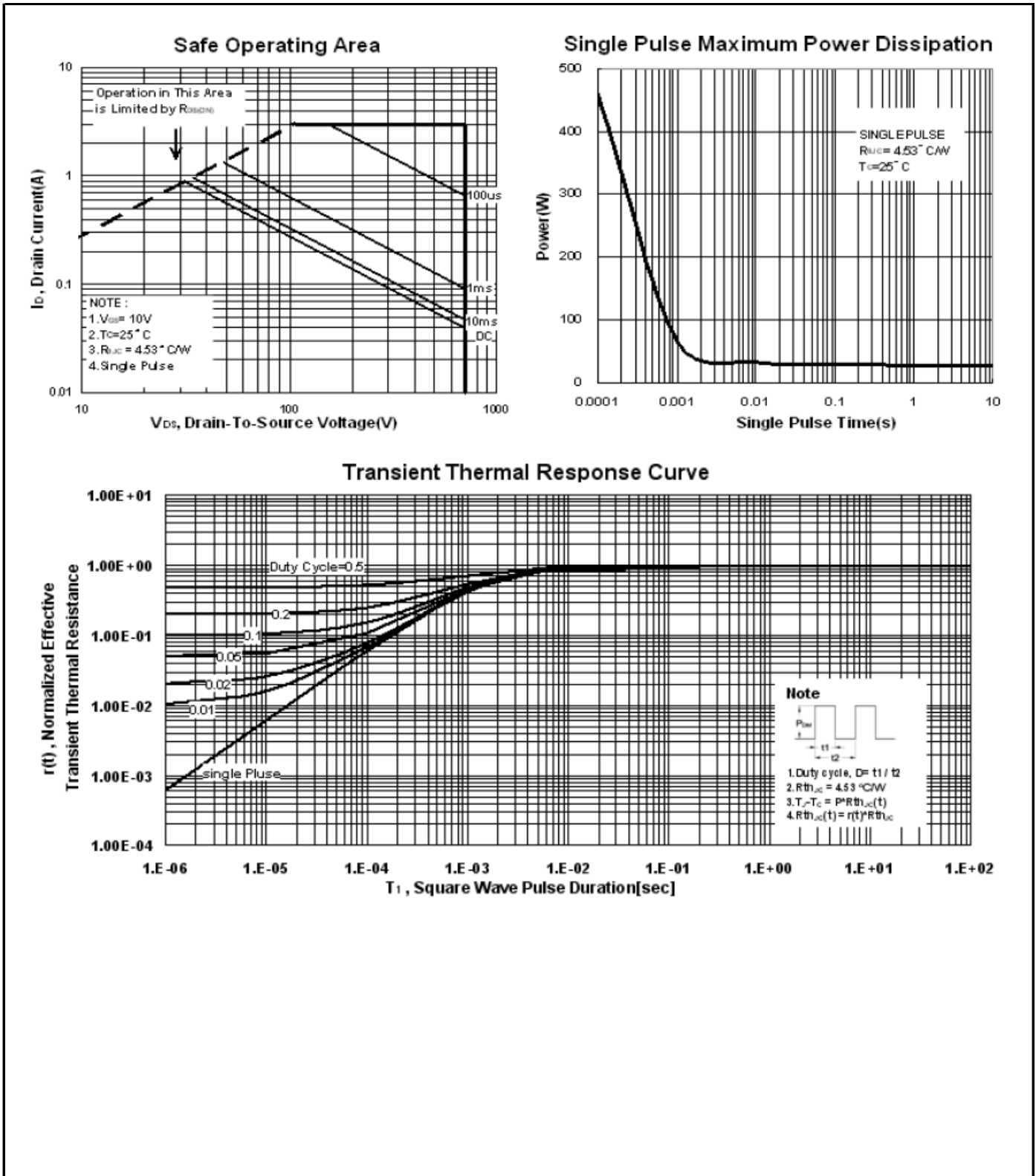
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### Package Dimension

### TO-251 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	14	15	17.14	H	0.89		1.7
B	2.1	2.3	2.5	I	6.3		6.8
C	0.4	0.5	0.6	J	4.8		5.5
D	0.35	0.5	0.65	K	0.5	0.84	1.14
E	0.9	1.1	1.5	L	0.4	0.76	0.912
F	7		9.65	M		2.3	
G	5.3		6.22	N	1.4	2.16	2.23

