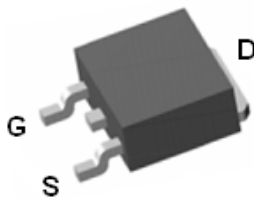


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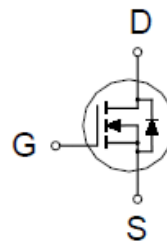
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
650V	$2.5\Omega @ V_{GS} = 10V$	4A



TO-252



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current ²	I_D	$T_C = 25\text{ }^\circ\text{C}$	4
		$T_C = 100\text{ }^\circ\text{C}$	2.5
Pulsed Drain Current ^{1, 2}	I_{DM}	7	A
Avalanche Energy ³	E_{AS}	20	mJ
Power Dissipation	P_D	$T_C = 25\text{ }^\circ\text{C}$	26
		$T_C = 100\text{ }^\circ\text{C}$	10
Operating Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		4.8	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

²Limited only by maximum temperature allowed.

³ $V_{DD} = 50V$, $L = 10mH$, starting $T_J = 25\text{ }^\circ\text{C}$.

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	650			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.5	3.6	4.5	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±30V			±100	nA
Gate Voltage Drain Current	I _{DSS}	V _{DS} = 650V, V _{GS} = 0V, T _C = 25 °C			25	μA
		V _{DS} = 520V, V _{GS} = 0V, T _C = 100 °C			250	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 2A		2	2.5	Ω
Forward Transconductance ¹	g _{fs}	V _{DS} = 15V, I _D = 2A		3.1		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		766		pF
Output Capacitance	C _{oss}			55		
Reverse Transfer Capacitance	C _{rss}			8		
Total Gate Charge ²	Q _g	V _{DD} = 520V, I _D = 4A, V _{GS} = 10V		16.1		nC
Gate-Source Charge ²	Q _{gs}			4.6		
Gate-Drain Charge ²	Q _{gd}			5.6		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 325V, I _D = 4A, R _G = 6Ω		23		nS
Rise Time ²	t _r			26		
Turn-Off Delay Time ²	t _{d(off)}			65		
Fall Time ²	t _f			31		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current ³	I _S				4	A
Forward Voltage ¹	V _{SD}	I _F = 2A, V _{GS} = 0V			1.5	V
Reverse Recovery Time	t _{rr}	I _F = 4A, di _F /dt = 100A / μS		304		nS
Reverse Recovery Charge	Q _{rr}				1.1	

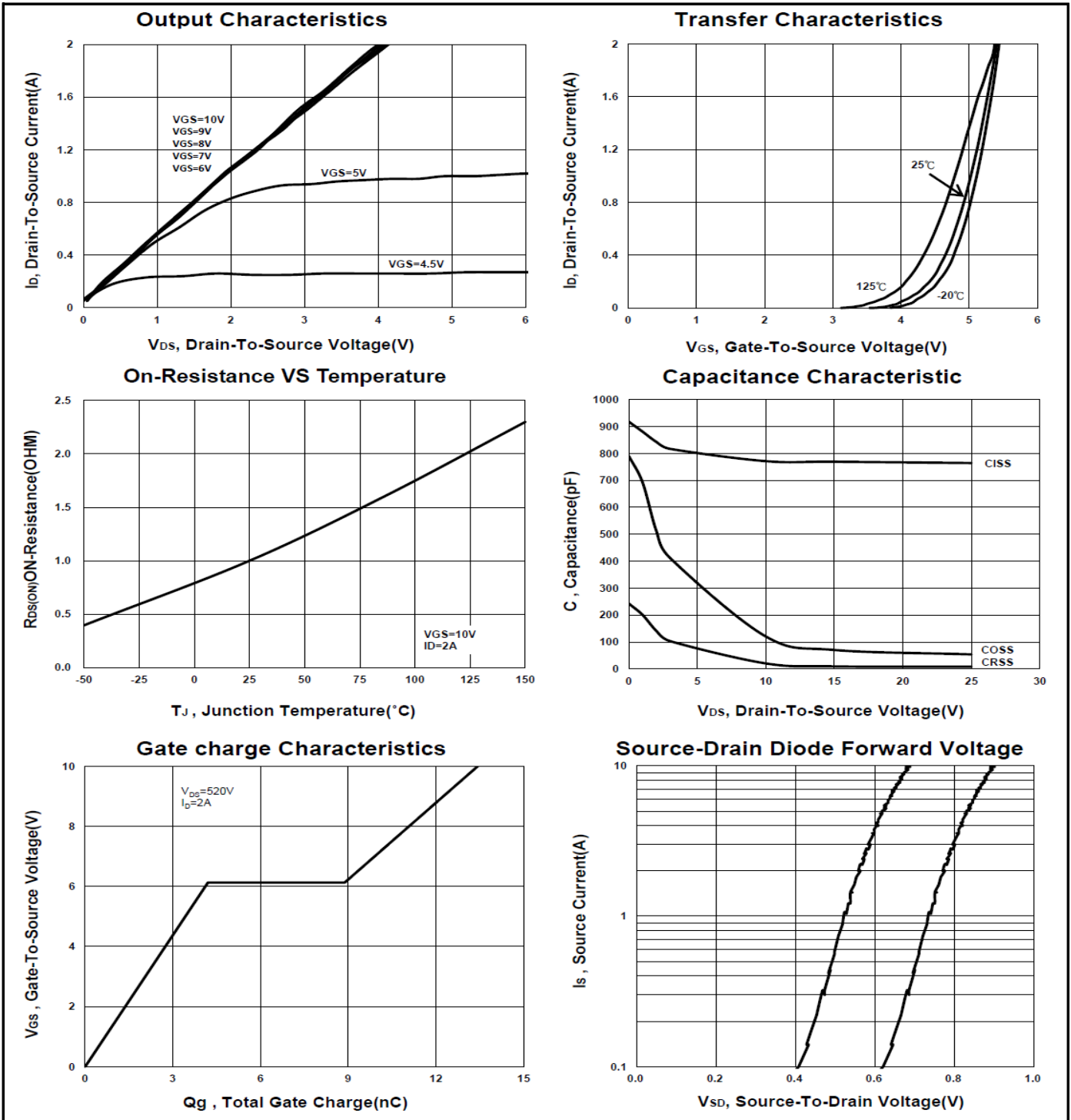
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

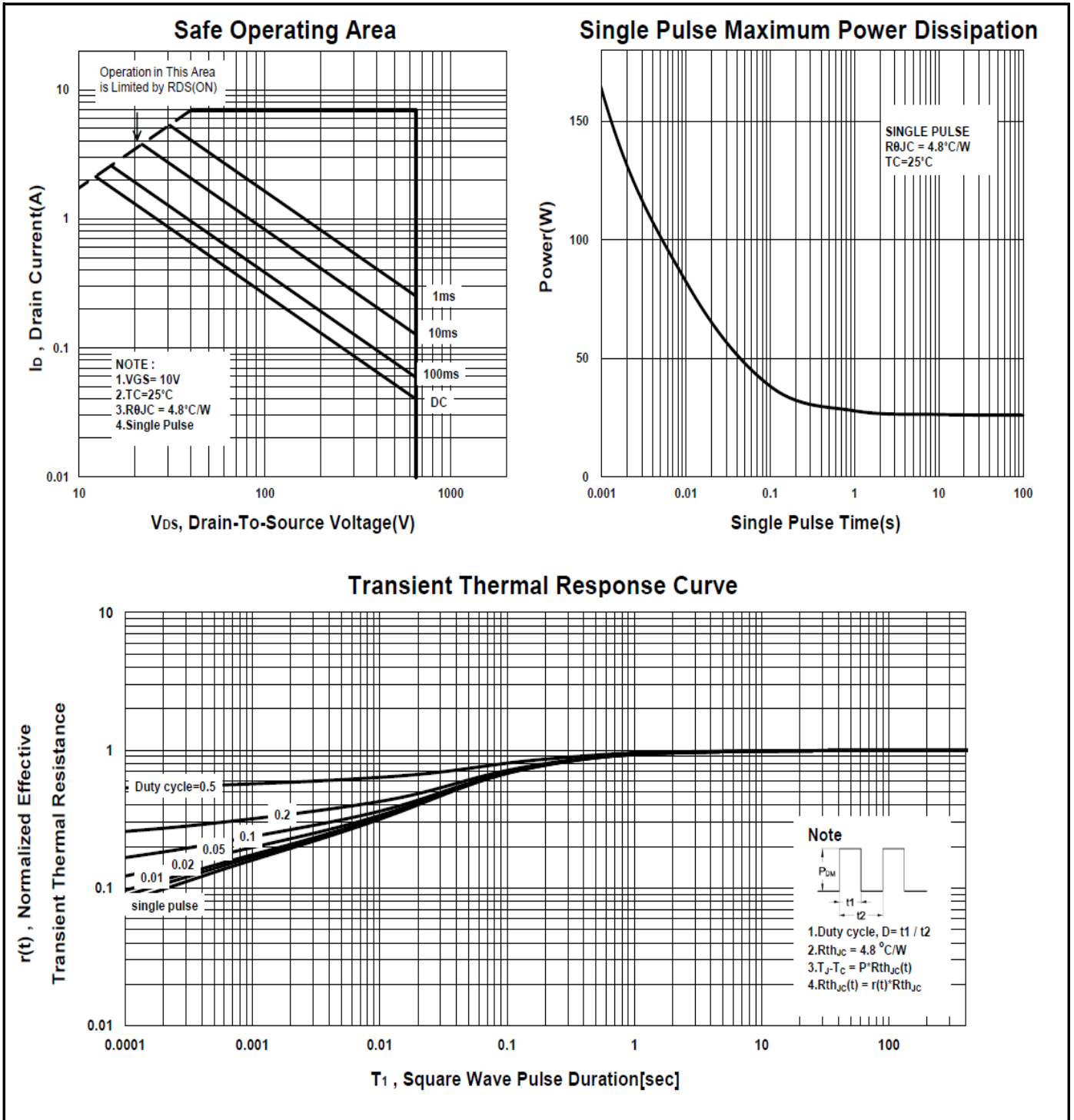
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Package Dimension

TO-252 (DPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	10	10.41	J	4.8		5.64
B	2.1	2.2	2.5	K	0.15		1.49
C	0.4	0.5	0.61	L	0.4	0.76	0.91
D	0.82	1.2	1.5	M	4.2	4.58	5
E	0.35	0.5	0.65	S	4.57	5.1	5.52
F	0		0.2	T	3.81	4.75	5.24
G	5.3	6.1	6.3	U	1.4		1.78
H	0.5		1.7	V	0.55	1.25	1.7
I	6.3	6.5	6.8				

