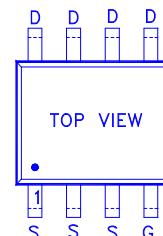
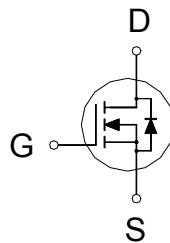


NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****P0502CV
SOP-8
Halogen-Free & Lead-Free****PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	5mΩ	18A



G: GATE
D: DRAIN
S: SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS		UNITS
Drain-Source Voltage		V_{DS}	20		V
Gate-Source Voltage		V_{GS}	± 10		V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	18		A
	$T_A = 100^\circ\text{C}$		11		
Pulsed Drain Current ¹		I_{DM}	70		
Power Dissipation	$T_A = 25^\circ\text{C}$	P_D	2.5		W
	$T_A = 100^\circ\text{C}$		1		
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	°C / W

¹Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.3	0.55	0.8	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 8V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20V, V_{GS} = 0V$			1	μA
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{DS(\text{ON})}$	$V_{GS} = 4.5V, I_D = 5A$		4.1	5	$\text{m}\Omega$
		$V_{GS} = 2.5V, I_D = 5A$		4.75	5.7	
		$V_{GS} = 1.8V, I_D = 5A$		6.7	8.2	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 18A$		100		S

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DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 20V, f = 1MHz$		3470		pF
Output Capacitance	C_{oss}			526		
Reverse Transfer Capacitance	C_{rss}			412		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		1.3		Ω
Total Gate Charge ²	Q_g	$V_{DS} = 0.5V_{(BR)DSS}, V_{GS} = 2.5V, I_D = 18A$		30		nC
Gate-Source Charge ²	Q_{gs}			15		
Gate-Drain Charge ²	Q_{gd}			5		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = 10V, R_L = 15\Omega$ $I_D \approx 1A, V_{GS} = 4.5V, R_{GEN} = 6\Omega$		20		nS
Rise Time ²	t_r			25		
Turn-Off Delay Time ²	$t_{d(off)}$			180		
Fall Time ²	t_f			85		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_A = 25^\circ C$)						
Continuous Current	I_S				18	A
Forward Voltage ¹	V_{SD}	$I_F = 5A, V_{GS} = 0V$			1.3	V

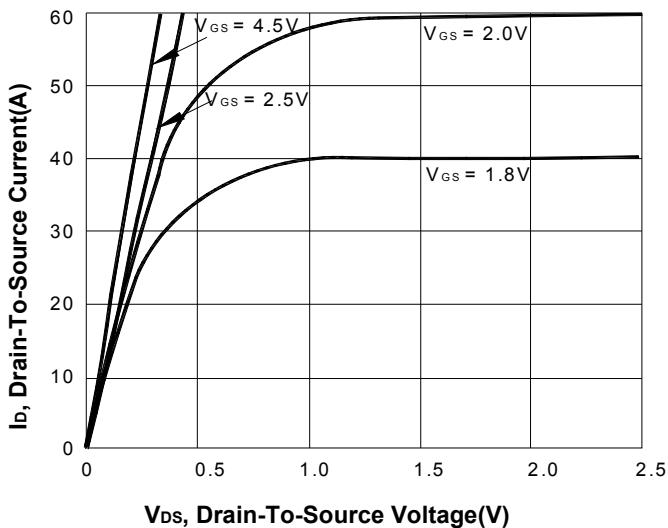
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

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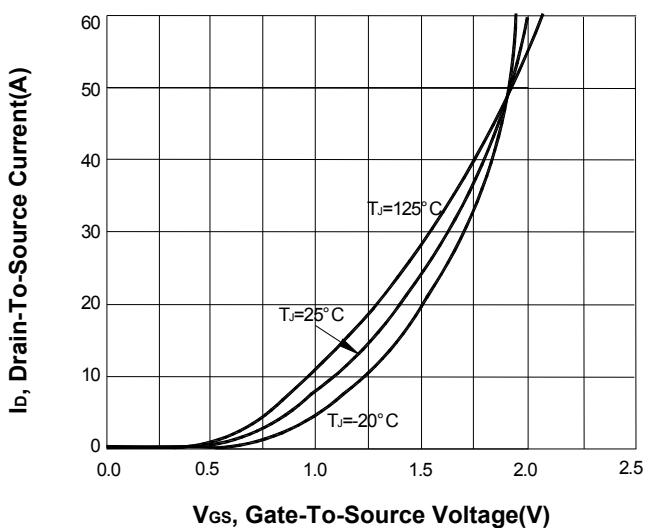
**N-Channel Enhancement Mode
Field Effect Transistor**

P0502CV
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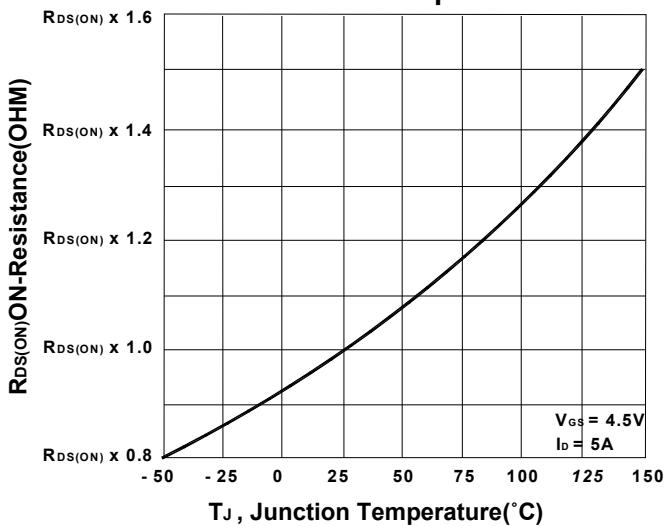
Output Characteristics



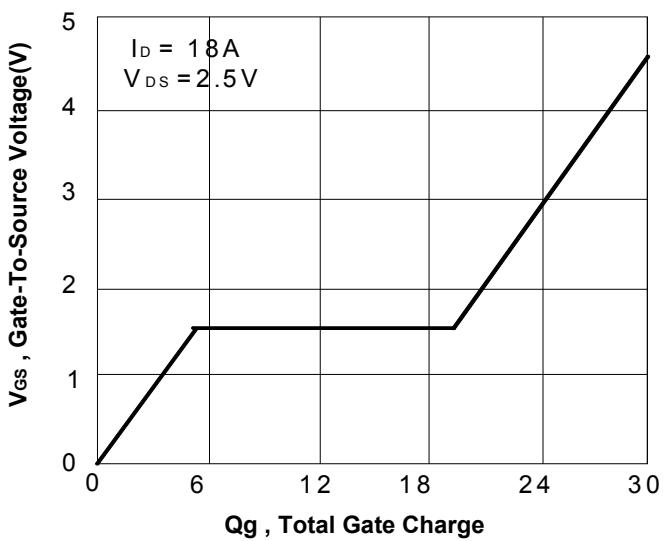
Transfer Characteristics



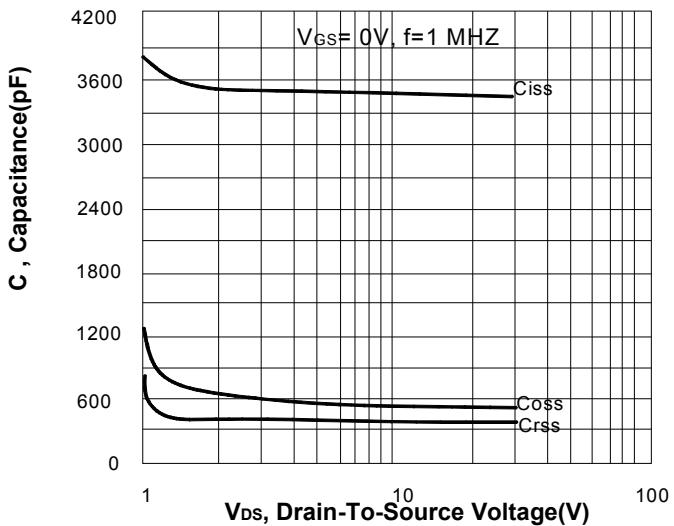
On-Resistance VS Temperature



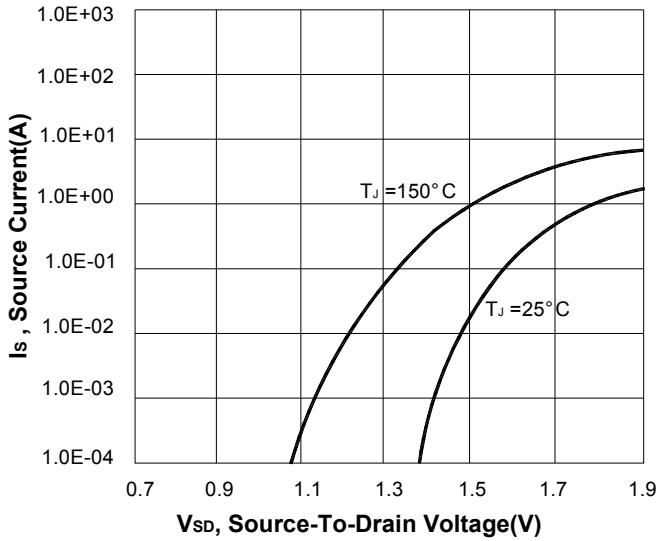
Gate charge Characteristics



Capacitance Characteristic



Source-Drain Diode Forward Voltage

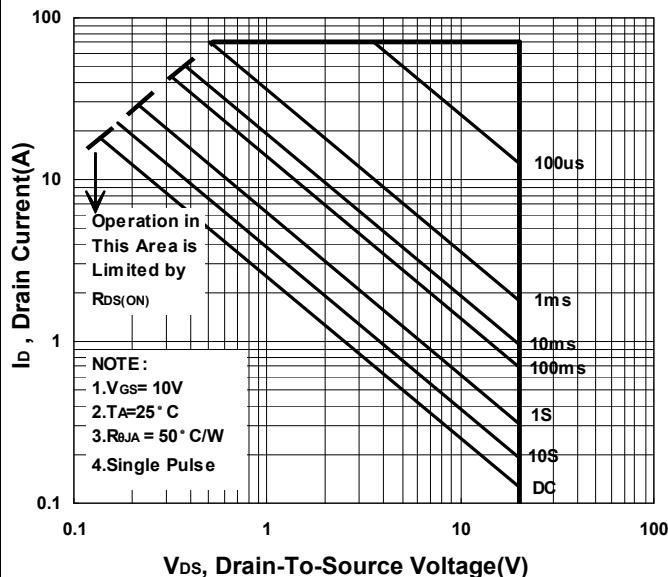


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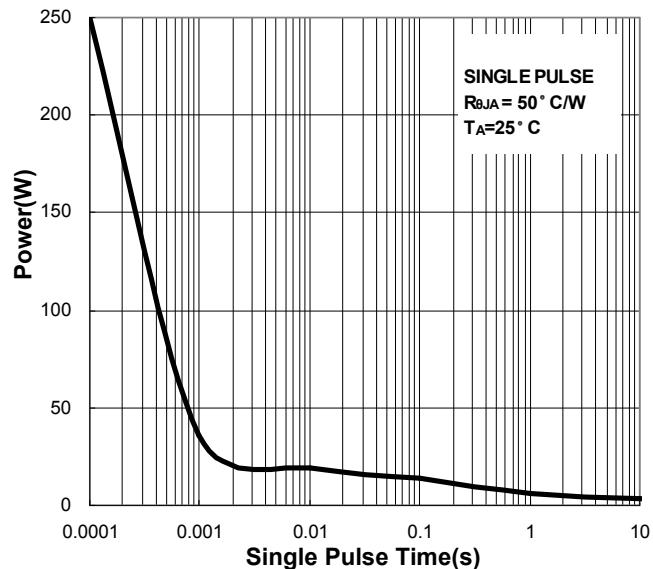
**N-Channel Enhancement Mode
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

