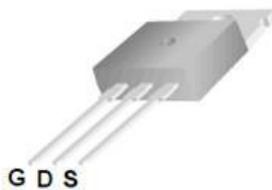


# P0550AT

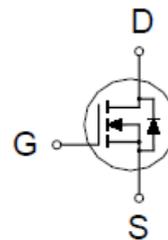
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
500V	$1.5\Omega @ V_{GS} = 10V$	5A



TO-220



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	500	V
Gate-Source Voltage		$V_{GS}$	$\pm 30$	
Continuous Drain Current <sup>2</sup>	$T_C = 25\text{ }^\circ\text{C}$	$I_D$	5	A
	$T_C = 100\text{ }^\circ\text{C}$		3	
Pulsed Drain Current <sup>1, 2</sup>		$I_{DM}$	15	
Avalanche Current <sup>3</sup>		$I_{AS}$	5	
Avalanche Energy <sup>3</sup>	$L = 10\text{mH}$	$E_{AS}$	128	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	$P_D$	74	W
	$T_C = 100\text{ }^\circ\text{C}$		30	
Operating Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.68	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Limited only by maximum temperature allowed

<sup>3</sup> $V_{DD} = 50V$ , starting  $T_J = 25\text{ }^\circ\text{C}$

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## N-Channel Enhancement Mode MOSFET

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	500			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5		4.5	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 30V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 500V, V_{GS} = 0V, T_C = 25^\circ C$			25	$\mu A$
		$V_{DS} = 500V, V_{GS} = 0V, T_C = 100^\circ C$			250	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 2.5A$		1.35	1.5	$\Omega$
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 10V, I_D = 2.5A$		4		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		691		pF
Output Capacitance	$C_{oss}$			93		
Reverse Transfer Capacitance	$C_{rss}$			12		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DD} = 250V, I_D = 2.5A, V_{GS} = 10V$		12.1		nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			3.7		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			3.6		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DD} = 250V, I_D = 2.5A, R_G = 25\Omega$		13		nS
Rise Time <sup>2</sup>	$t_r$			22		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			28		
Fall Time <sup>2</sup>	$t_f$			20		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>						
Continuous Current <sup>3</sup>	$I_S$				5	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 5A, V_{GS} = 0V$			1.5	V
Reverse Recovery Time	$t_{rr}$	$I_F = 5A, di_F/dt = 100A/\mu S$ $V_{GS} = 0V$		1450		nS
Reverse Recovery Charge	$Q_{rr}$				10	nC

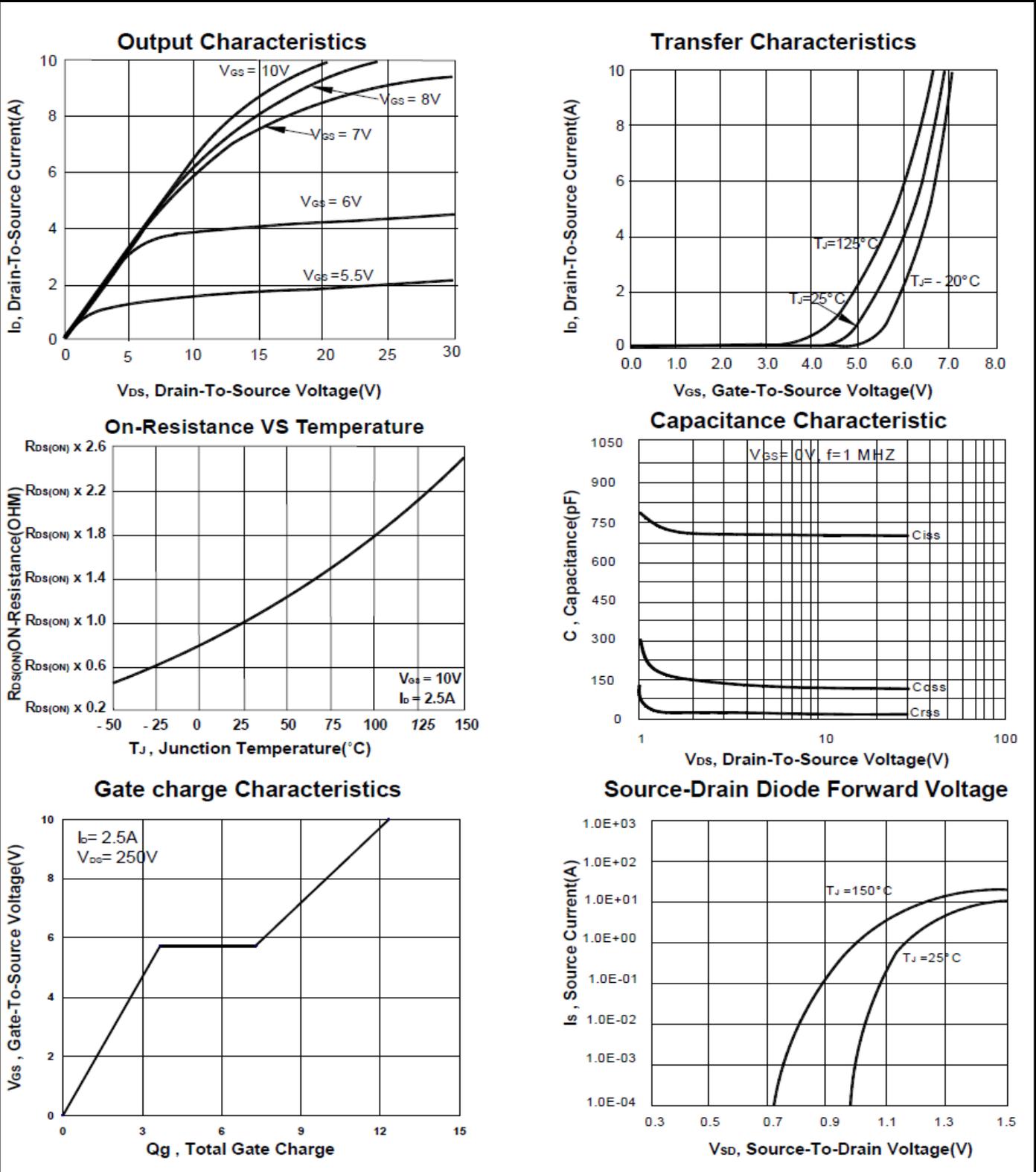
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

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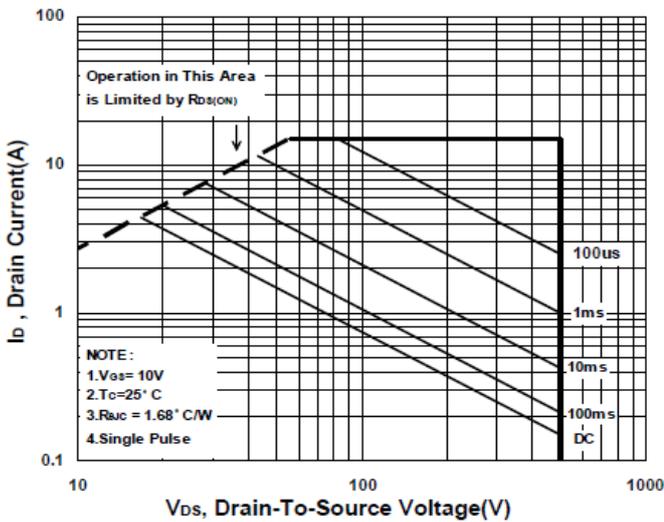
## N-Channel Enhancement Mode MOSFET



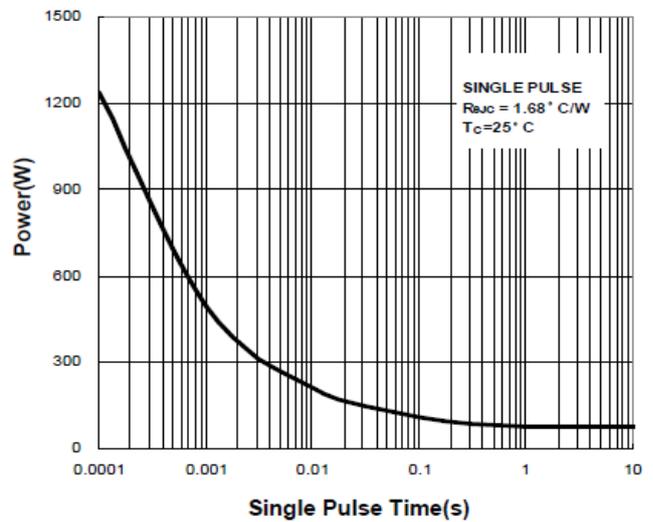
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## N-Channel Enhancement Mode MOSFET

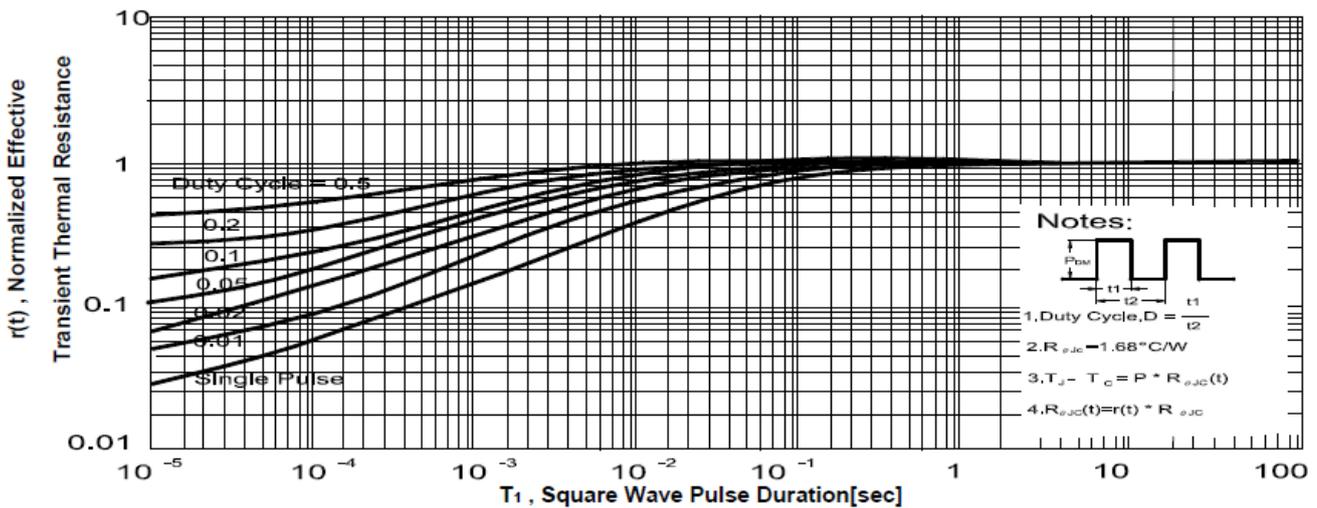
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**



# P0550AT

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

