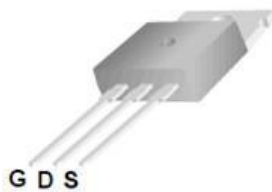


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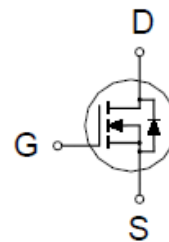
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
75V	5.8m Ω @ $V_{GS} = 10V$	129A



TO-220



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ¹	$T_C = 25\text{ }^\circ\text{C}$	I_D	129	A
	$T_C = 100\text{ }^\circ\text{C}$		82	
Pulsed Drain Current ²		I_{DM}	410	
Avalanche Current		I_{AS}	60	
Avalanche Energy	$L = 0.3\text{mH}$	E_{AS}	557	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	P_D	192	W
	$T_C = 100\text{ }^\circ\text{C}$		77	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$
Lead Temperature ($1/16$ " from case for 10 sec.)		T_L	275	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		0.65	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		63	

¹Limited by package.

²Pulse width limited by maximum junction temperature.

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N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	75			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.0	2.5	4.0	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V			1	μA
		V _{DS} = 60V, V _{GS} = 0V, T _J = 125 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	129			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 100A		4.7	5.8	mΩ
Forward Transconductance ¹	g _{fs}	V _{DS} = 25V, I _D = 50A			100	S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		12500		pF
Output Capacitance	C _{oss}			1090		
Reverse Transfer Capacitance	C _{rss}			629		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		3.8	4	Ω
Total Gate Charge ²	Q _g	V _{DS} = 60V, V _{GS} = 10V, I _D = 80A		223		nC
Gate-Source Charge ²	Q _{gs}			64		
Gate-Drain Charge ²	Q _{gd}			80		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 40V, I _D ≅ 40A, V _{GS} = 10V, R _{GS} = 2.5Ω		40		nS
Rise Time ²	t _r			60		
Turn-Off Delay Time ²	t _{d(off)}			130		
Fall Time ²	t _f			50		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current ³	I _S				129	A
Forward Voltage ¹	V _{SD}	I _F = 100A, V _{GS} = 0V			1.3	V
Reverse Recovery Time	t _{rr}			90		nS
Reverse Recovery Charge	Q _{rr}			260		nC

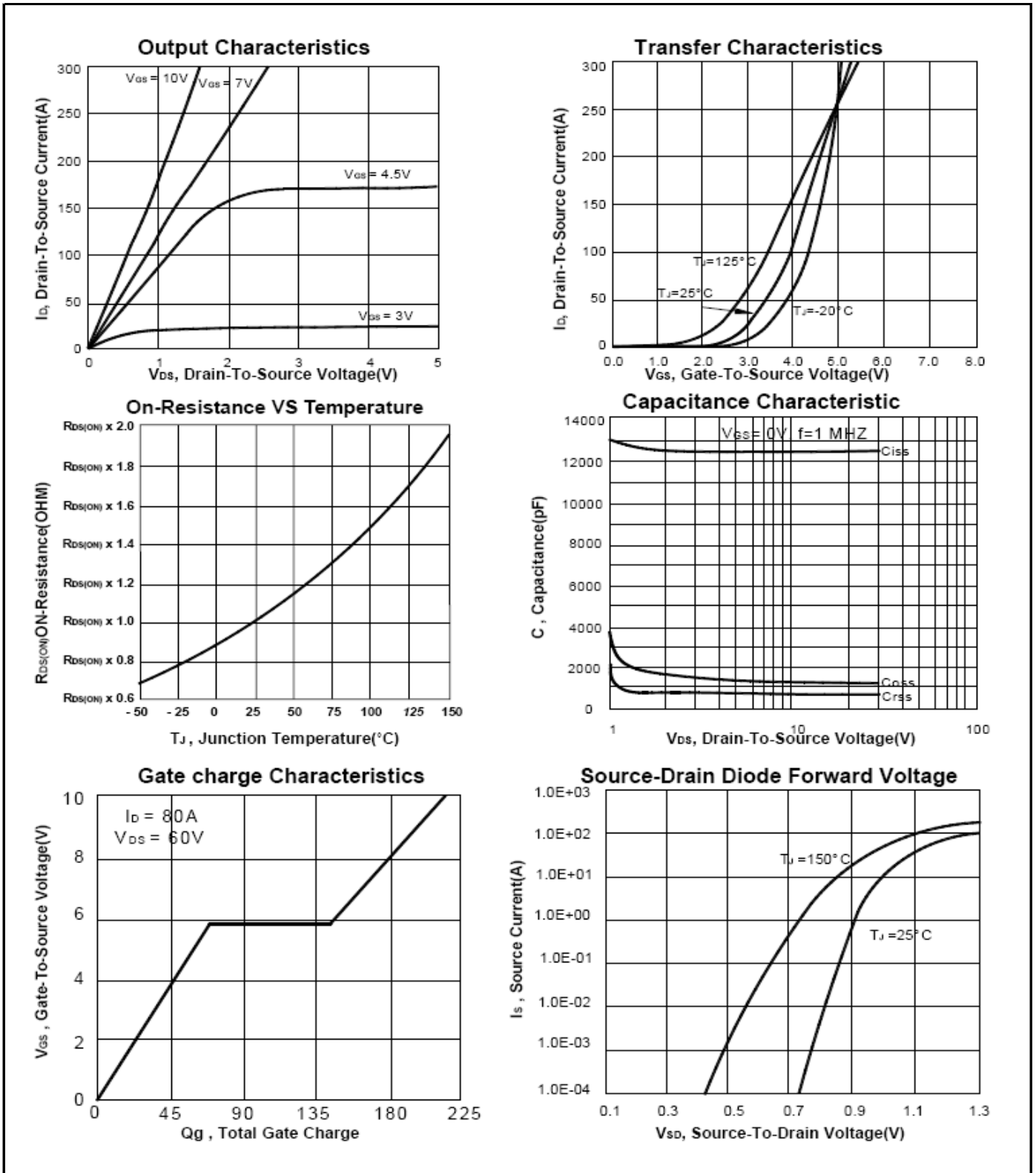
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

³Limited by package.

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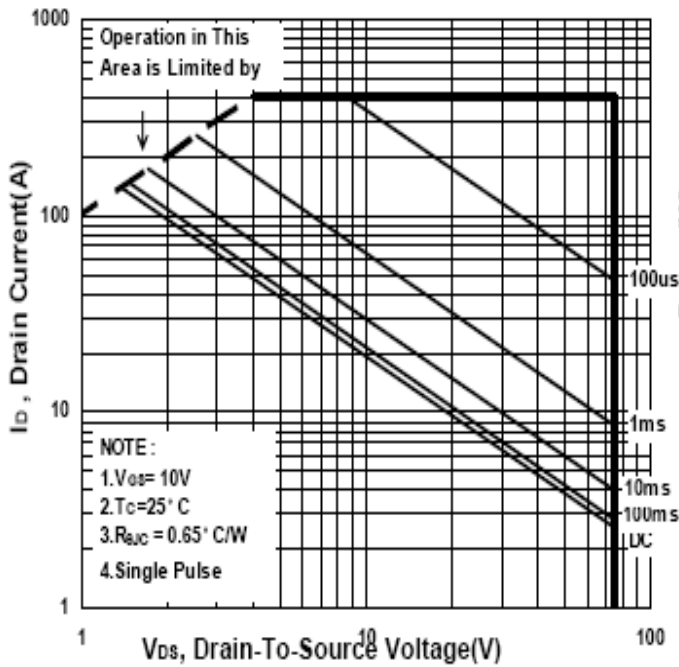
N-Channel Enhancement Mode MOSFET



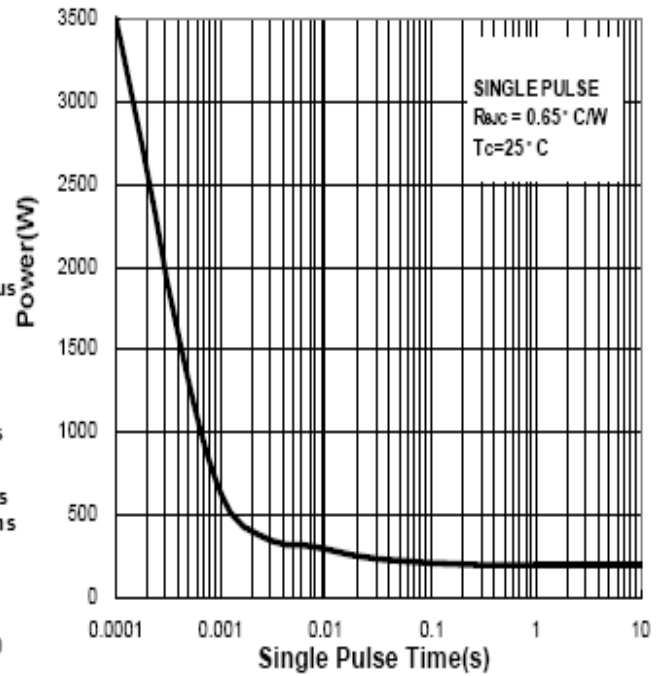
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N-Channel Enhancement Mode MOSFET

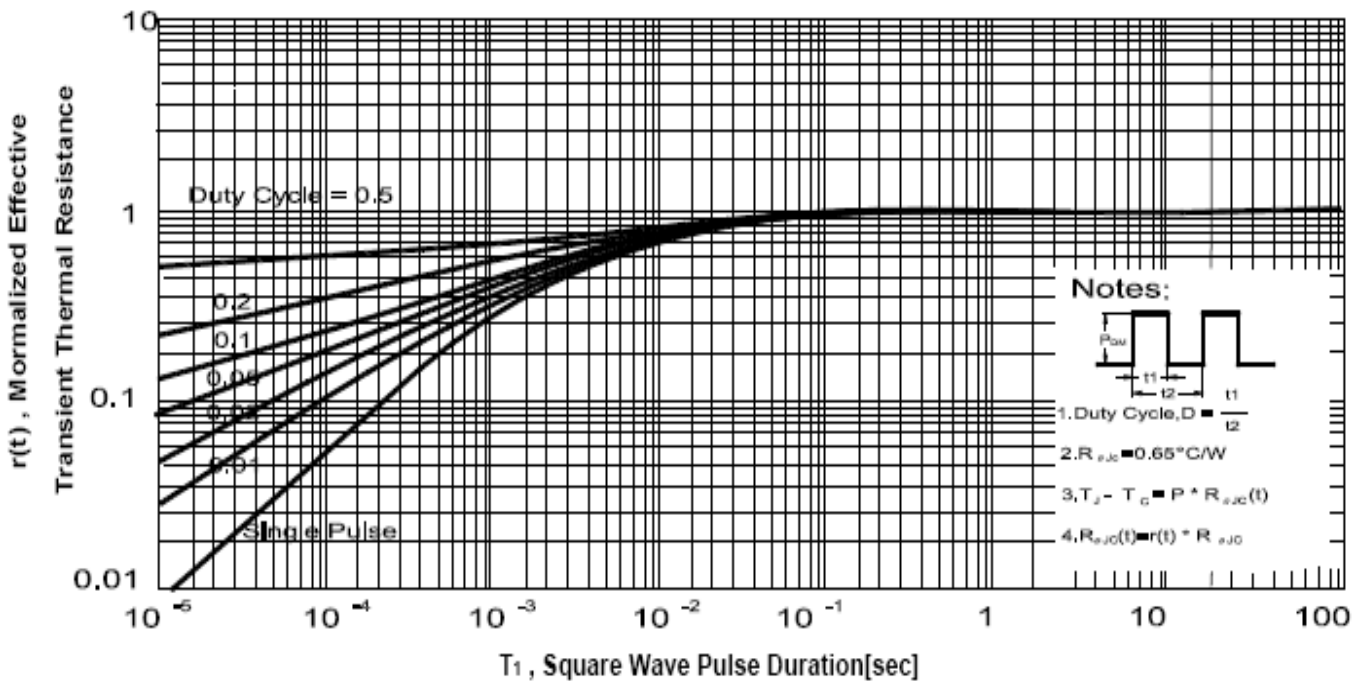
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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Package Dimension

TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

