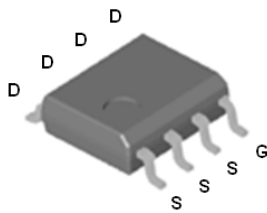


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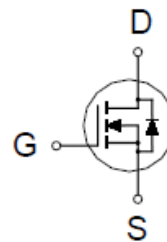
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	$6m\Omega @ V_{GS} = 10V$	16A



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ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_A = 25\text{ }^\circ\text{C}$	I_D	16	A
	$T_A = 70\text{ }^\circ\text{C}$		13	
Pulsed Drain Current ¹		I_{DM}	70	
Avalanche Current		I_{AS}	48	
Avalanche Energy	$L = 0.1mH$	E_{AS}	113	mJ
Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	P_D	2.5	W
	$T_A = 70\text{ }^\circ\text{C}$		1.6	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	$^\circ\text{C} / W$

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS (T_A = 25 °C, Unless Otherwise Noted)

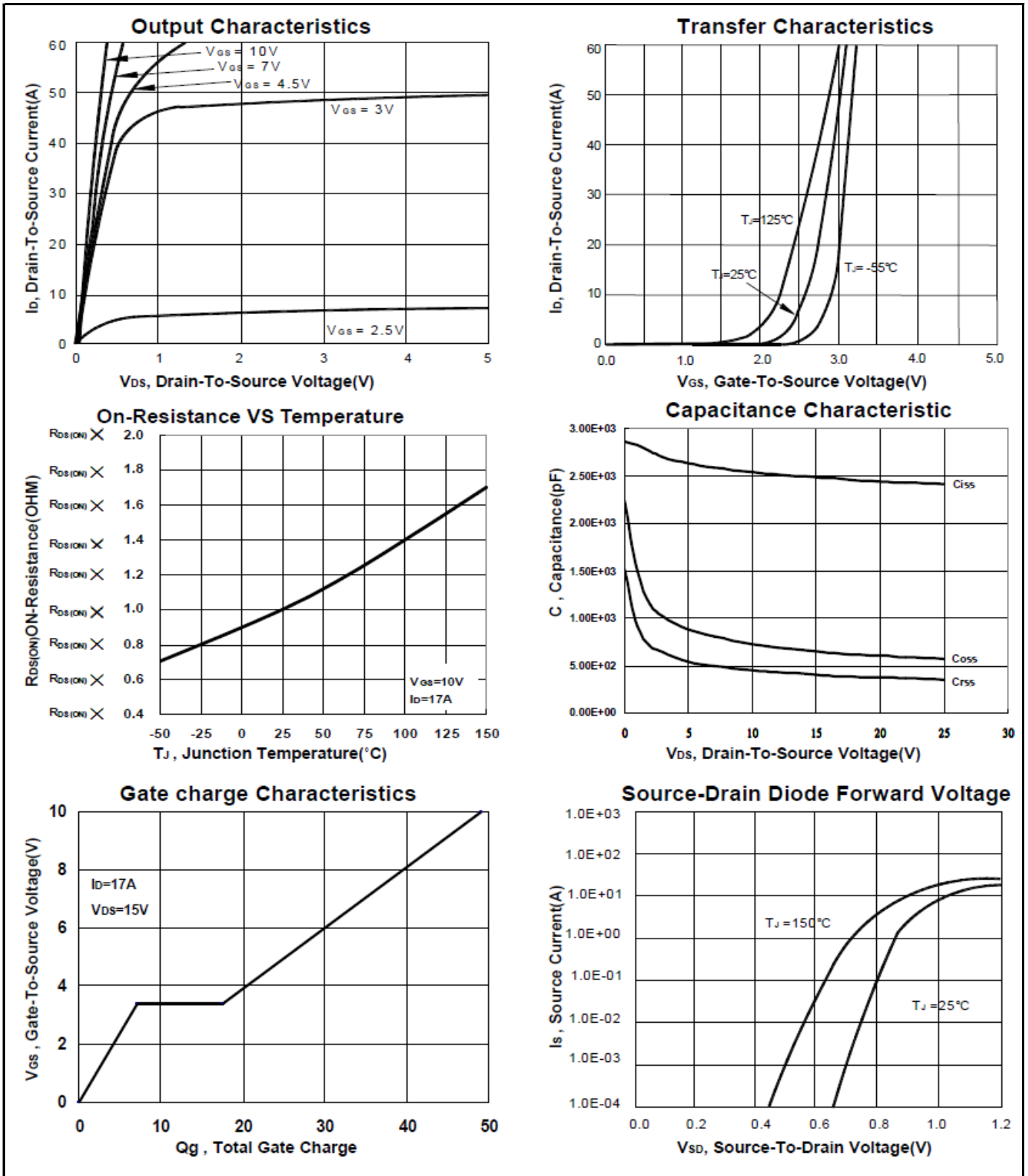
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			MIN	TYP	MAX		
STATIC							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.6	3.0		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	μA	
		V _{DS} = 20V, V _{GS} = 0V, T _J = 55 °C			10		
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 14A		6.2	12	mΩ	
		V _{GS} = 10V, I _D = 17A		4.6	6		
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 17A		80		S	
DYNAMIC							
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		2410		pF	
Output Capacitance	C _{oss}			567			
Reverse Transfer Capacitance	C _{rss}			351			
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.3		Ω	
Total Gate Charge ²	Q _g	V _{GS} =10V	V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = 10V, I _D = 17A	50		nC	
		V _{GS} =4.5V		25			
Gate-Source Charge ²	Q _{gs}	7					
Gate-Drain Charge ²	Q _{gd}	10					
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 15V, R _L = 15Ω I _D ≅ 1A, V _{GS} = 10V, R _{GEN} = 6Ω		10			nS
Rise Time ²	t _r			9			
Turn-Off Delay Time ²	t _{d(off)}		45				
Fall Time ²	t _f		10				
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_A = 25 °C)							
Forward Voltage ¹	V _{SD}	I _F = 17A, V _{GS} = 0V			1.2	V	
Diode Reverse Recovery Time	t _{rr}	I _F = 17A, di/dt=300A/μs			43	nS	
Diode Reverse Recovery Charge	Q _{rr}					32	nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

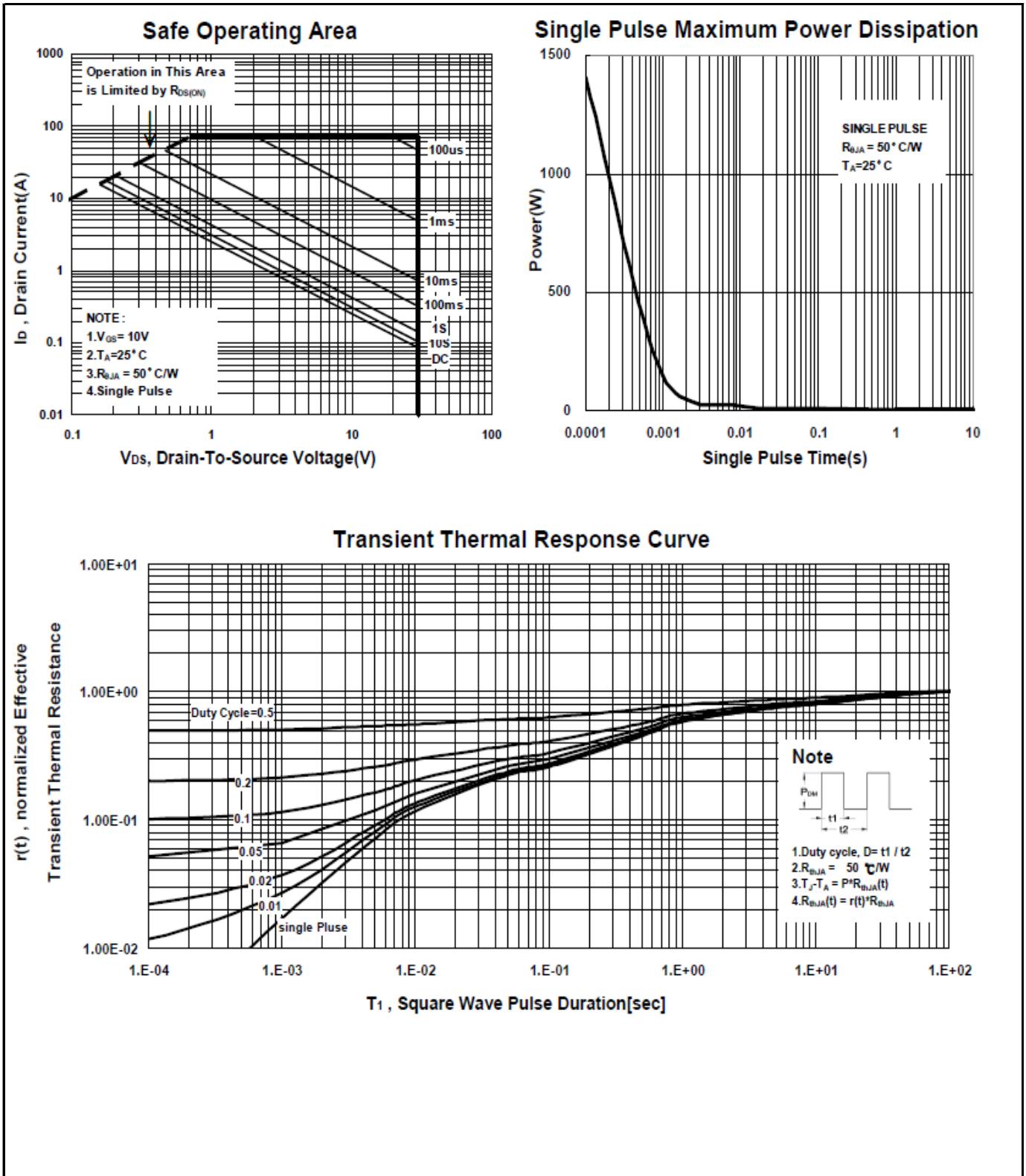
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Package Dimension

SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

