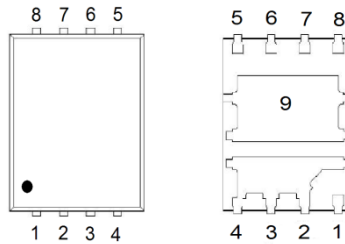




**PRODUCT SUMMARY**

	$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D^3$
Q2	30V	5.8mΩ	61A
Q1	30V	9mΩ	43A



1 : G1  
2,3,4 : D1  
5,6,7 : S2  
8 : G2  
9 : S1/D2

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ °C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	Q2	Q1	UNITS
Drain-Source Voltage		$V_{DS}$	30	30	V
Gate-Source Voltage		$V_{GS}$	±20	±20	V
Continuous Drain Current <sup>3</sup>	$T_C = 25\text{ °C}$	$I_D$	61	43	A
	$T_C = 100\text{ °C}$		38	21	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	130	120	
Continuous Drain Current	$T_A = 25\text{ °C}$	$I_D$	15	11	
	$T_A = 70\text{ °C}$		12	9	
Avalanche Current		$I_{AS}$	36	25	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	64	31	mJ
Power Dissipation	$T_C = 25\text{ °C}$	$P_D$	35	27	W
	$T_C = 100\text{ °C}$		14	10	
Power Dissipation	$T_A = 25\text{ °C}$	$P_D$	2.3	1.9	W
	$T_A = 70\text{ °C}$		1.5	1.2	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150		°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL		TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$R_{\theta JA}$	Q2		53	°C / W
	$R_{\theta JA}$	Q1		65	
Junction-to-Case	$R_{\theta JC}$	Q2		3.5	
	$R_{\theta JC}$	Q1		4.6	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25\text{ °C}$ . The value in any given application depends on the user's specific board design.

<sup>3</sup>Package limitation current :Q1=27A,Q2=31A

**ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			MIN	TYP	MAX		
<b>STATIC</b>							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	Q2	30		V	
			Q1	30			
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	Q2	1	1.6	3	V
			Q1	1	1.7	3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	Q2			±100	nA
			Q1			±100	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V	Q2			1	μA
			Q1			1	
		V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55 °C	Q2			10	
			Q1			10	
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 12A	Q2		5.7	7	mΩ
			Q1		9.7	14	
			Q2		4.4	5.8	
			Q1		7.5	9	
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>GS</sub> = 5V, I <sub>D</sub> = 15A	Q2		57		S
			Q1		55		
<b>DYNAMIC</b>							
Input Capacitance	C <sub>iss</sub>		Q2		1740		pF
			Q1		877		
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz	Q2		223		pF
			Q1		128		
Reverse Transfer Capacitance	C <sub>rss</sub>		Q2		199		pF
			Q1		115		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz	Q2		1		Ω
			Q1		2.1		
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	Q2 V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A	Q2		40.3		nC
			Q1		23		
			Q2		21.1		
			Q1		12.4		
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>	Q1 V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =11A	Q2		6		nC
			Q1		3.2		
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>		Q2		9.8		nC
			Q1		6.5		

Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	Q2 $V_{DS} = 15V,$ $I_D=15A, V_{GS} = 10V, R_{GEN} =6\Omega$ Q1 $V_{DS} = 15V,$ $I_D=11A, V_{GS} = 10V, R_{GEN} =6\Omega$	Q2		23		nS		
Rise Time <sup>2</sup>	$t_r$		Q1		20				
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		Q2		18				
			Q1		12				
Fall Time <sup>2</sup>	$t_f$		Q2		56				
			Q1		41				
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>									
Continuous Current <sup>3</sup>	$I_S$			Q2				61	A
		Q1				43			
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 15A, V_{GS} = 0V$	Q2		0.83	1.3	V		
		$I_F = 11A, V_{GS} = 0V$	Q1		0.87	1.3			
Reverse Recovery Time	$t_{rr}$	Q2 $I_F = 15A, di_F/dt = 100A / \mu S$	Q2		15		nS		
			Q1		11.7				
Reverse Recovery Charge	$Q_{rr}$	Q1 $I_F = 11A, di_F/dt = 100A / \mu S$	Q2		5.1		nC		
			Q1		3				

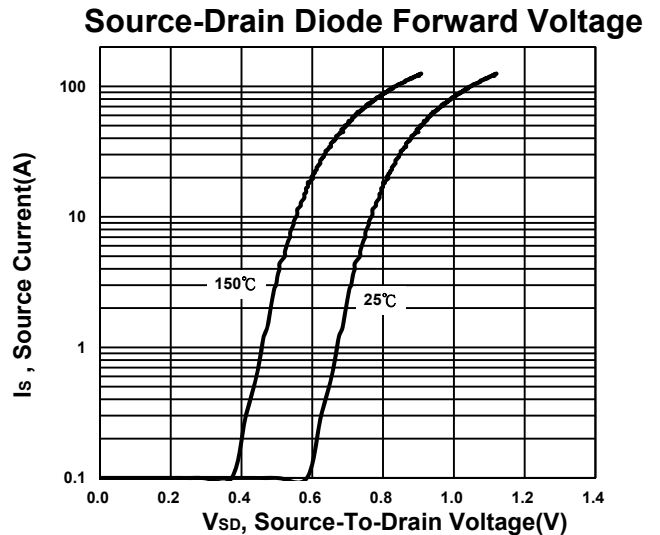
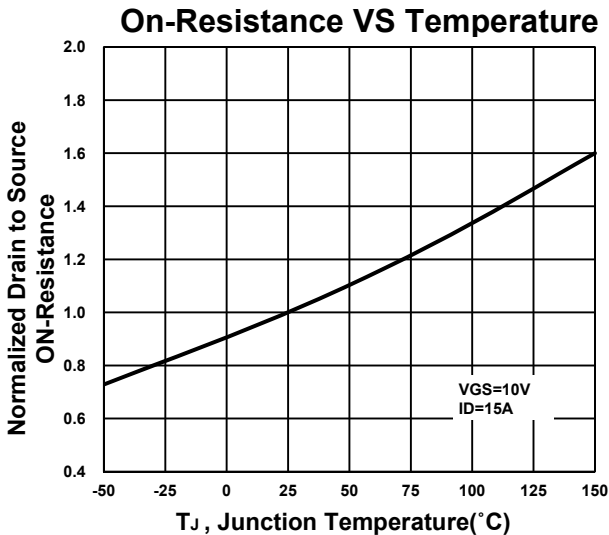
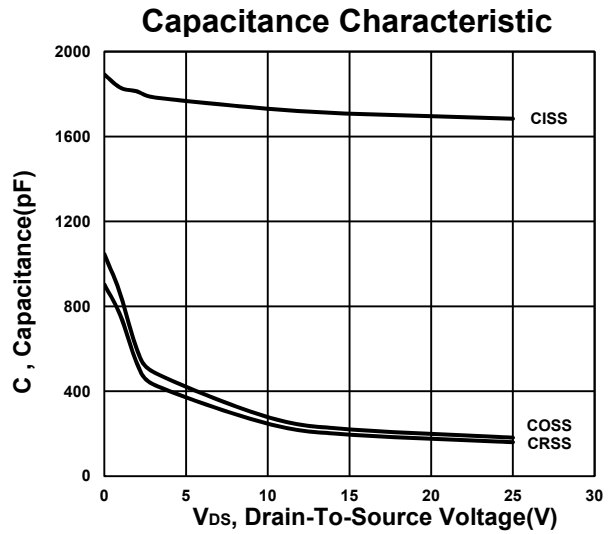
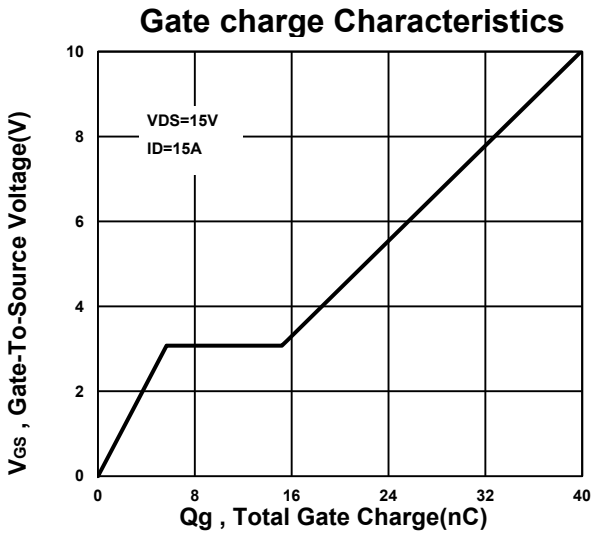
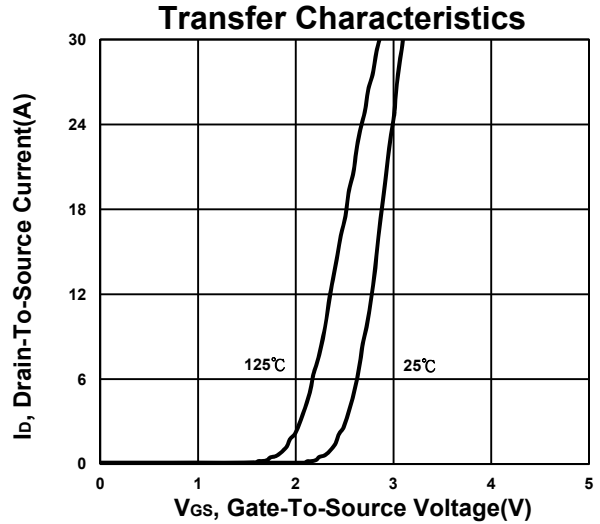
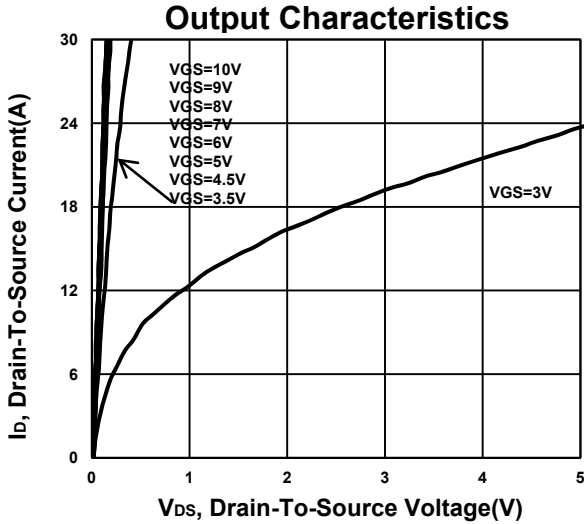
<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

<sup>2</sup>Independent of operating temperature.

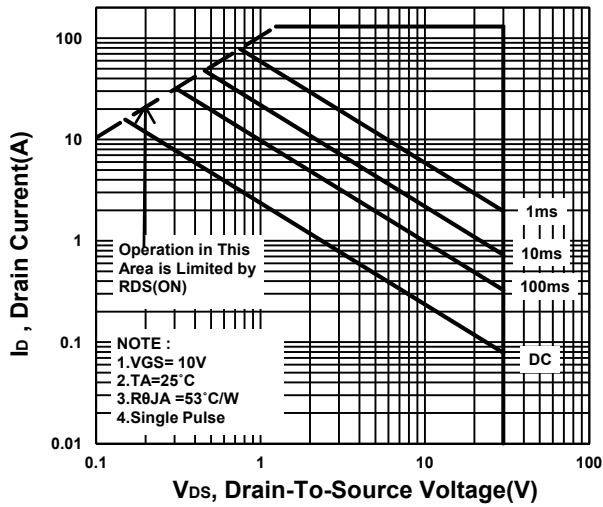
<sup>3</sup>Package limitation current :Q1=27A,Q2=31A

**TYPICAL PERFORMANCE CHARACTERISTICS**

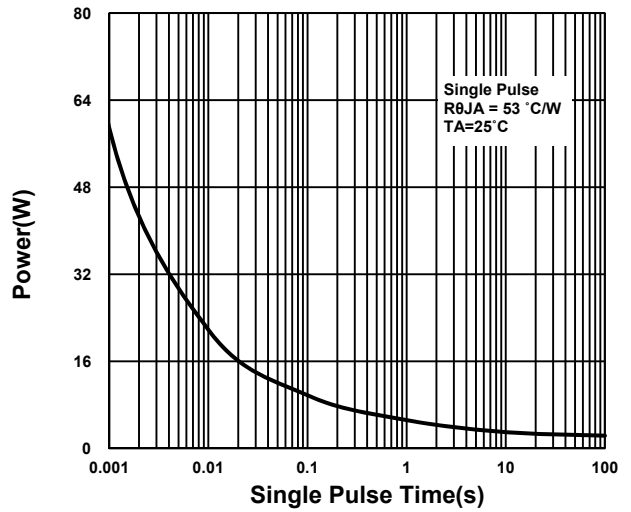
**Q2**



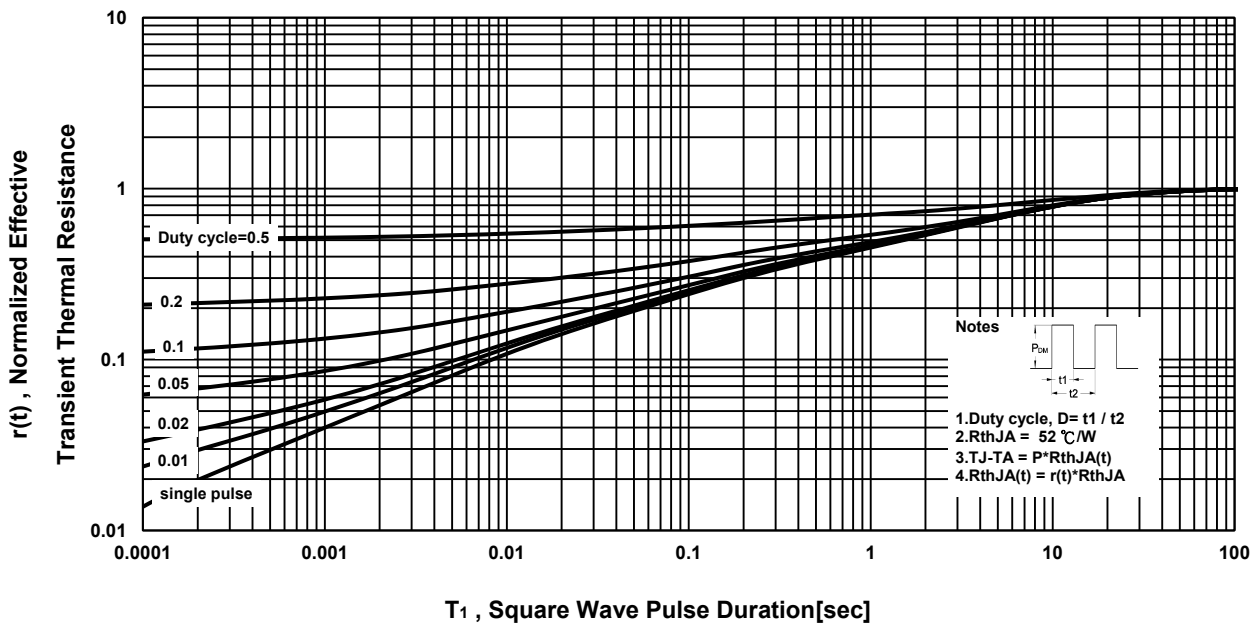
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



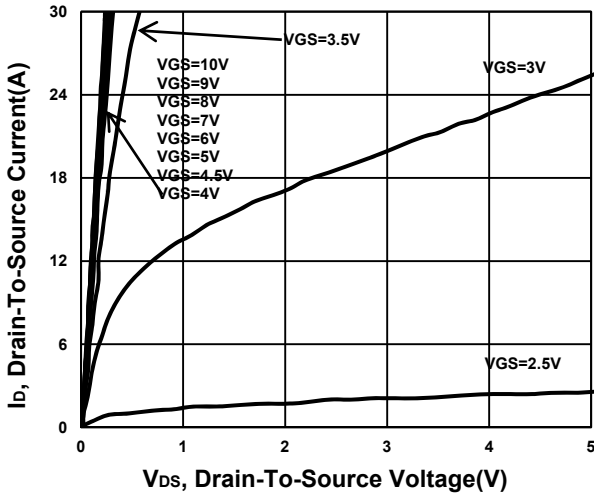
**Transient Thermal Response Curve**



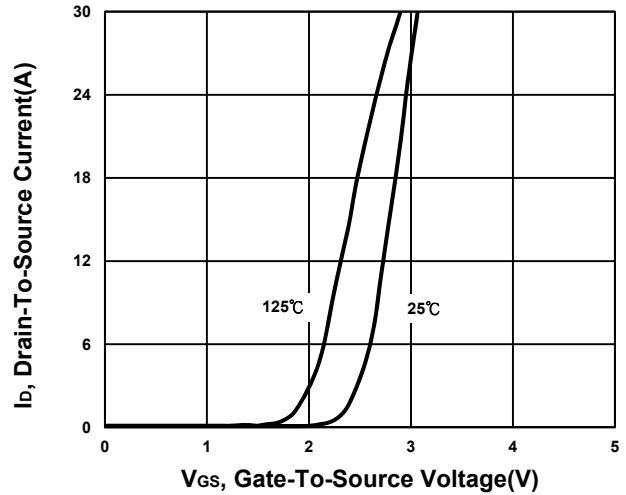
**TYPICAL PERFORMANCE CHARACTERISTICS**

**Q1**

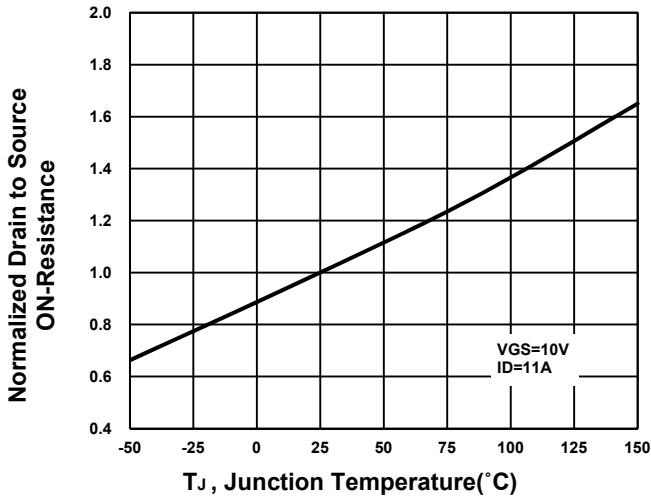
**Output Characteristics**



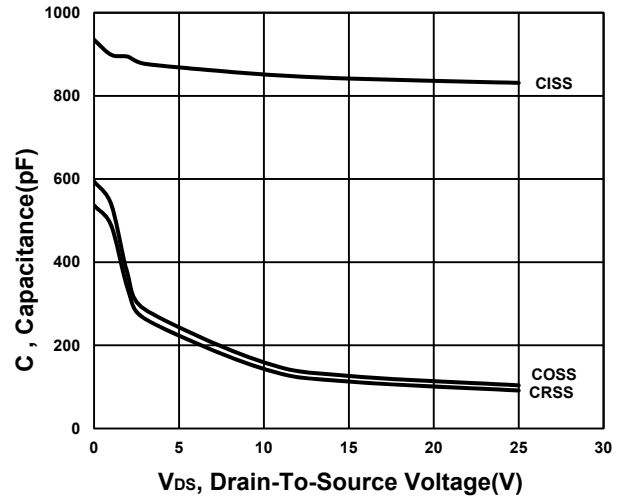
**Transfer Characteristics**



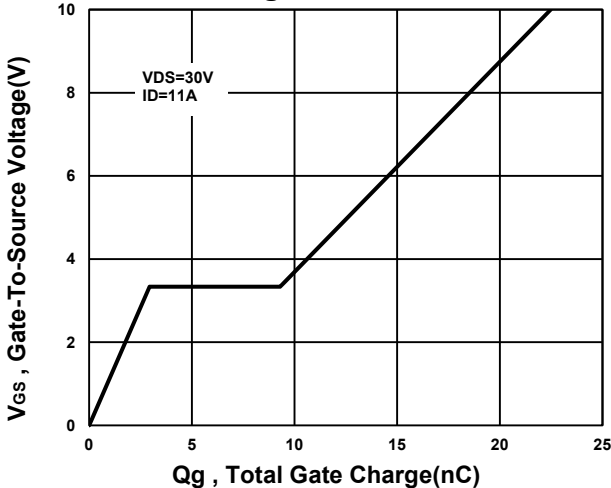
**On-Resistance VS Temperature**



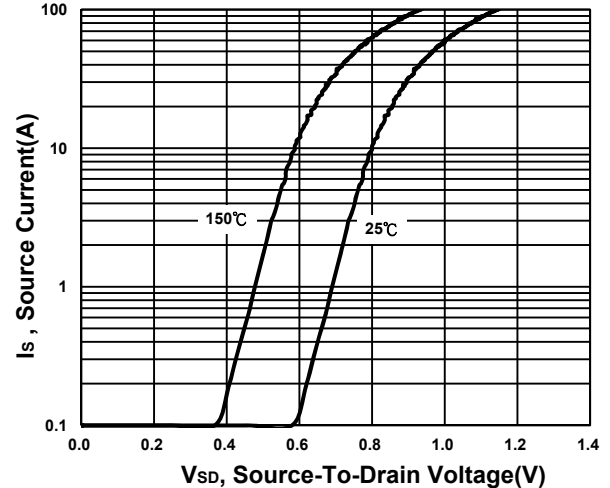
**Capacitance Characteristic**



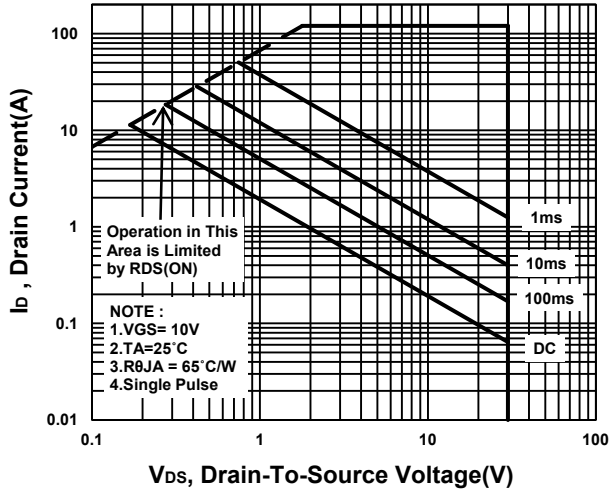
**Gate charge Characteristics**



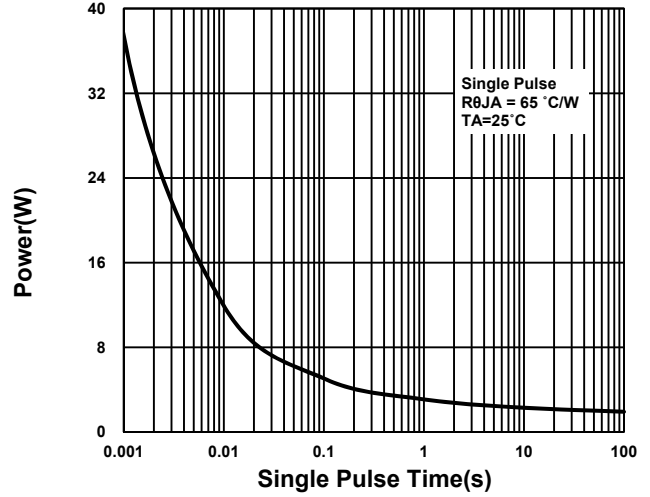
**Source-Drain Diode Forward Voltage**



**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**

