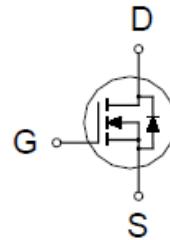
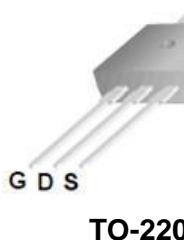


# P0903BT

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	9.7mΩ @ $V_{GS} = 10V$	60A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>2</sup>	$I_D$	60	A
$T_C = 100^\circ C$		38	
Pulsed Drain Current <sup>1, 2</sup>	$I_{DM}$	240	
Avalanche Current <sup>3</sup>	$I_{AS}$	34	
Avalanche Energy <sup>3</sup>	$E_{AS}$	58	mJ
Power Dissipation	$P_D$	56	W
$T_C = 100^\circ C$		22	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.2	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Limited only by maximum temperature allowe.

# P0903BT

## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.0	1.9	3.0	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}, T_C = 25^\circ\text{C}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_C = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 20\text{A}$		14	16	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		8.0	9.7	
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 20\text{V}, I_D = 20\text{A}$		37		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$		1150		pF
Output Capacitance	$C_{\text{oss}}$			304		
Reverse Transfer Capacitance	$C_{\text{rss}}$			175		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DD}} = 15\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		25		nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			5		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			8		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 15\text{V}, I_D = 20\text{A}, R_G = 6\Omega$		25		nS
Rise Time <sup>2</sup>	$t_r$			23		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			35		
Fall Time <sup>2</sup>	$t_f$			44		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current <sup>3</sup>	$I_S$				60	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			1.3	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		23		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			12		nC

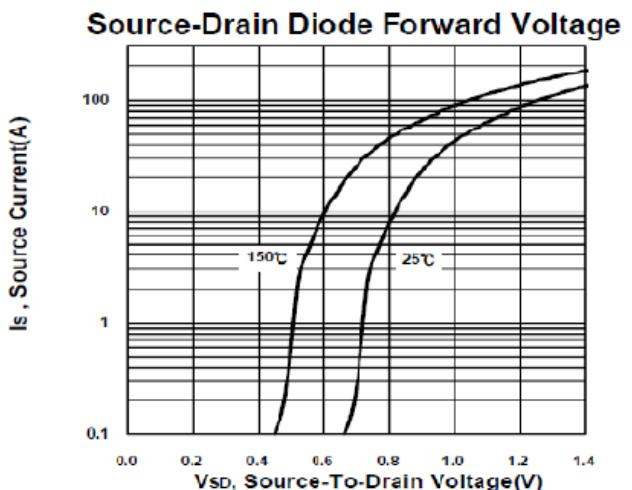
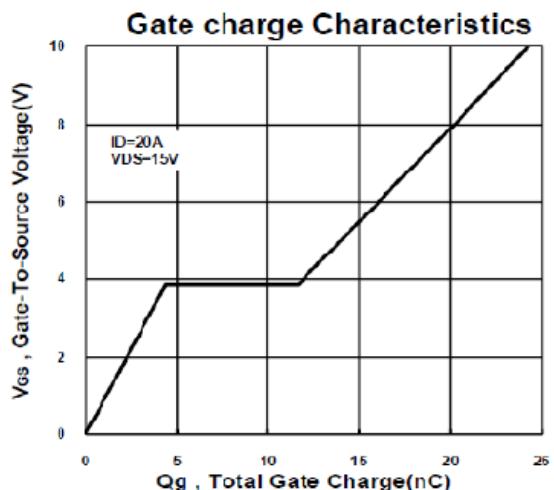
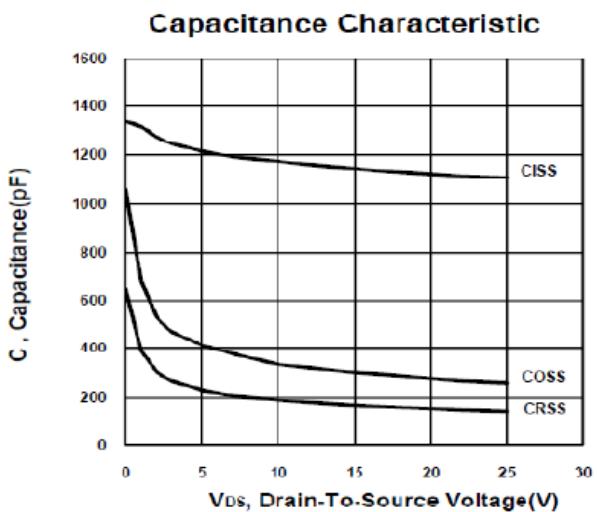
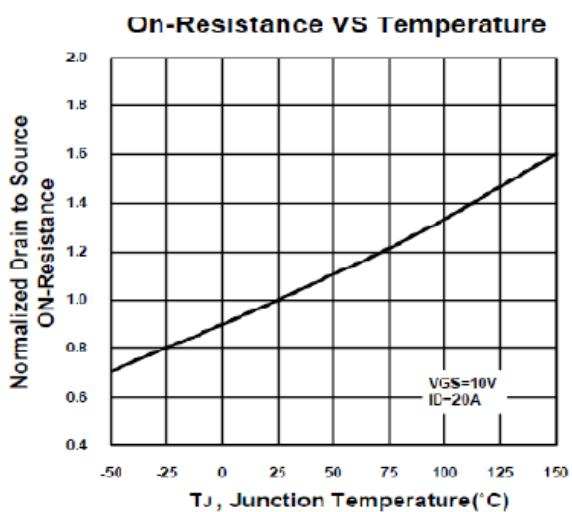
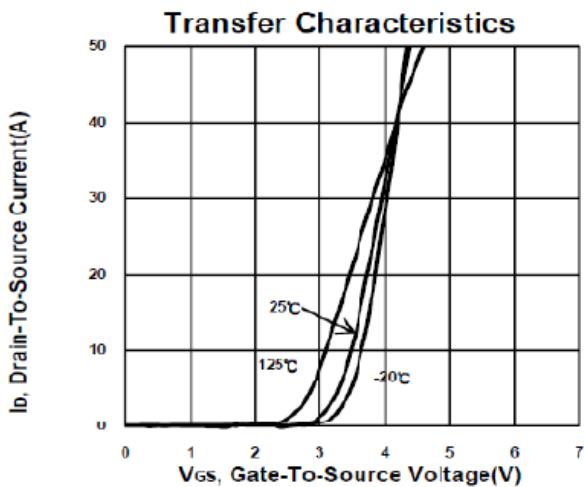
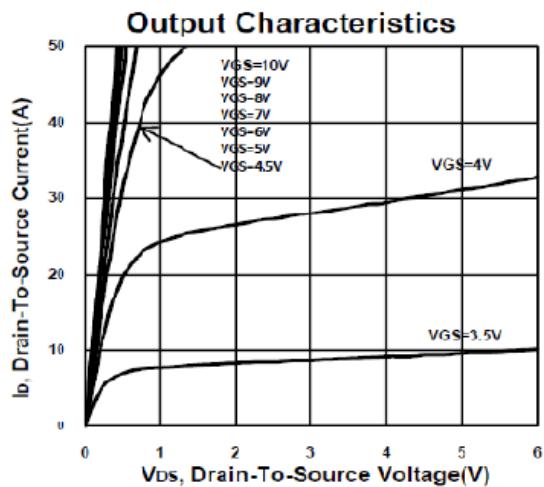
<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

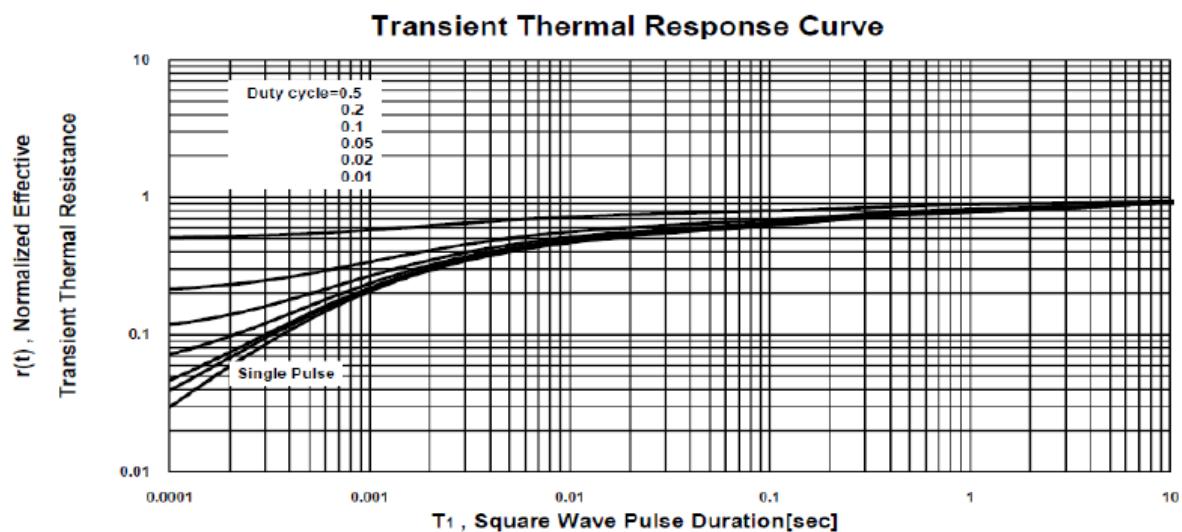
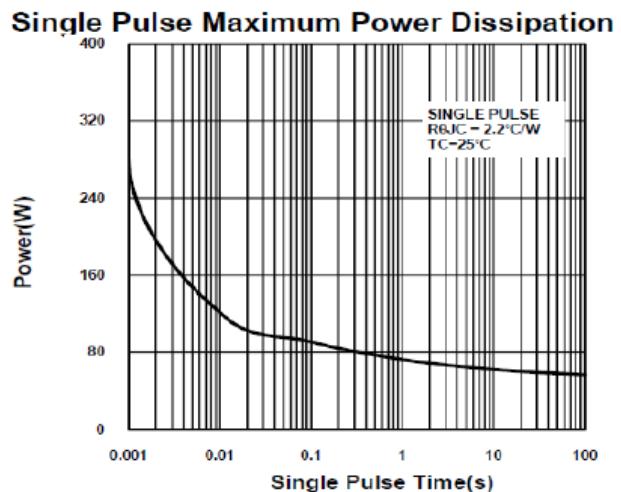
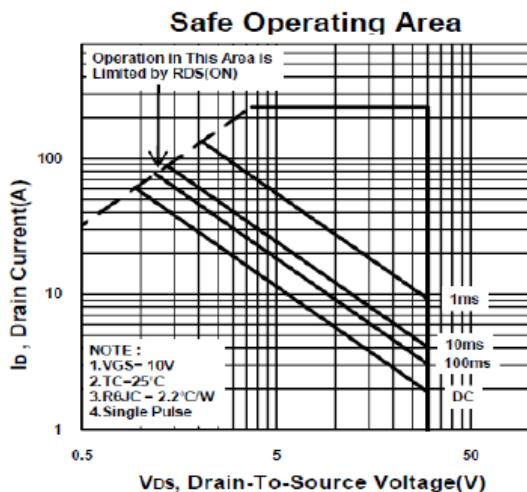
## P0903BT

### N-Channel Enhancement Mode MOSFET



## P0903BT

### N-Channel Enhancement Mode MOSFET



# P0903BT

## N-Channel Enhancement Mode MOSFET

### Package Dimension

#### TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

