NIKO-SEM

N-Channel Enhancement Mode Field Effect Transistor

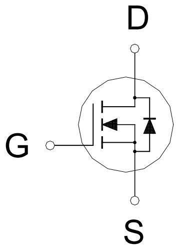
P0908AK

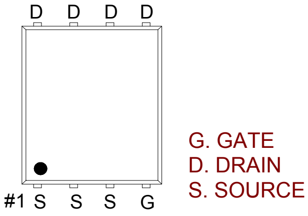
PDFN 5x6P

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
80V	9mΩ	50A





ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current[3]	T_C = 25 °C	I_D	50	A
	T_C = 100 °C		31	
Pulsed Drain Current[1]		I_{DM}	90	
Continuous Drain Current	T_A = 25 °C	I_D	10	
	T_A = 70 °C		8.7	
Avalanche Current		I_{AS}	49	
Avalanche Energy	L = 0.1mH	E_{AS}	120	mJ
Power Dissipation	T_C = 25 °C	P_D	50	W
	T_C = 100 °C		20	
Power Dissipation	T_A = 25 °C	P_D	2.3	W
	T_A = 70 °C		1.5	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient[2]	$R_{\theta JA}$		53	°C / W
Junction-to-Case	$R_{\theta JC}$		2.5	

[1] Pulse width limited by maximum junction temperature.

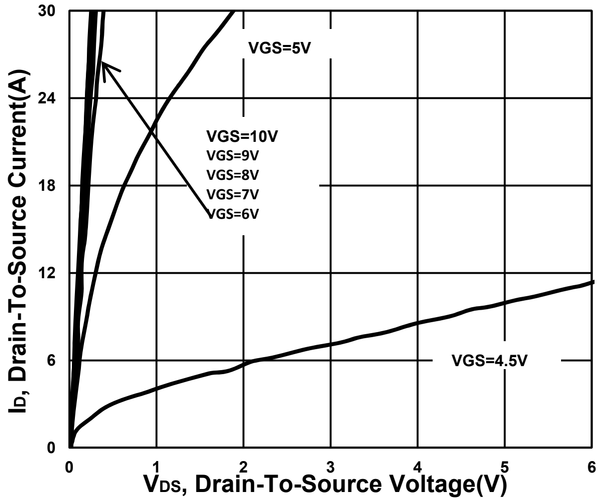
[2] The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

[3] Package limitation current is 50A.

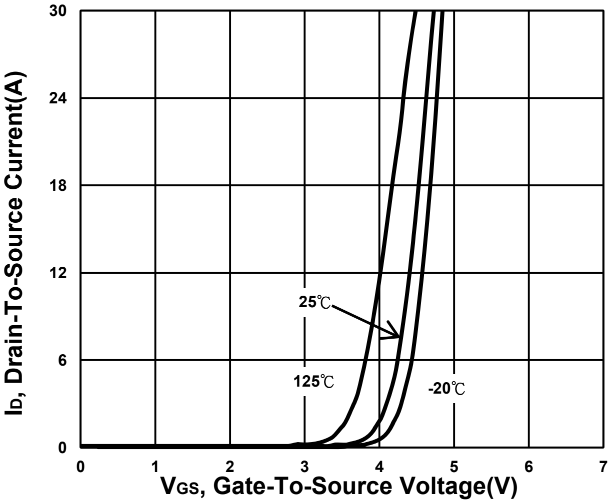
ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250µA	80			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250µA	2	3	4	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 64V, V_{GS} = 0V			1	µA
		V_{DS} = 60V, V_{GS} = 0V, T_J = 55 °C			10	
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 7V, I_D = 10A		7.8	12	mΩ
		V_{GS} = 10V, I_D = 10A		7.2	9	
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D = 10A		57		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		2824		pF
Output Capacitance	C_{oss}			322		
Reverse Transfer Capacitance	C_{rss}			178		
Gate Resistance	R_g	V_{GS} = 0V, V_{DS} = 0V, f = 1MHz		0.9		Ω
Total Gate Charge[2]	Q_g V_{GS} = 10V	V_{DS} = 40V , V_{GS} = 10V, I_D = 10A		53.2		nC
	Q_g V_{GS} = 7V			40.4		
Gate-Source Charge[2]	Q_{gs}			13.5		
Gate-Drain Charge[2]	Q_{gd}			17.8		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} = 40V , I_D ≅ 10A, V_{GS} = 10V, R_{GEN} =6Ω		35		nS
Rise Time[2]	t_r			40		
Turn-Off Delay Time[2]	$t_{d(off)}$			60		
Fall Time[2]	t_f			40		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current[3]	I_S				38	A
Forward Voltage[1]	V_{SD}	I_F = 10A, V_{GS} = 0V			1.4	V
Reverse Recovery Time	t_{rr}	I_F = 10A, dI_F/dt = 100A / µS		34		nS
Reverse Recovery Charge	Q_{rr}			37		nC

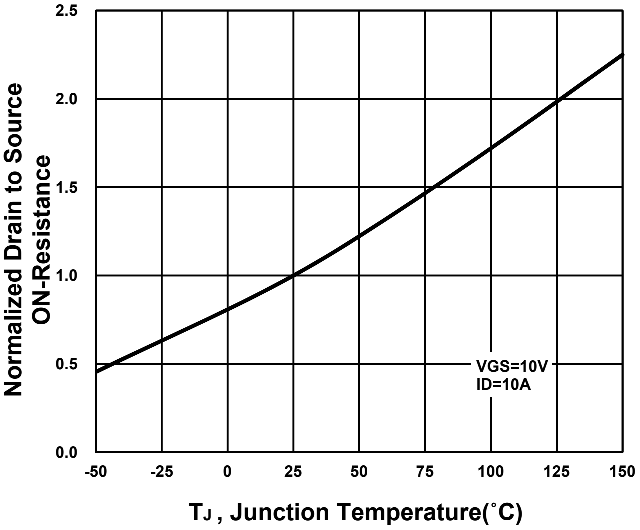
[1] Pulse test : Pulse Width ≤ 300 µsec, Duty Cycle ≤ 2%.
[2] Independent of operating temperature.
[3] Package limitation current is 50A.



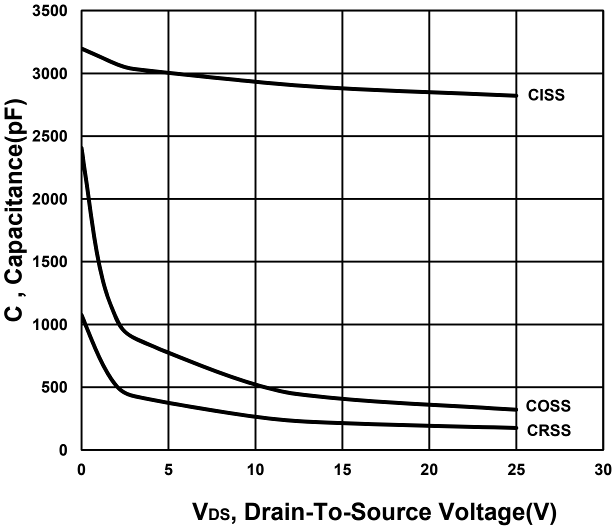
Output Characteristics



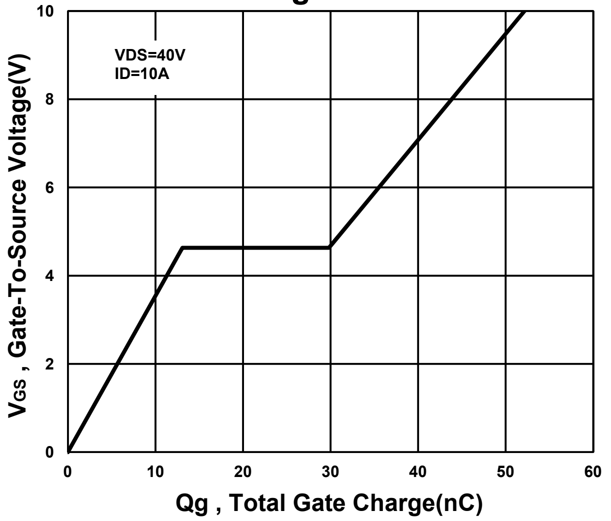
Transfer Characteristics



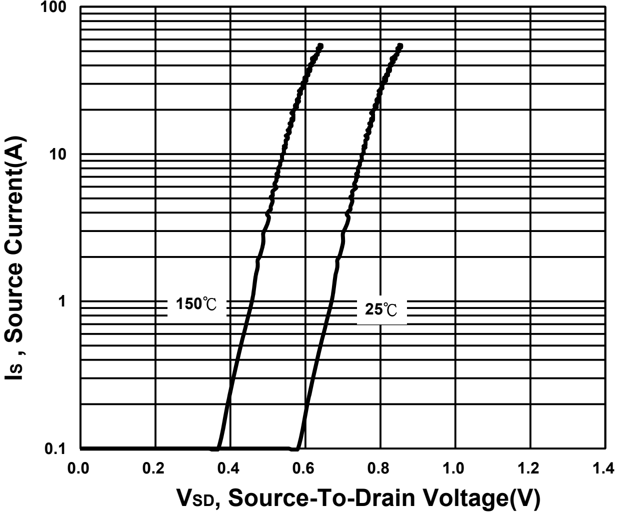
On-Resistance VS Temperature



Capacitance Characteristic

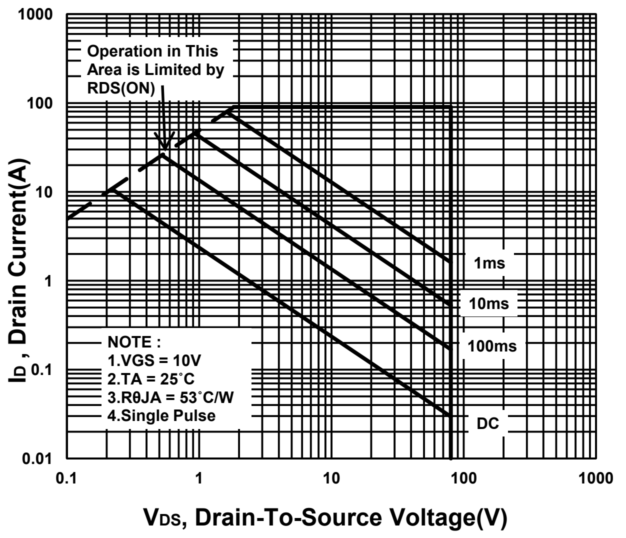


Gate charge Characteristics

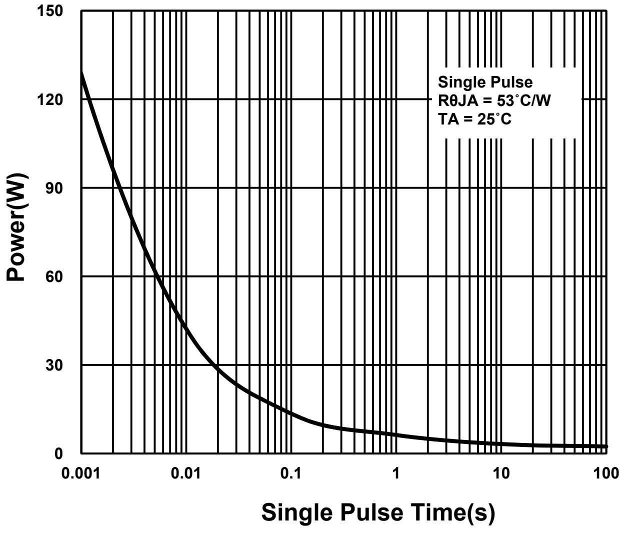


Source-Drain Diode Forward Voltage

Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve