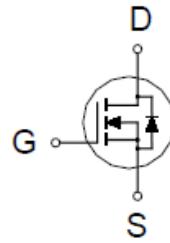
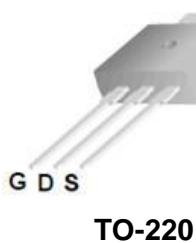


P0908AT

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
80V	9mΩ @ $V_{GS} = 10V$	64A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_C = 25^\circ C$	I_D	64	A
	$T_C = 100^\circ C$		41	
Pulsed Drain Current ¹		I_{DM}	160	A
Avalanche Current		I_{AS}	38	
Avalanche Energy	$L = 0.1mH$	E_{AS}	72	mJ
Power Dissipation	$T_C = 25^\circ C$	P_D	83	W
	$T_C = 100^\circ C$		33	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$	1.5	1.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		50	

¹Pulse width limited by maximum junction temperature.

P0908AT

N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

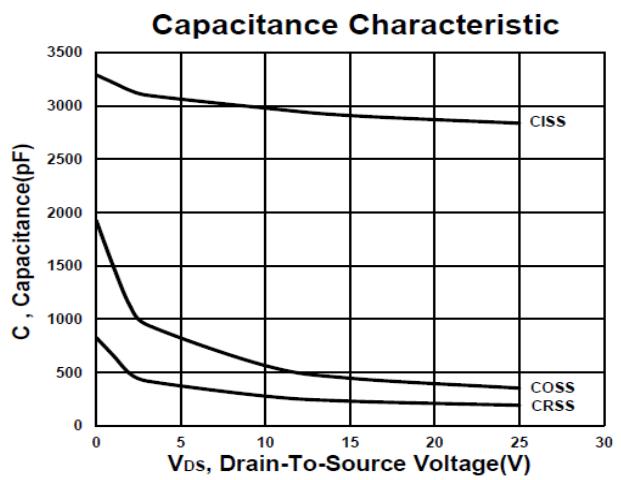
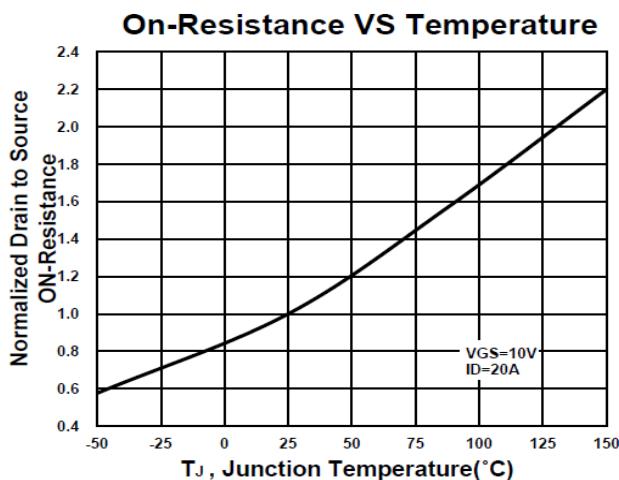
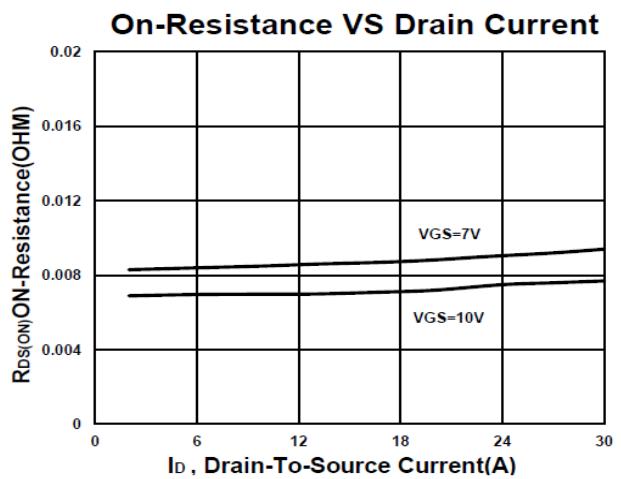
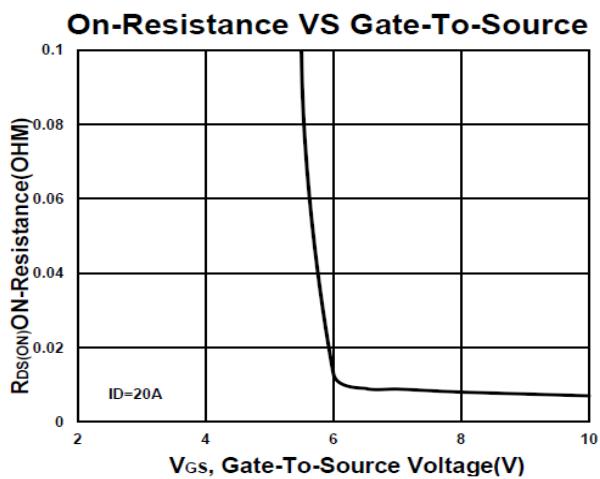
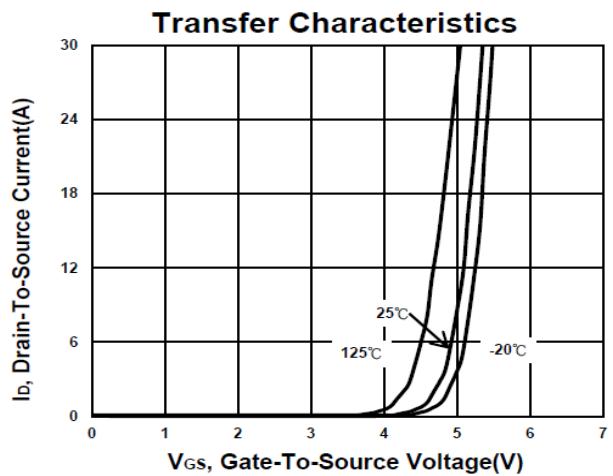
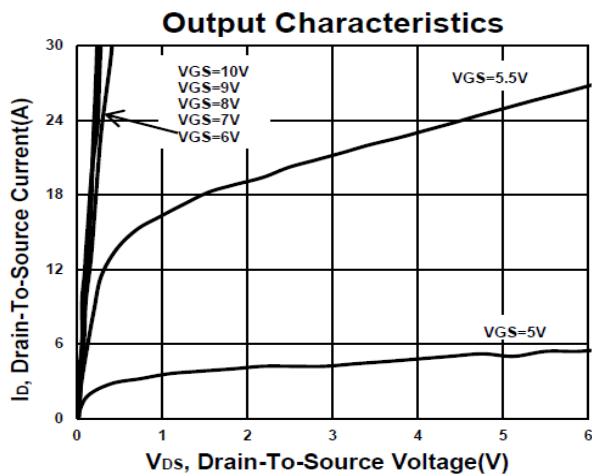
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	80			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	3.4	4	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 64\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 7\text{V}, I_D = 15\text{A}$		8.1	12	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		7	9	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 20\text{A}$		57		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		2853		pF
Output Capacitance	C_{oss}			355		
Reverse Transfer Capacitance	C_{rss}			199		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		0.9		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 15\text{V}, I_D = 20\text{A}$		55		nC
				41.5		
Gate-Source Charge ²	Q_{gs}			15.3		
Gate-Drain Charge ²	Q_{gd}			19.4		
Turn-On Delay Time ²	$t_{\text{d(on)}}$	$V_{\text{DD}} = 15\text{V}, I_D \geq 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		37		nS
Rise Time ²	t_r			42		
Turn-Off Delay Time ²	$t_{\text{d(off)}}$			63		
Fall Time ²	t_f			48		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_s				59	A
Forward Voltage ¹	V_{SD}	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			1.4	V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI_s/dt = 100\text{A}/\mu\text{s}$		35		nS
Reverse Recovery Charge	Q_{rr}			40		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

P0908AT

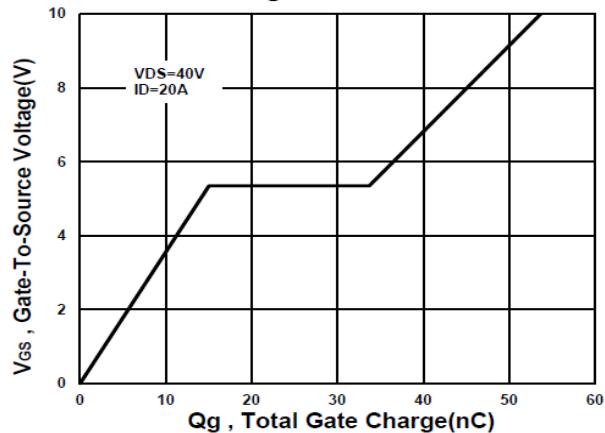
N-Channel Enhancement Mode MOSFET



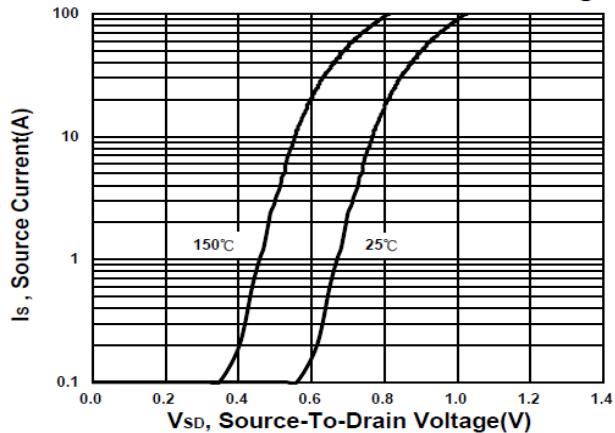
P0908AT

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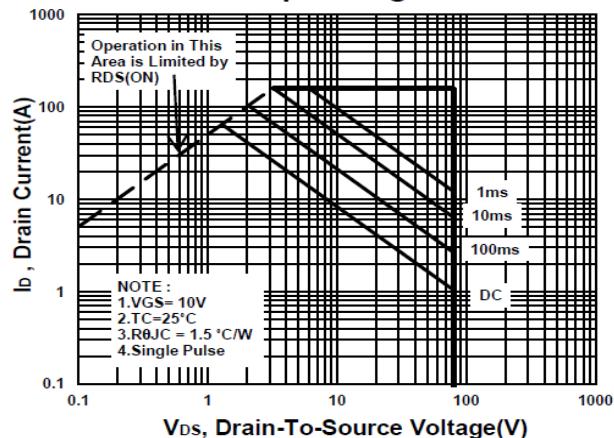
Gate charge Characteristics



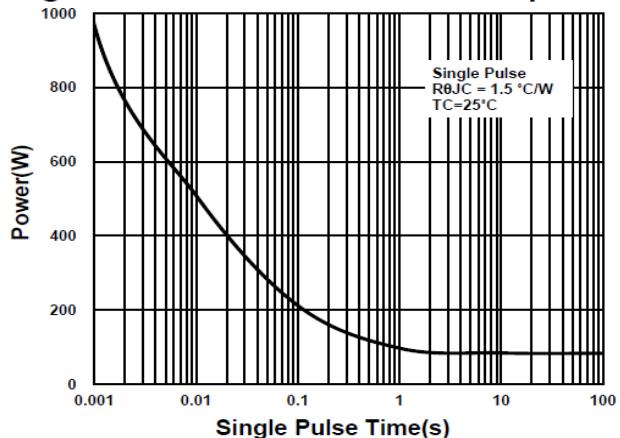
Source-Drain Diode Forward Voltage



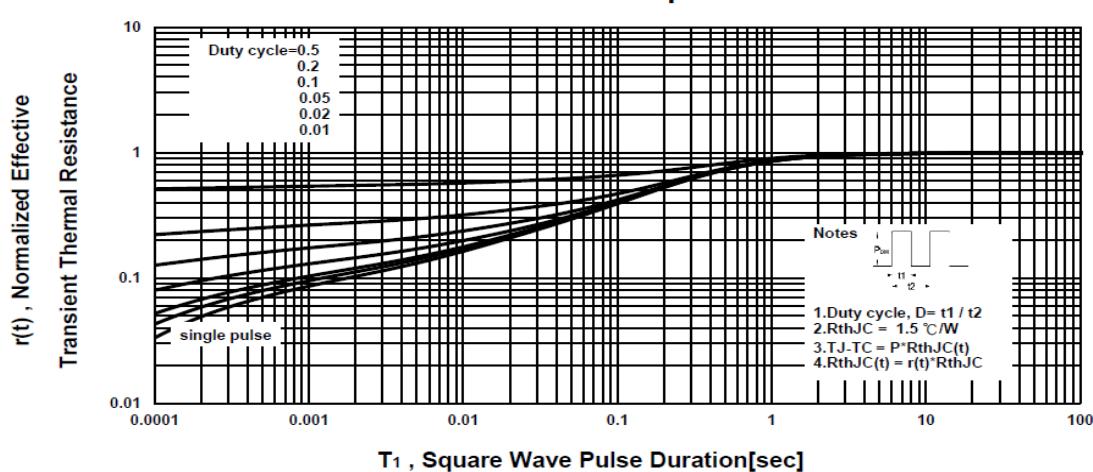
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



P0908AT

N-Channel Enhancement Mode MOSFET

Package Dimension

TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

