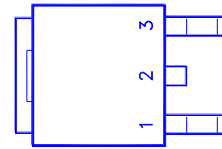
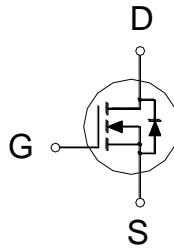




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
200	0.42Ω	9A



- 1. GATE
- 2. DRAIN
- 3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25\text{ }^\circ\text{C}$	I_D	9	A
	$T_C = 100\text{ }^\circ\text{C}$		5.8	
Pulsed Drain Current ^{1,2}		I_{DM}	36	
Avalanche Current		I_{AS}	9	
Avalanche Energy	$L = 2.8\text{mH}$	E_{AS}	112	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	P_D	62	W
	$T_C = 100\text{ }^\circ\text{C}$		25	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATING

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

²Limited by package.

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$, Unless Otherwise Noted)

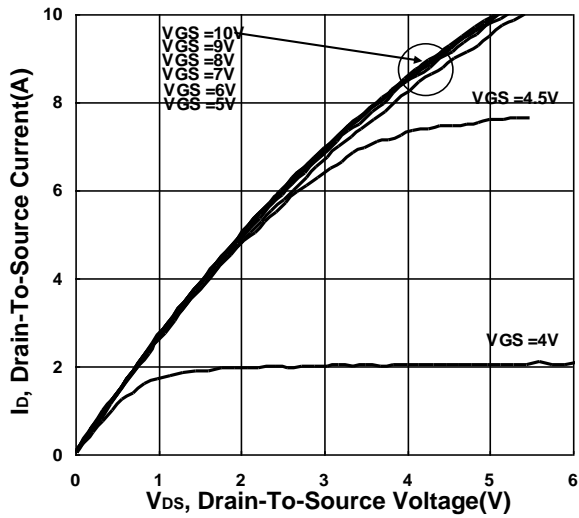
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	2.6	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 200\text{V}, V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 160\text{V}, V_{GS} = 0\text{V}, T_J = 125\text{ }^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 10\text{V}, I_D = 4.5\text{A}$		0.35	0.42	Ω
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10\text{V}, I_D = 4.5\text{A}$		10		S

DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		852		nF
Output Capacitance	C_{oss}			122		
Reverse Transfer Capacitance	C_{rss}			25		
Total Gate Charge ²	Q_g	$V_{DS} = 160V, V_{GS} = 10V,$ $I_D = 9A$		27		nC
Gate-Source Charge ²	Q_{gs}			3.6		
Gate-Drain Charge ²	Q_{gd}			13		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 100V,$ $I_D \cong 9A, V_{GS} = 10V, R_{GEN} = 6\Omega$		35		nS
Rise Time ²	t_r			250		
Turn-Off Delay Time ²	$t_{d(off)}$			149		
Fall Time ²	t_f			120		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$)						
Continuous Current ²	I_S				9	A
Forward Voltage ¹	V_{SD}	$I_F = 9A, V_{GS} = 0V$			1.6	V
Reverse Recovery Time	t_{rr}	$I_F = 9A, di_F/dt = 100A / \mu S$		166		nS
Reverse Recovery Charge	Q_{rr}	$V_{GS} = 0V$		861		nC

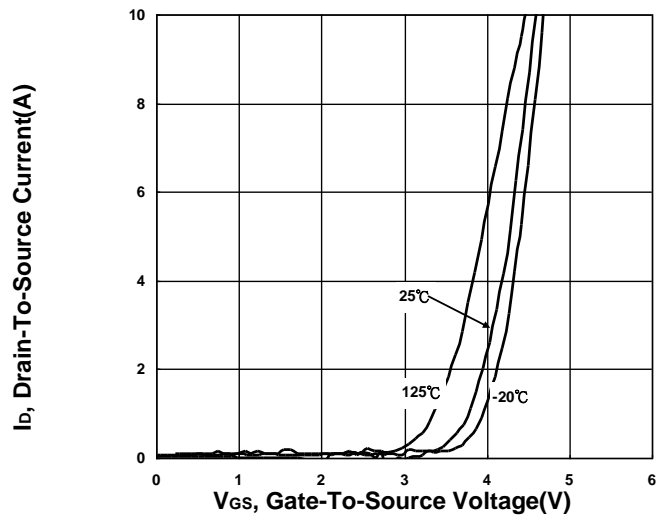
¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

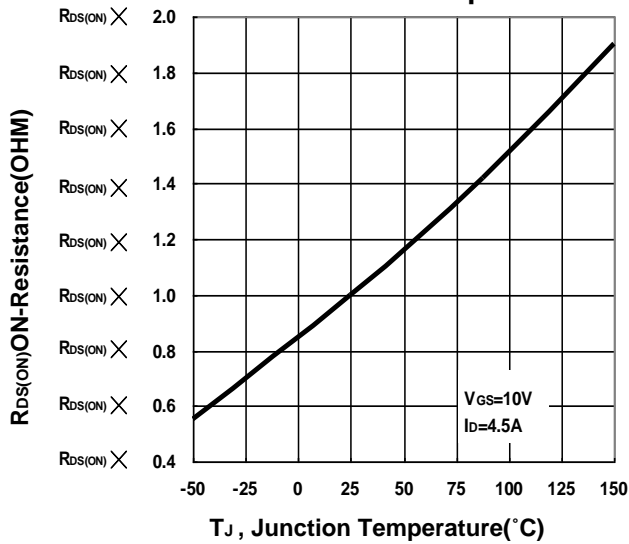
Output Characteristics



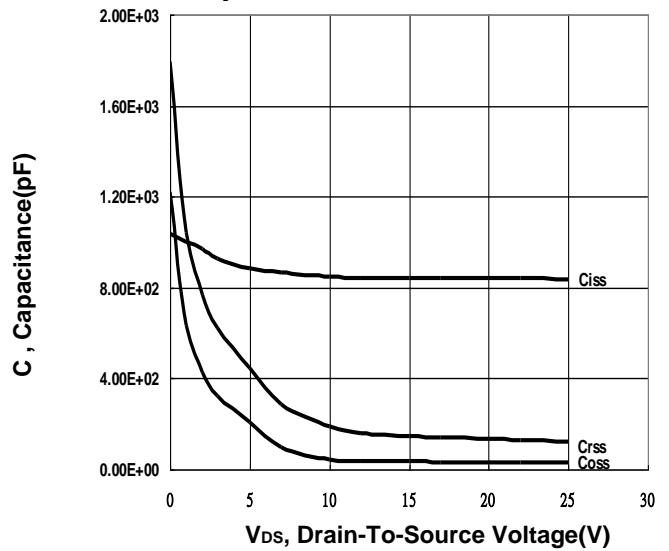
Transfer Characteristics



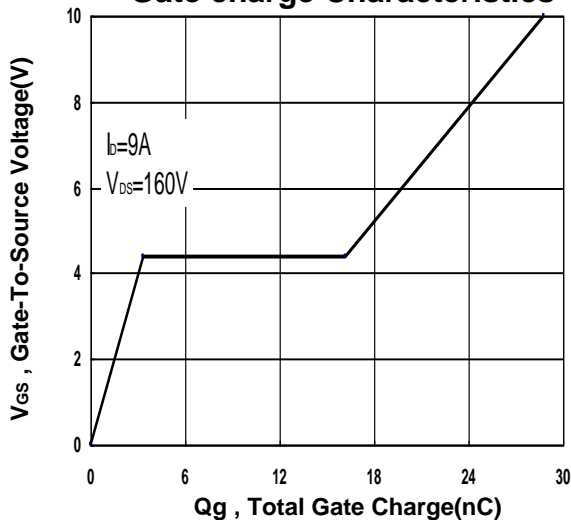
On-Resistance VS Temperature



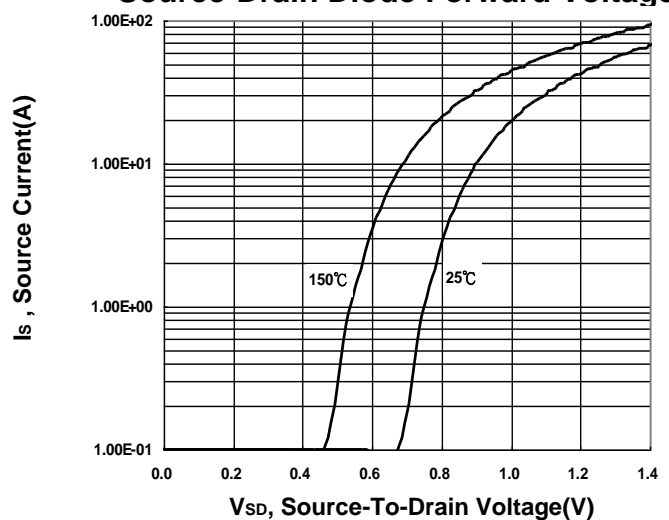
Capacitance Characteristic



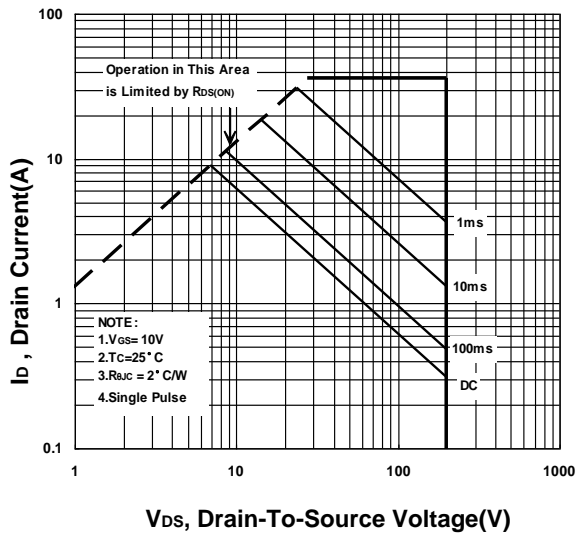
Gate charge Characteristics



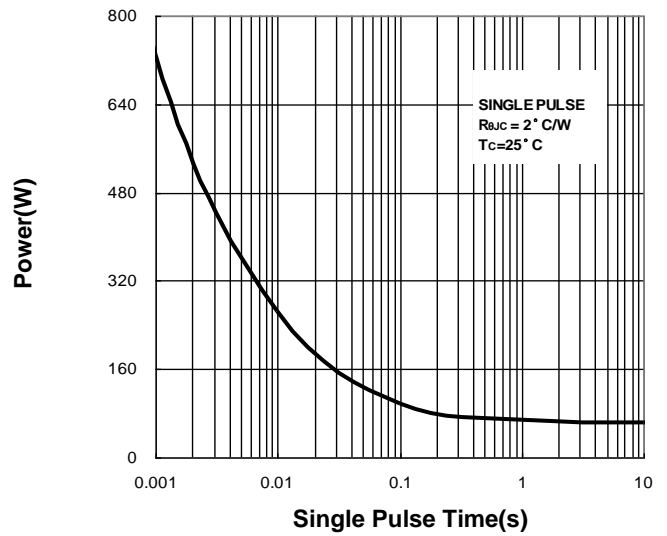
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

