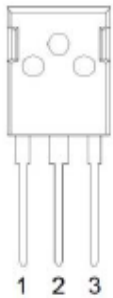


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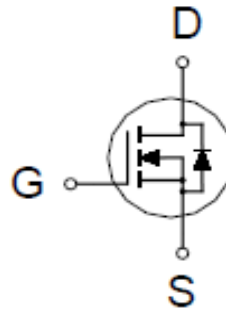
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
900V	1.3Ω @ $V_{GS} = 10V$	9A



1: GATE
2: DRAIN
3: SOURCE



TO-247

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	900	V
Gate-Source Voltage		V_{GS}	±30	V
Continuous Drain Current	$T_C = 25\text{ °C}$	I_D	9	A
	$T_C = 100\text{ °C}$		5.6	
Pulsed Drain Current ¹		I_{DM}	36	
Avalanche Current		I_{AS}	6	
Avalanche Energy	L = 10mH	E_{AS}	180	mJ
Power Dissipation	$T_C = 25\text{ °C}$	P_D	250	W
	$T_C = 100\text{ °C}$		100	
Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	°C / W
Junction-to-Case	$R_{\theta JC}$		0.5	

¹Pulse width limited by maximum junction temperature.

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N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

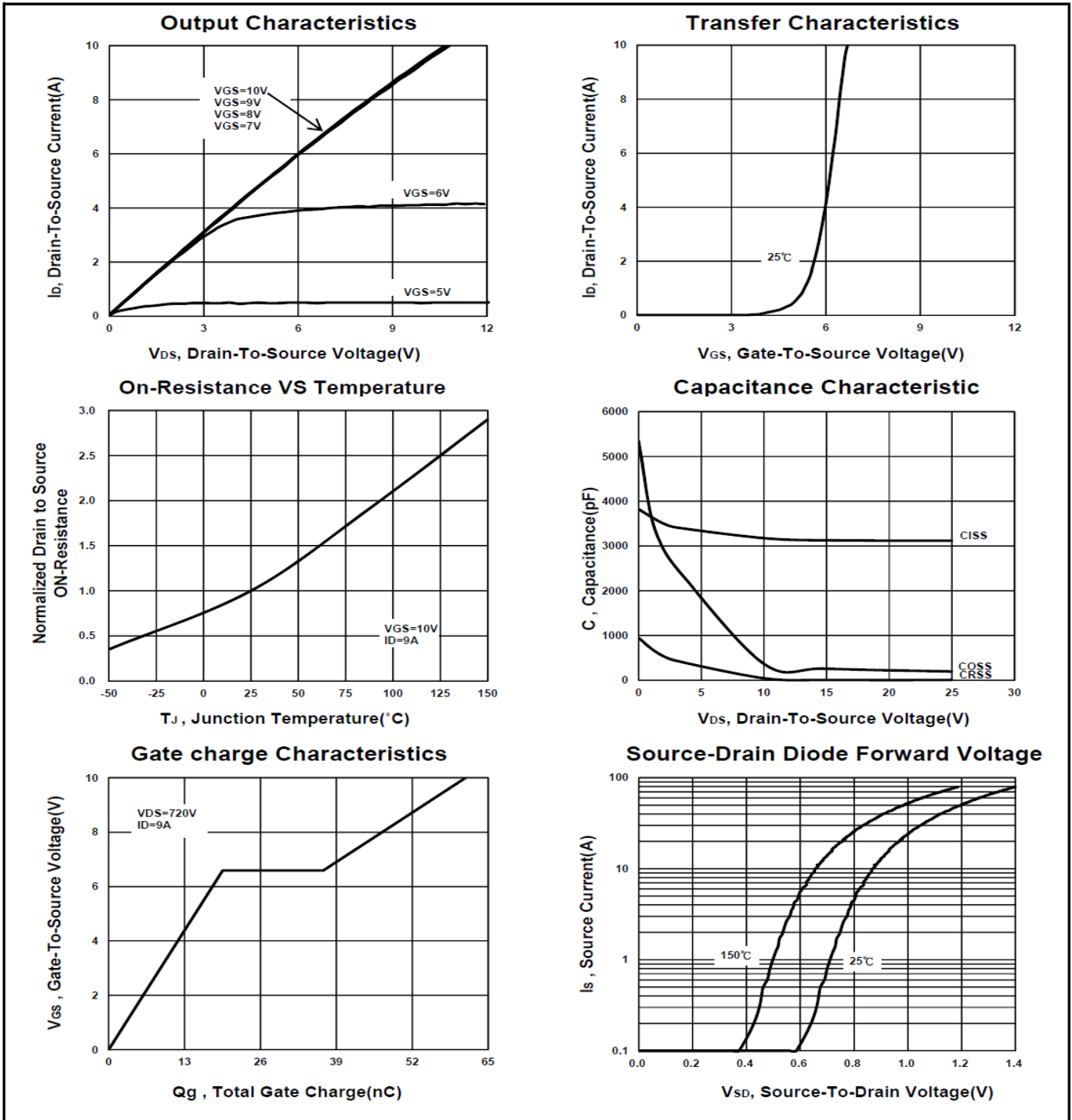
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	900			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2	3.3	4	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±30V			±100	nA
Gate Voltage Drain Current	I _{DSS}	V _{DS} = 900V, V _{GS} = 0V			1	μA
		V _{DS} = 720V, V _{GS} = 0V, T _J = 125°C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 4.5A		1	1.3	Ω
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 4.5A		7.7		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		3137		pF
Output Capacitance	C _{oss}			197		
Reverse Transfer Capacitance	C _{riss}			7		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		4.3		Ω
Total Gate Charge ²	Q _g	V _{GS} = 0V , V _{DS} = 720V , I _D = 9A		62		nC
Gate-Source Charge ²	Q _{gs}			20		
Gate-Drain Charge ²	Q _{gd}			18		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 450V , I _D ≅ 9A, V _{GS} = 10V, R _{GEN} = 25Ω		58		nS
Rise Time ²	t _r			104		
Turn-Off Delay Time ²	t _{d(off)}			150		
Fall Time ²	t _f			78		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				9	A
Forward Voltage ¹	V _{SD}	I _F = 9A, V _{GS} = 0V			1.4	V
Reverse Recovery Time	t _{rr}	I _F = 9A, dI/dt = 100A / μS		734		nS
Reverse Recovery Charge	Q _{rr}				8	

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

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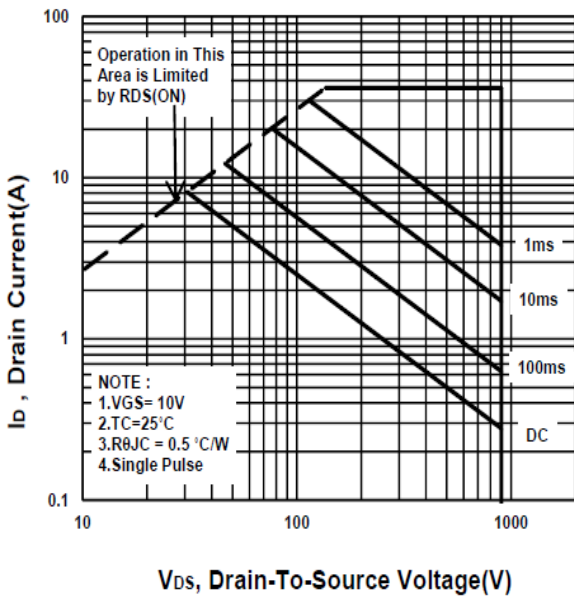
N-Channel Enhancement Mode MOSFET



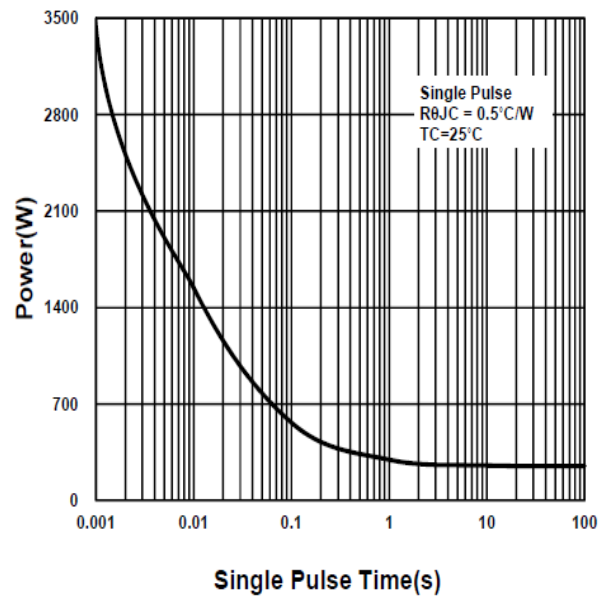
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N-Channel Enhancement Mode MOSFET

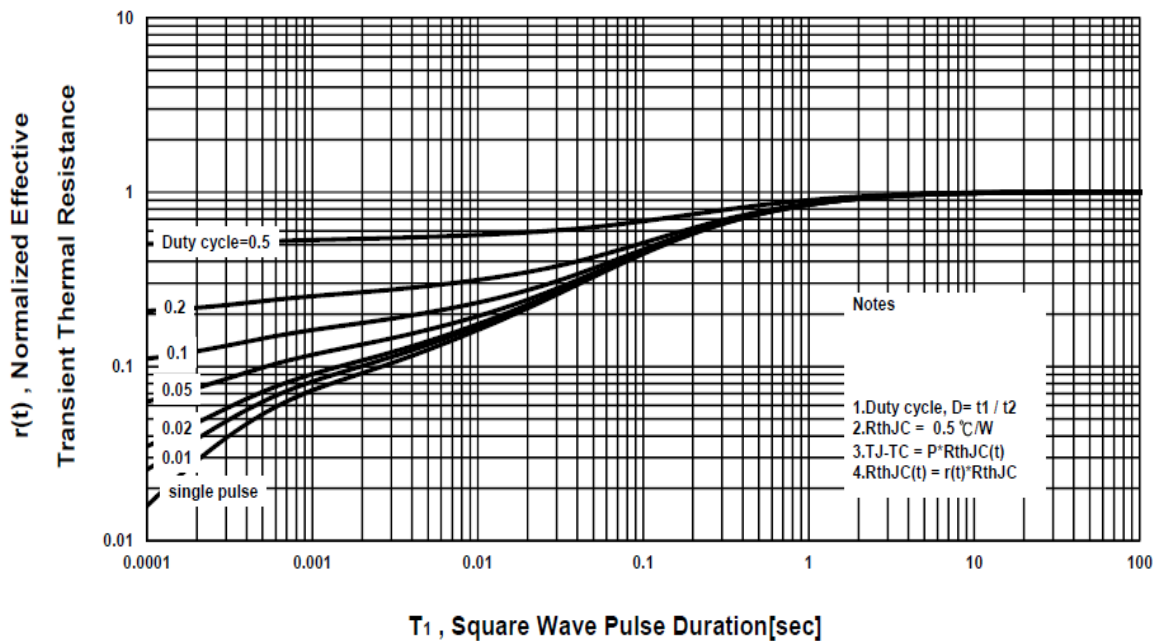
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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N-Channel Enhancement Mode MOSFET

Package Dimension

TO-247 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	15.45	15.6	15.75	I		3.5	
B	20.3	20.45	20.6	J		3.6	
C	24.8	24.95	25.1	K		5.45	
D	40.9	41.1	41.3	L	7.1	7.2	7.3
E		5.98		M	1.9	2	2.1
F	1.8	2	2.2	N	2.2	2.4	2.6
G	2.8	3	3.2	O	0.5	0.6	0.7
H	1	1.2	1.4	P	4.85	5	5.15

