

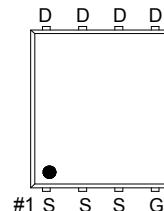
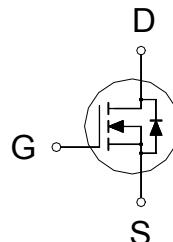
**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****P1003BKA**

PDFN 5x6P

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	9.8mΩ	44A

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>2</sup>	$T_C = 25^\circ\text{C}$	$I_D$	44	A
	$T_C = 100^\circ\text{C}$		28	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	110	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	12	W
	$T_A = 70^\circ\text{C}$		10	
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	31	W
	$T_C = 100^\circ\text{C}$		12	
Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	2.5	W
	$T_A = 70^\circ\text{C}$		1.6	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	Steady-State	$R_{\theta JA}$		50	°C / W
Junction-to-Case	Steady-State	$R_{\theta JC}$		4	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>Package limitation current is 30A**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$			1	$\mu\text{A}$
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ\text{C}$			10	

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Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 15A$		11	14.5	$m\Omega$
		$V_{GS} = 10V, I_D = 20A$		8.2	9.8	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 20A$		50		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		1130		$pF$
Output Capacitance	$C_{oss}$			157		
Reverse Transfer Capacitance	$C_{rss}$			116		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		2.3		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 0.5V_{(BR)DSS}, V_{GS} = 10V, I_D = 20A$		22		$nC$
				11		
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			3.6		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			5		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$			17		$nS$
Rise Time <sup>2</sup>	$t_r$			10		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			33		
Fall Time <sup>2</sup>	$t_f$			10		

**SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_J = 25^\circ C$ )**

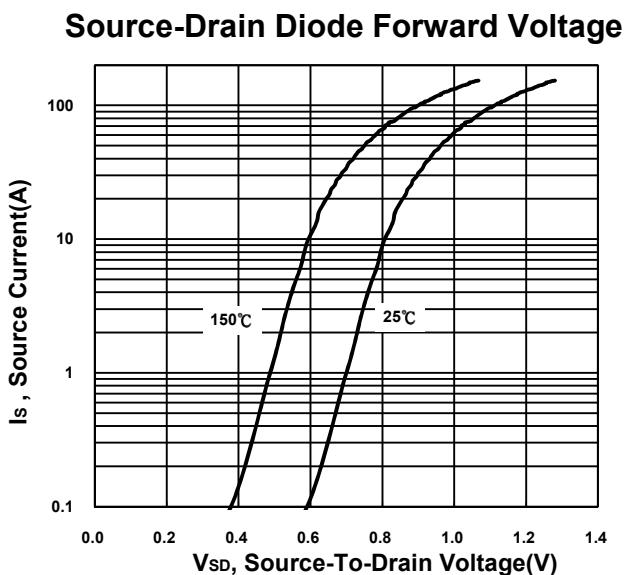
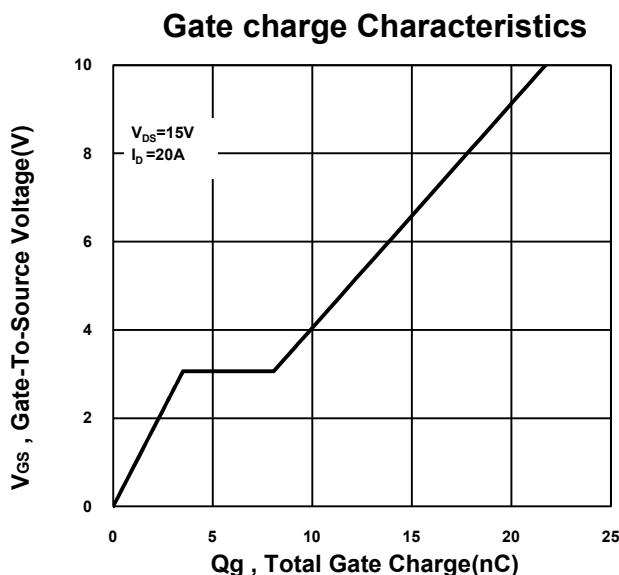
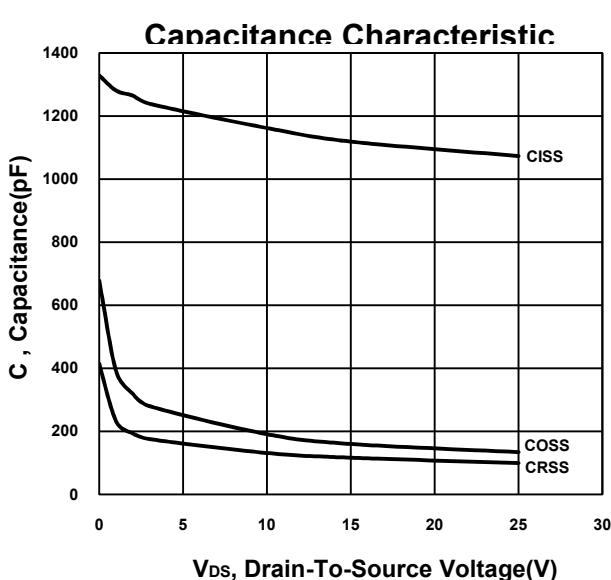
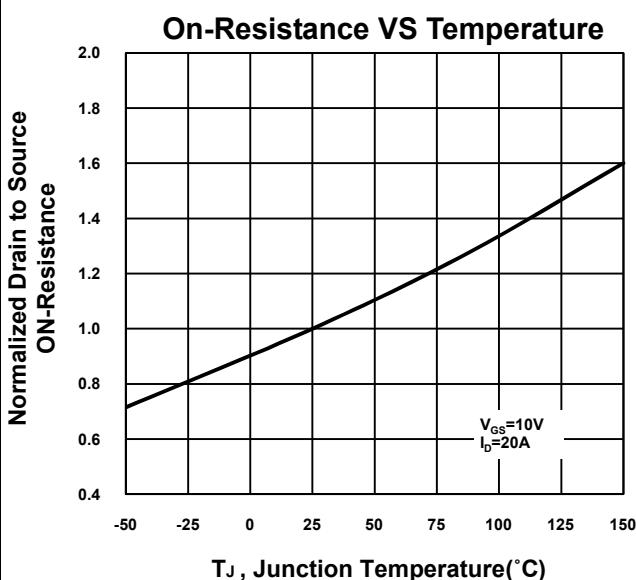
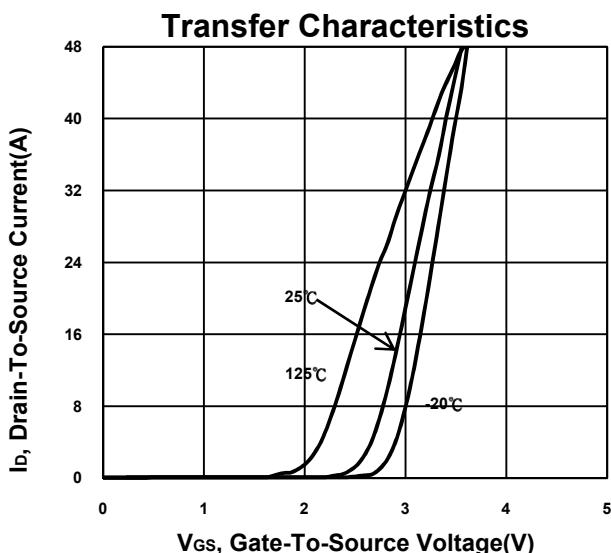
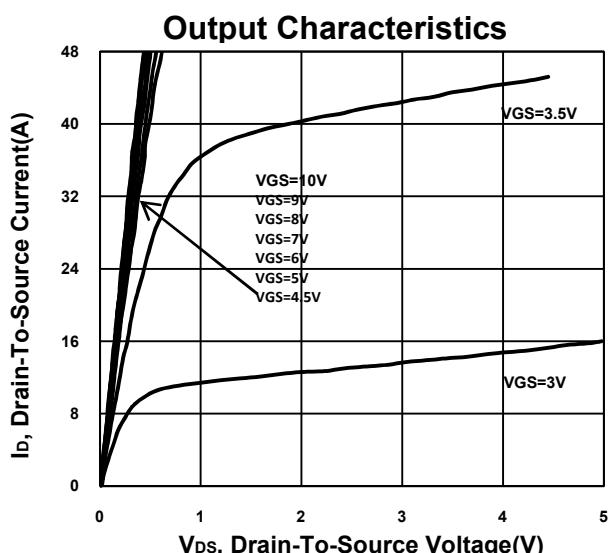
Continuous Current <sup>3</sup>	$I_S$				44	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 20A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 20A, dI_F/dt = 100A / \mu S$		15		$nS$
Reverse Recovery Charge	$Q_{rr}$			5		$nC$

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.<sup>3</sup>Package limitation current is 30A

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