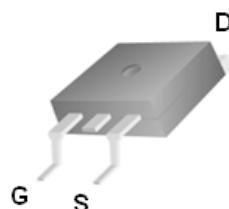


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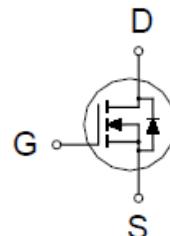
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
40V	11mΩ @ $V_{GS} = 10V$	53A



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ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ²	I_D	53	A
		34	
Pulsed Drain Current ¹	I_{DM}	159	
Avalanche Current	I_{AS}	40	
Avalanche Energy	E_{AS}	82	mJ
Power Dissipation	P_D	63	W
		25	
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2	°C / W

¹Pulse width limited by maximum junction temperature.

²The maximum current rating is limited by bond-wires.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.5	1.7	3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 32\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
On-State Drain Current ¹	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 10\text{V}$	159			A
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 5\text{V}, I_D = 8\text{A}$		13	21	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 15\text{A}$		8	11	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 15\text{A}$		26		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 20\text{V}, f = 1\text{MHz}$		1500		pF
Output Capacitance	C_{oss}			271		
Reverse Transfer Capacitance	C_{rss}			196		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		1.9		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 15\text{A}$		34		nC
Gate-Source Charge ²	Q_{gs}			6		
Gate-Drain Charge ²	Q_{gd}			11		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 20\text{V}, I_D \geq 15\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		6.4		nS
Rise Time ²	t_r			17		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			29		
Fall Time ²	t_f			16		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				48	A
Forward Voltage ¹	V_{SD}	$I_F = 15\text{A}, V_{\text{GS}} = 0\text{V}$			1.3	V
Reverse Recovery Time	t_{rr}	$I_F = 15\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		21		nS
Reverse Recovery Charge	Q_{rr}			13		nC

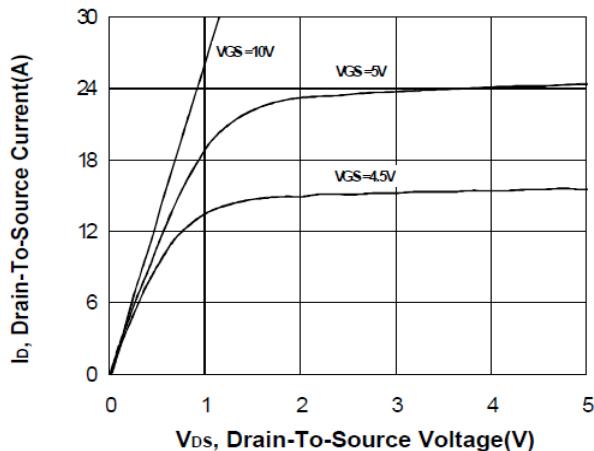
¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

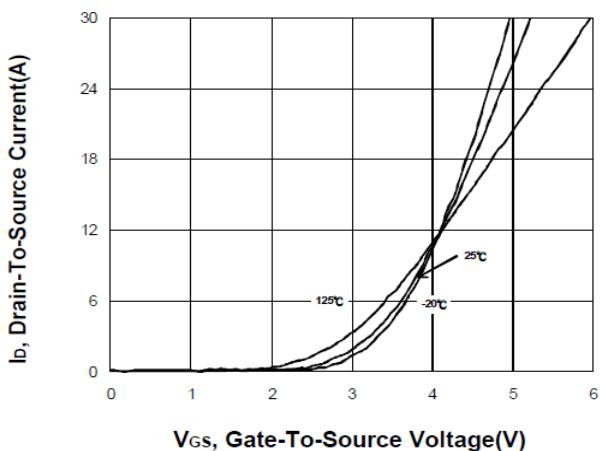
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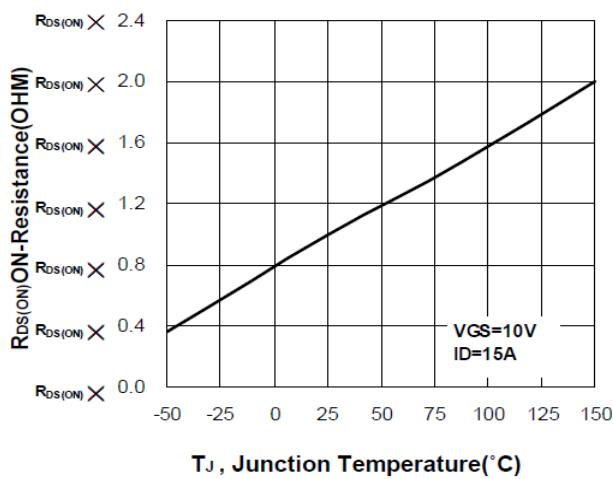
Output Characteristics



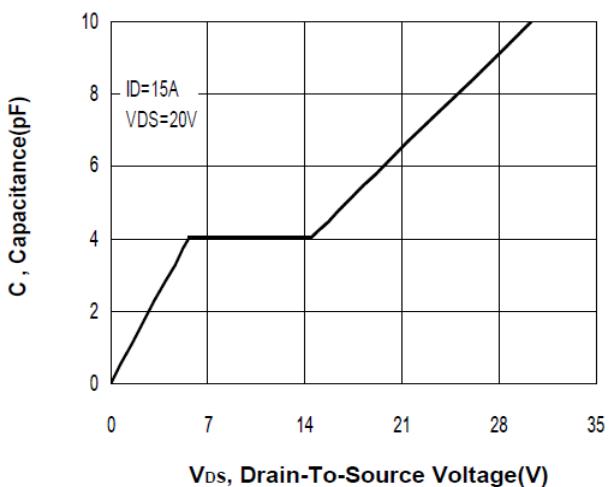
Transfer Characteristics



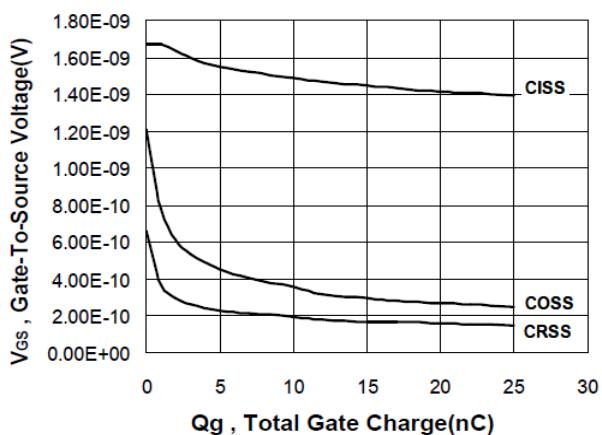
On-Resistance VS Temperature



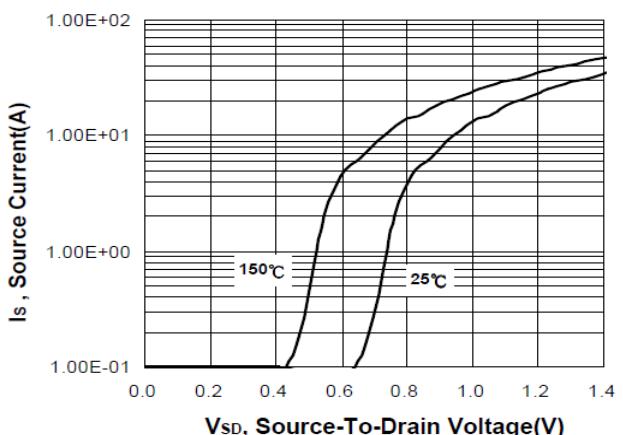
Capacitance Characteristic



Gate charge Characteristics

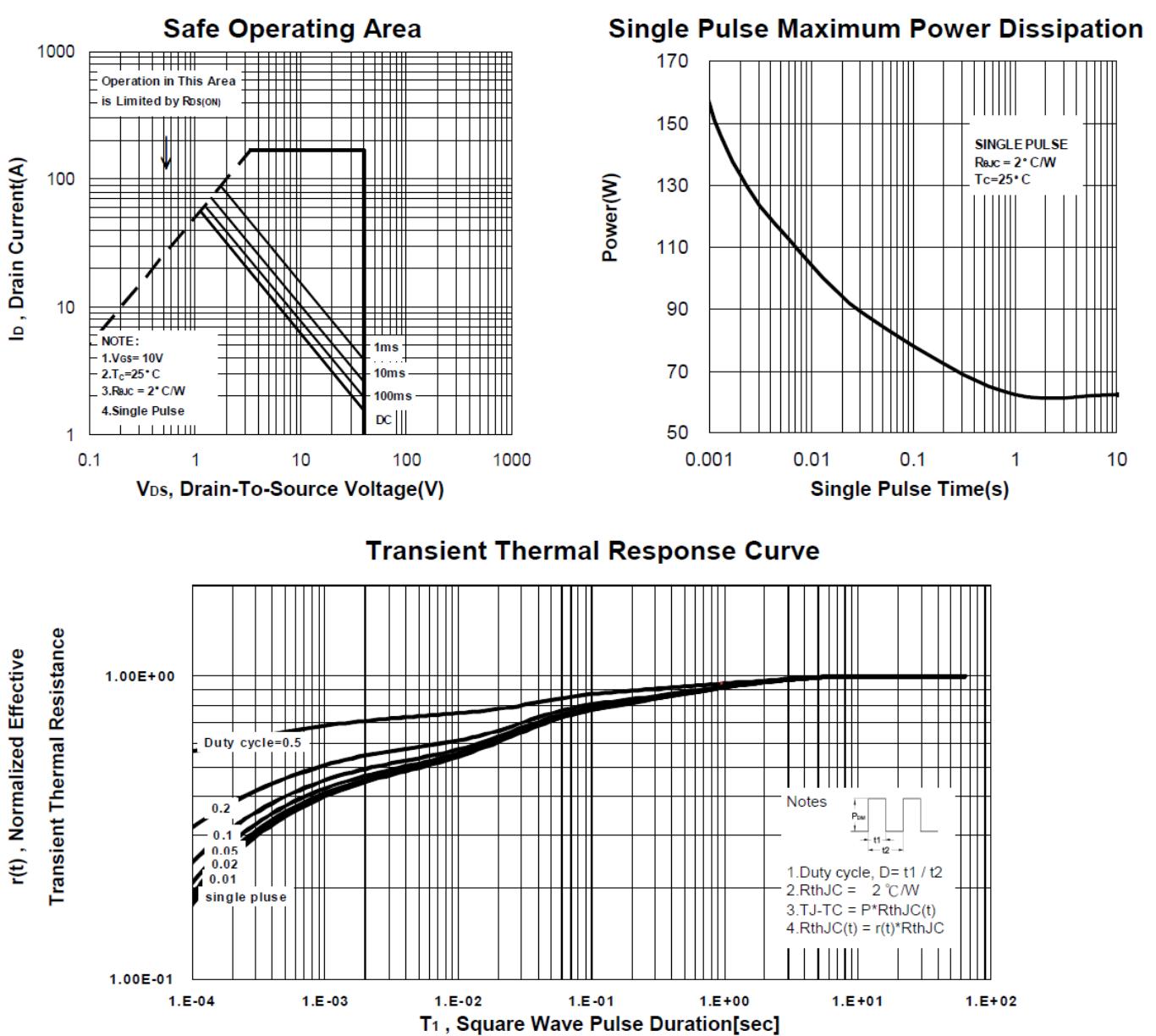


Source-Drain Diode Forward Voltage



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Package Dimension

TO-263 (D²PAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.8	e	4.08	5.08	6.08
A1	0		0.3	E	9.8		10.55
b	0.71		1.06	E1	6.9		8.7
b2	1.07		1.47	H	14.2		15.8
C	0.3		0.69	L	1.2		2.79
C2	1.15		1.45	L1	1		1.65
D	8.3		9.4	L2	1.2		1.78
D1	6.37		8.23				

