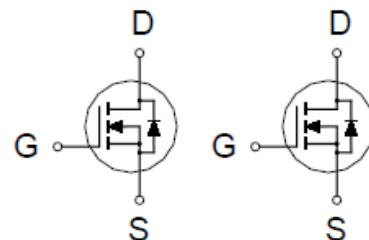
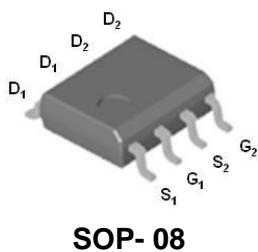


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N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
40V	13mΩ @ $V_{GS} = 10V$	10A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 24	
Continuous Drain Current	$T_A = 25^\circ C$	I_D	10	A
	$T_A = 70^\circ C$		8	
Pulsed Drain Current ¹		I_{DM}	40	A
Avalanche Current		I_{AS}	38	
Avalanche Energy ²	$L = 0.1mH$	E_{AS}	71	mJ
Power Dissipation	$T_A = 25^\circ C$	P_D	2	W
	$T_A = 70^\circ C$		1.28	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C
Lead Temperature($1/16"$ from case for 10 sec)		T_L	275	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		62.5	°C / W
Junction-to-Lead	$R_{\theta JL}$		25	

¹Pulse width limited by maximum junction temperature.

² $V_{DD} = 20V$, Starting $T_J = 25^\circ C$.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

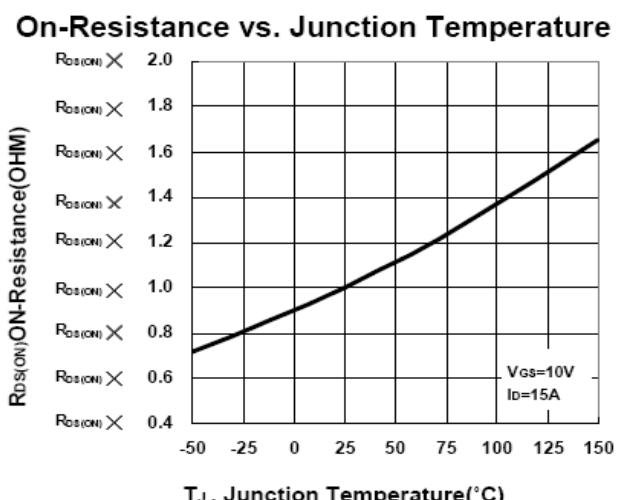
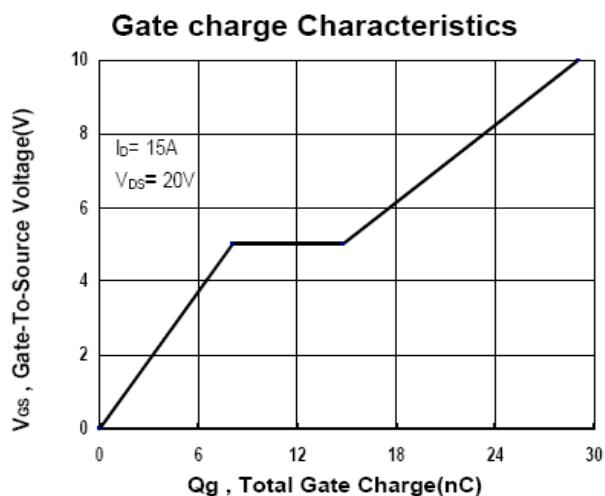
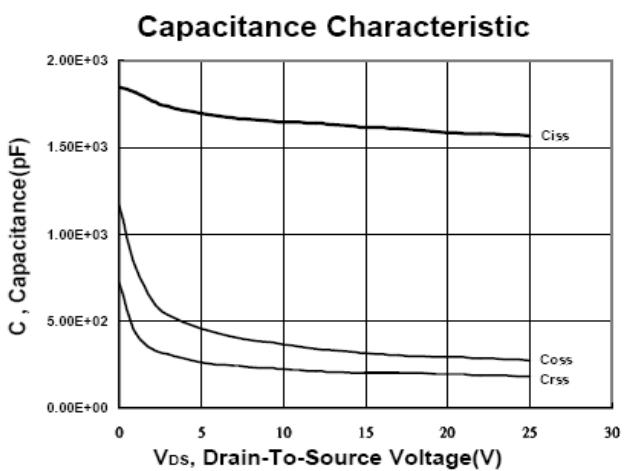
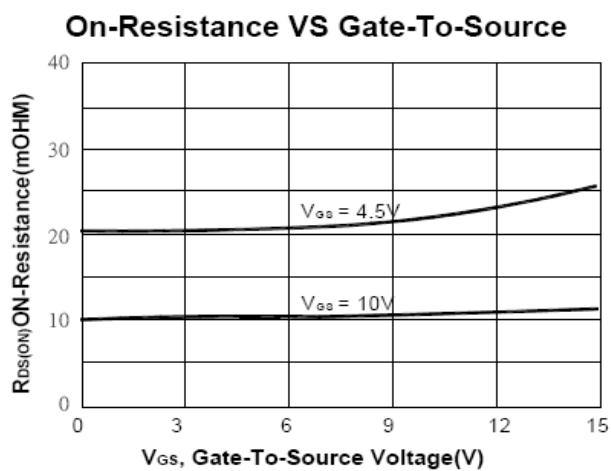
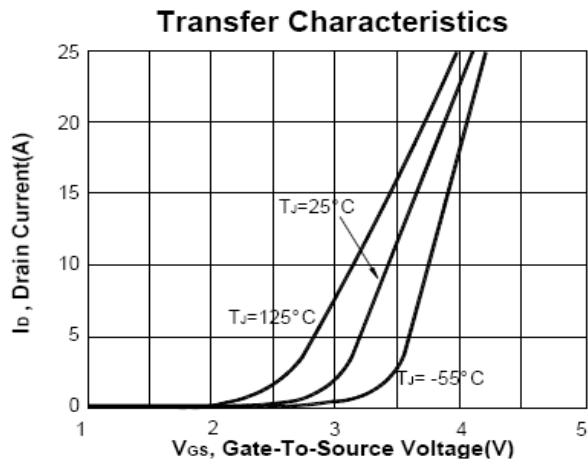
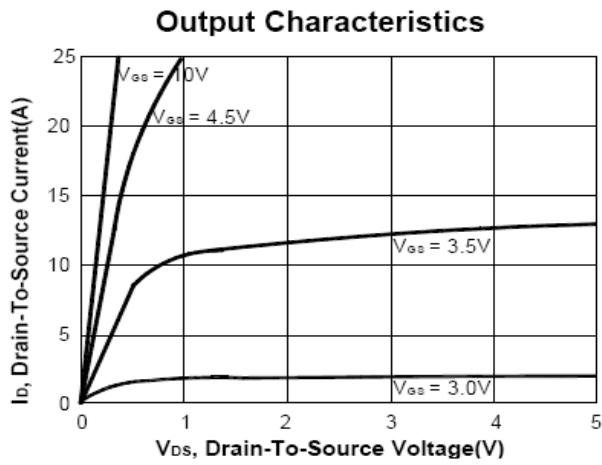
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.7	2.0	3.0	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 24\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 32\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	
On-State Drain Current ¹	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 10\text{V}$	40			A
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 5\text{V}, I_D = 8\text{A}$		17	20	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 15\text{A}$		12	13	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 10\text{V}, I_D = 20\text{A}$		25		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 20\text{V}, f = 1\text{MHz}$		1570		pF
Output Capacitance	C_{oss}			273		
Reverse Transfer Capacitance	C_{rss}			183		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		1.38		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 0.5V_{(\text{BR})\text{DSS}}, V_{\text{GS}} = 10\text{V}, I_D = 15\text{A}$		29		nC
Gate-Source Charge ²	Q_{gs}			8		
Gate-Drain Charge ²	Q_{gd}			7		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 20\text{V}, R_L = 1\Omega$ $I_D \geq 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		12	33	nS
Rise Time ²	t_r			35	65	
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			37	68	
Fall Time ²	t_f			12	23	
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				2	A
Forward Voltage ¹	V_{SD}	$I_F = I_S, V_{\text{GS}} = 0\text{V}$			1	V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		75		nS
Reverse Recovery Charge	Q_{rr}			55		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

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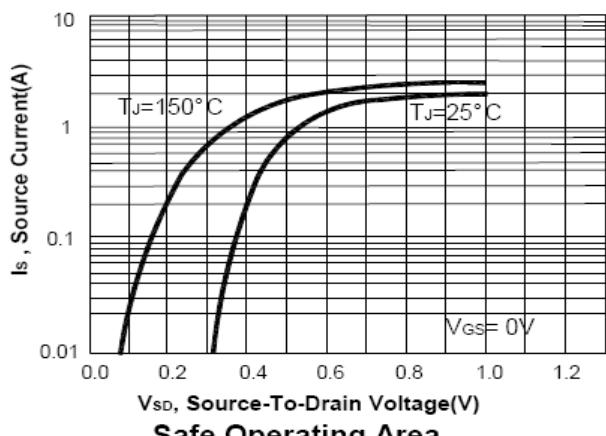
N-Channel Enhancement Mode MOSFET



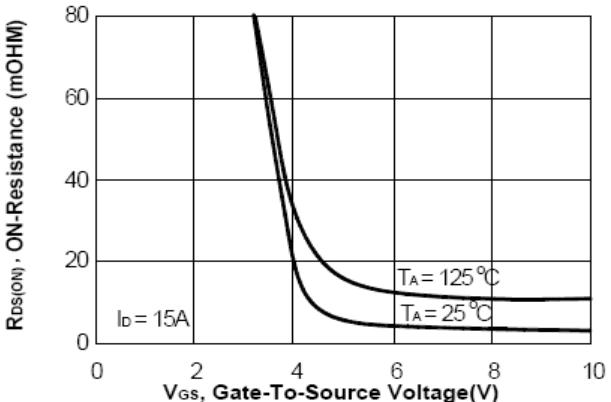
P1004HV

N-Channel Enhancement Mode MOSFET

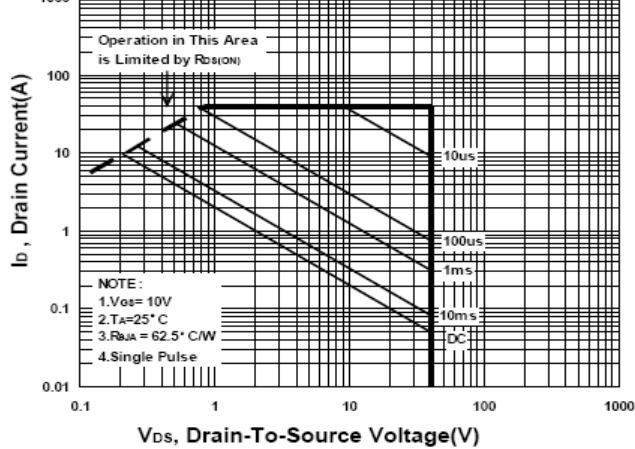
Body Diode Forward Voltage VS Source current



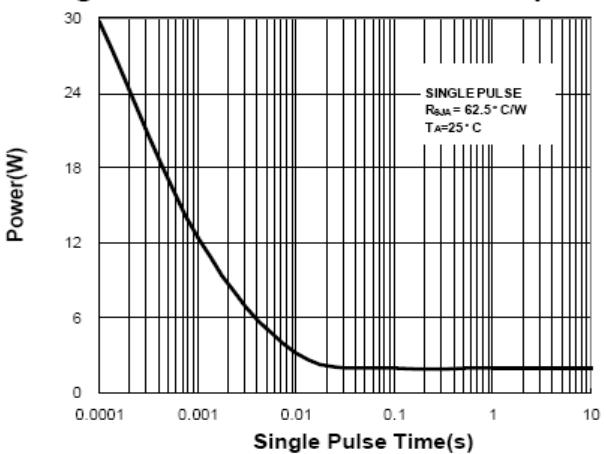
On-Resistance VS Gate-To-Source



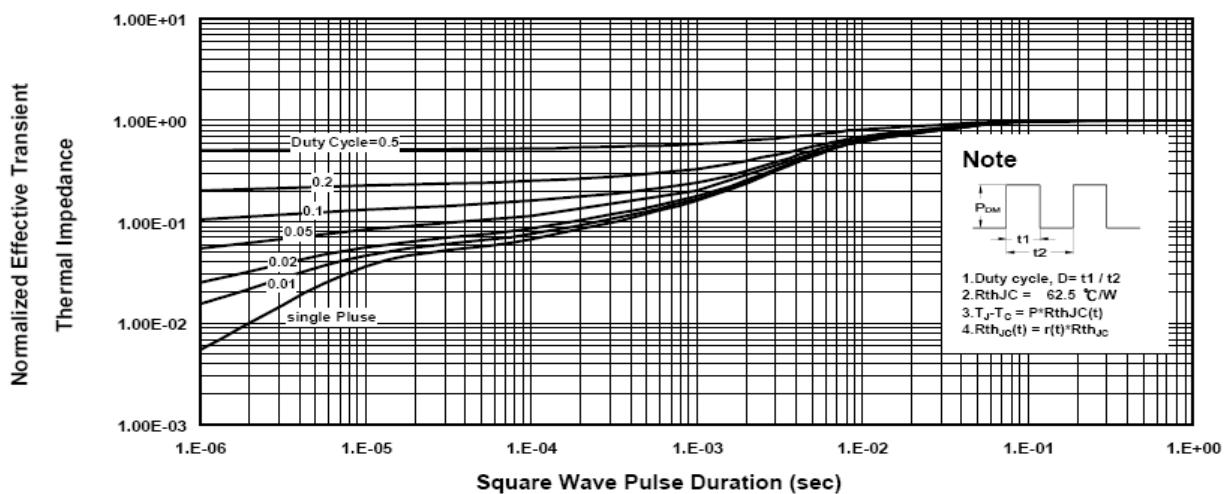
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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Package Dimension

SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

