

P1006BI

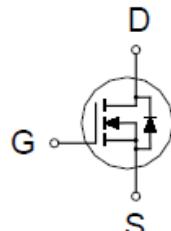
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
60V	10mΩ @ $V_{GS} = 10V$	66A



TO-251



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ²	I_D	66	A
		42	
Pulsed Drain Current ¹	I_{DM}	150	
Avalanche Current	I_{AS}	38.5	
Avalanche Energy	E_{AS}	74	mJ
Power Dissipation	P_D	96	W
		38	
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$	1.3	62.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$			

¹Pulse width limited by maximum junction temperature.

²Package limitation current is 30A.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 80\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 20\text{A}$		8.1	13	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		6.8	10	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 10\text{V}, I_D = 20\text{A}$		60		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		1920		pF
Output Capacitance	C_{oss}			215		
Reverse Transfer Capacitance	C_{rss}			140		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		0.7		Ω
Total Gate Charge ²	$Q_{\text{g}}(V_{\text{GS}}=10\text{V})$	$V_{\text{DS}} = 30\text{V}, I_D = 20\text{A}$		42		nC
	$Q_{\text{g}}(V_{\text{GS}}=4.5\text{V})$			23		
Gate-Source Charge ²	Q_{gs}			6		
Gate-Drain Charge ²	Q_{gd}			12		
Turn-On Delay Time ²	$t_{\text{d(on)}}$	$V_{\text{DS}} = 30\text{V}, I_D \geq 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		29		nS
Rise Time ²	t_r			31		
Turn-Off Delay Time ²	$t_{\text{d(off)}}$			51		
Fall Time ²	t_f			31		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current ³	I_s				66	A
Forward Voltage ¹	V_{SD}	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			1.3	V
Diode Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		26		nS
Diode Reverse Recovery Charge	Q_{rr}			19		nC

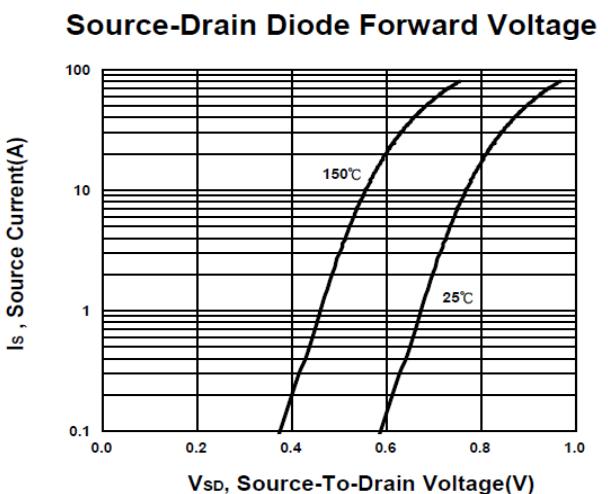
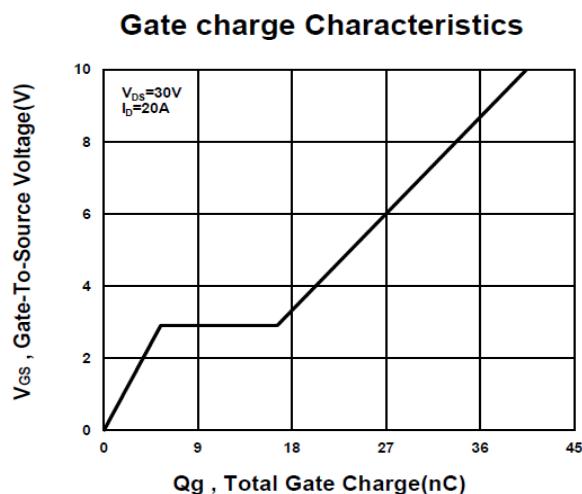
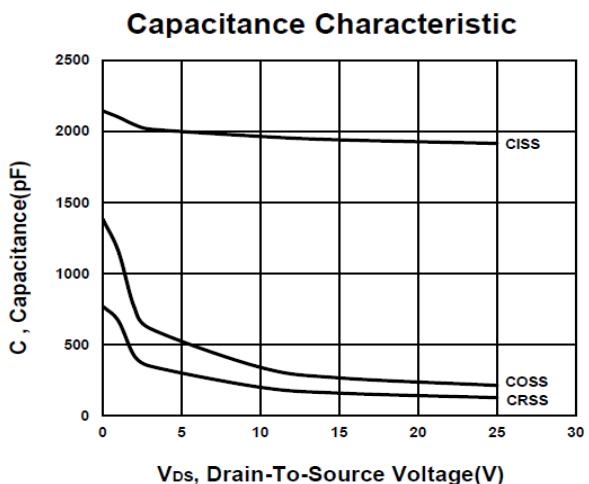
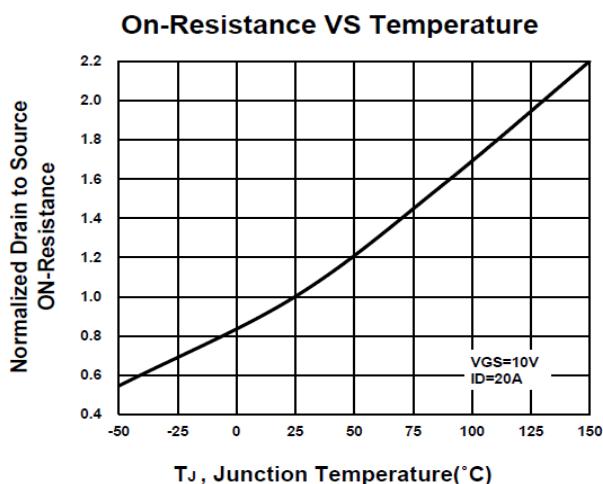
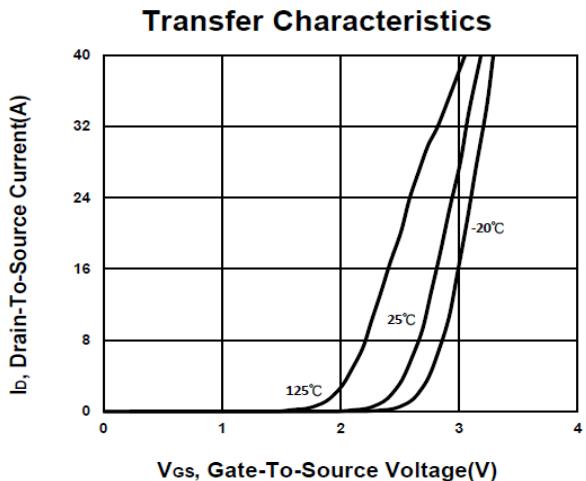
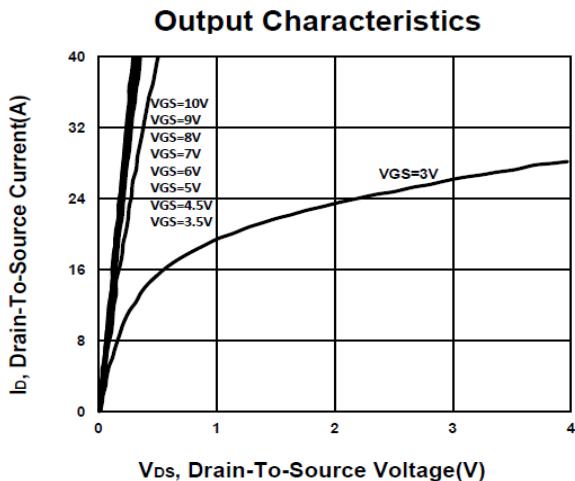
¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

³Package limitation current is 30A.

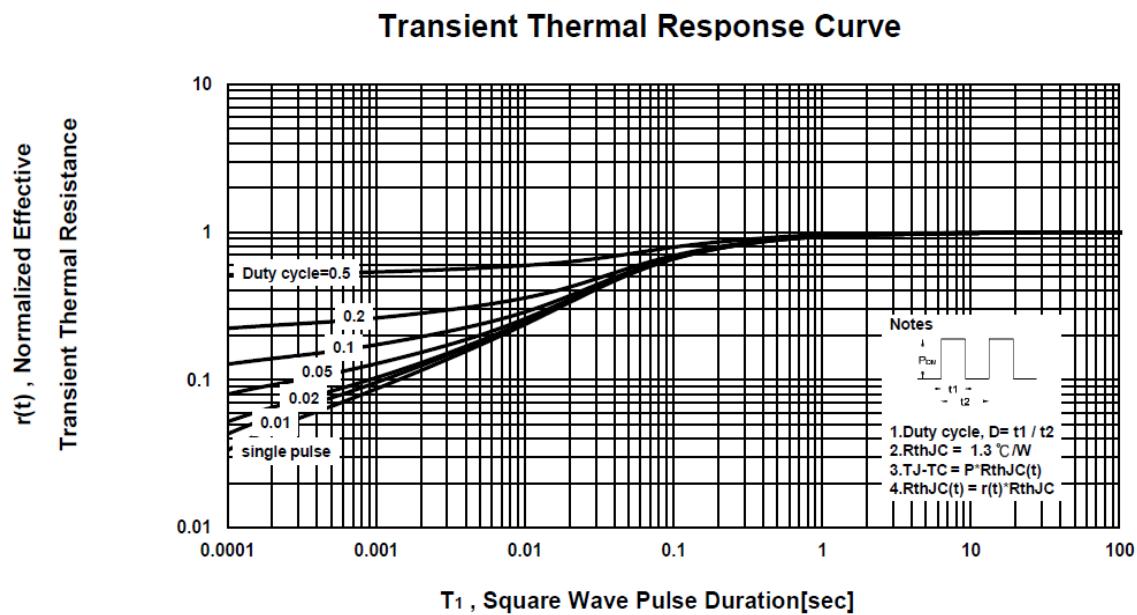
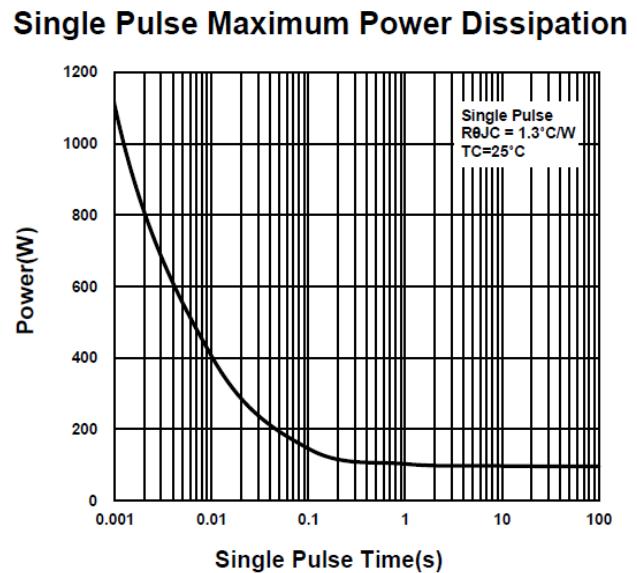
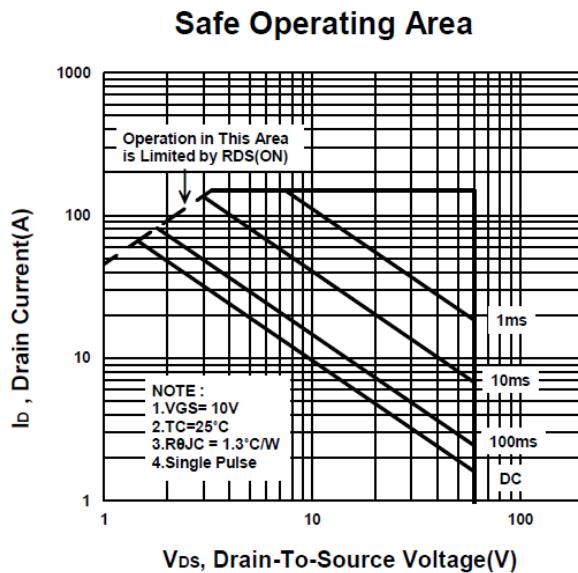
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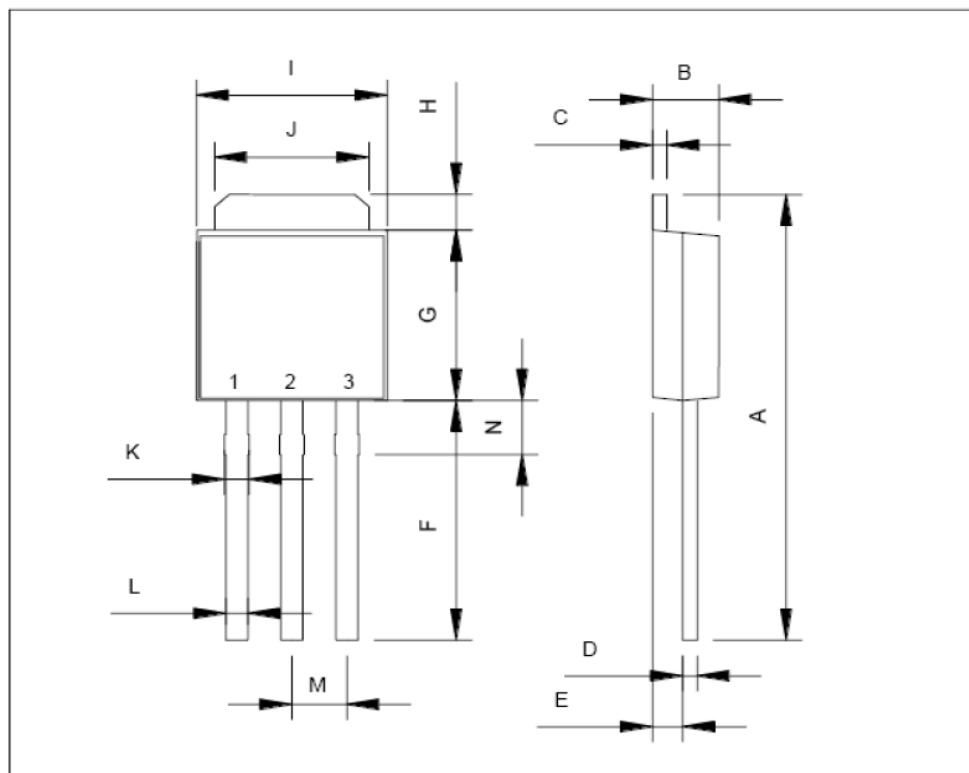
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Package Dimension

TO-251 MECHANICAL DATA

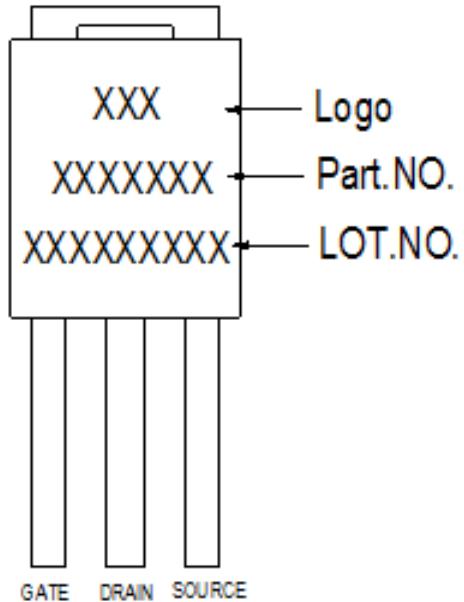
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	14	15	17.14	H	0.89		1.7
B	2.1	2.3	2.5	I	6.3		6.8
C	0.4	0.5	0.6	J	4.8		5.5
D	0.35	0.5	0.65	K	0.5	0.84	1.14
E	0.9	1.1	1.5	L	0.4	0.76	0.912
F	7		9.65	M		2.3	
G	5.3		6.22	N	1.4	2.16	2.23



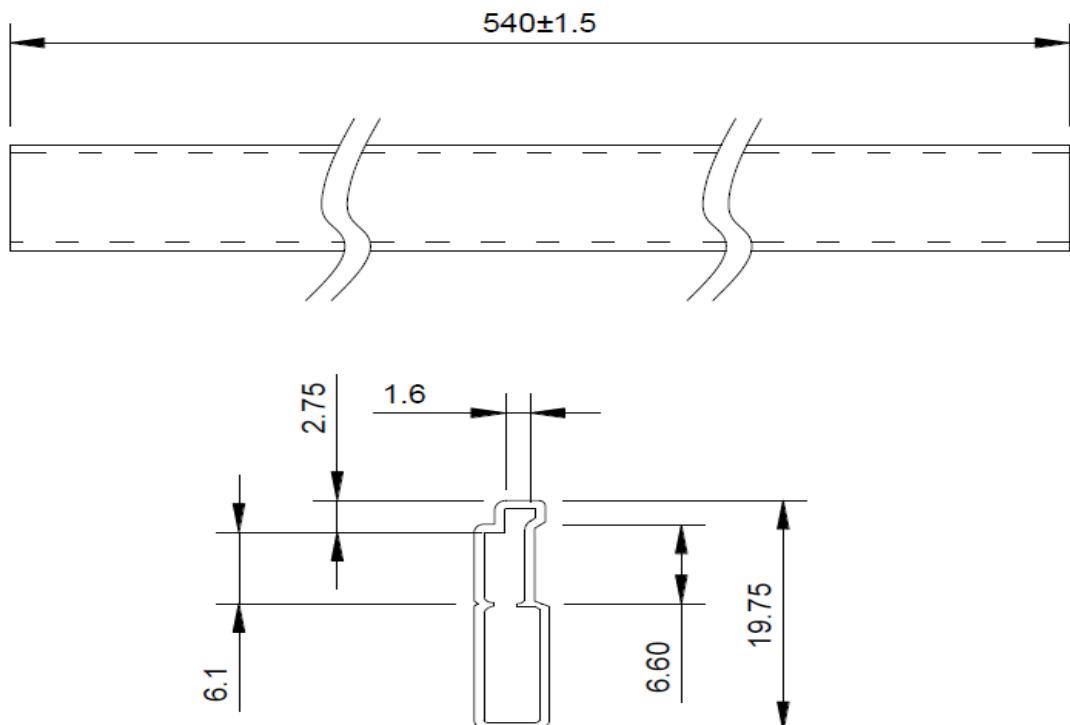
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A. Marking Information



B. Tape&Reel Information: 75pcs/Tube

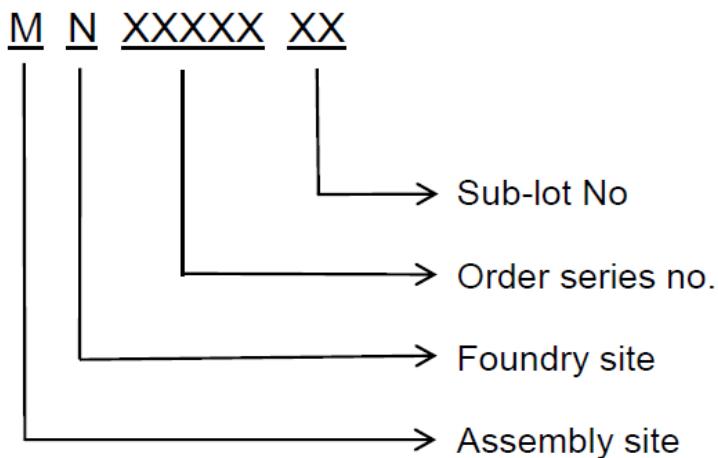


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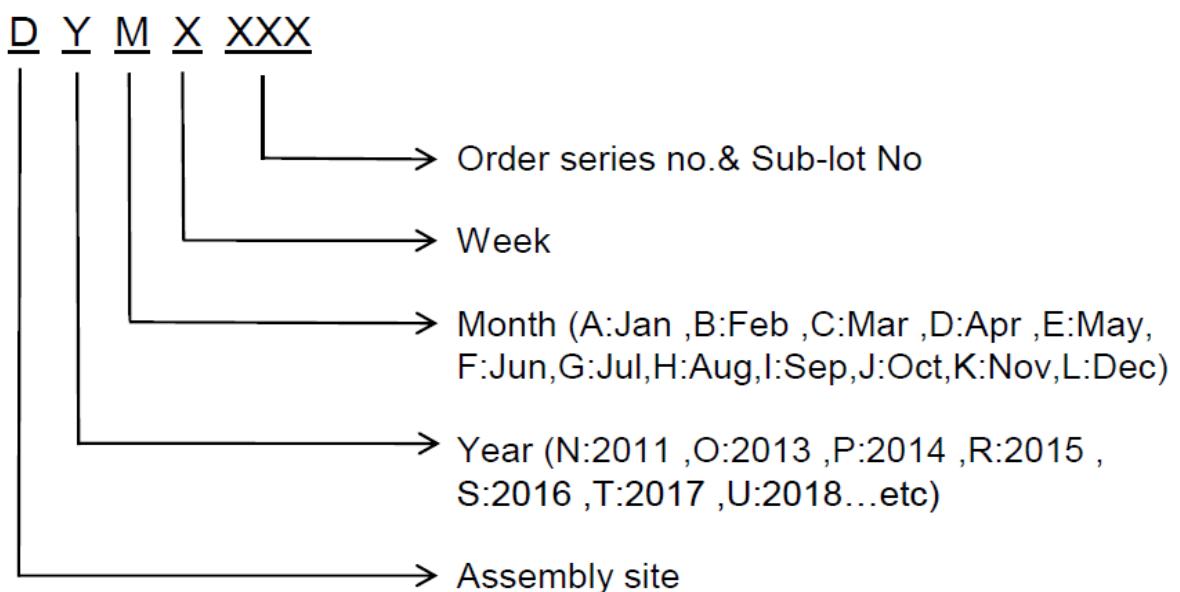
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C. Lot No.&Date Code rule

1.Lot No.



2.Date Code



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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm			
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)			
3	U-NIKC	Height: 4 mm			
4	Package	Height: 2 mm			
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12			
6	Device	Height: 3 mm (Max: 16 Digit)			
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot			
8	D/C	Height: 3 mm (Max: 7 Digit)			
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed			
10	RoHS label	 long axis: 12 mm minor axis: 6 mm bottom color: White Font color: Black Font style: Arial			
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial			
12	Scan information	Device / Lot / D/C / QTY , Insert “ / ” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least			