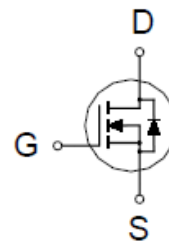


P1203BEA

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	12m Ω @ $V_{GS} = 10V$	35A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_C = 25\text{ }^\circ\text{C}$	I_D	35	A
	$T_C = 100\text{ }^\circ\text{C}$		22	
	$T_A = 25\text{ }^\circ\text{C}$		12	
	$T_A = 70\text{ }^\circ\text{C}$		10	
Pulsed Drain Current ¹		I_{DM}	95	
Avalanche Current		I_{AS}	28	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	39	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	P_D	25	W
	$T_C = 100\text{ }^\circ\text{C}$		10	
	$T_A = 25\text{ }^\circ\text{C}$		3	
	$T_A = 70\text{ }^\circ\text{C}$		2	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

P1203BEA

N-Channel Enhancement Mode MOSFET

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$t \leq 10s$	$R_{\theta JA}$		40	°C / W
Junction-to-Ambient	Steady-State	$R_{\theta JA}$		75	
Junction-to-Case	Steady-State	$R_{\theta JC}$		5	

¹Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.8	2.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			0.1	μA
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 125^\circ C$			1	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = 5V, V_{GS} = 10V$	95			A
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 11A$		13	17.5	mΩ
		$V_{GS} = 10V, I_D = 11A$		8	12	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 11A$		22		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		911		pF
Output Capacitance	C_{oss}			225		
Reverse Transfer Capacitance	C_{rss}			124		
Total Gate Charge ²	$Q_{g(VGS=10V)}$	$V_{DS} = 0.5V_{(BR)DSS}, I_D = 11A$		15.7		nC
	$Q_{g(VGS=4.5V)}$			6.8		
Gate-Source Charge ²	Q_{gs}			2.6		
Gate-Drain Charge ²	Q_{gd}			4		
Gate Resistance	R_g		$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		1.7	
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 15V, I_D \cong 11A, V_{GEN} = 10V, R_G = 6\Omega$		9.8		nS
Rise Time ²	t_r			12.2		
Turn-Off Delay Time ²	$t_{d(off)}$			9.7		
Fall Time ²	t_f			11.7		

P1203BEA

N-Channel Enhancement Mode MOSFET

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)

Continuous Current	I _S			35	A
Forward Voltage ¹	V _{SD}	I _F = 11A, V _{GS} = 0V		1.3	V
Reverse Recovery Time	t _{rr}	I _F = 11A, di _F /dt = 100A / μS		30	nS
Reverse Recovery Charge	Q _{rr}			41	nC

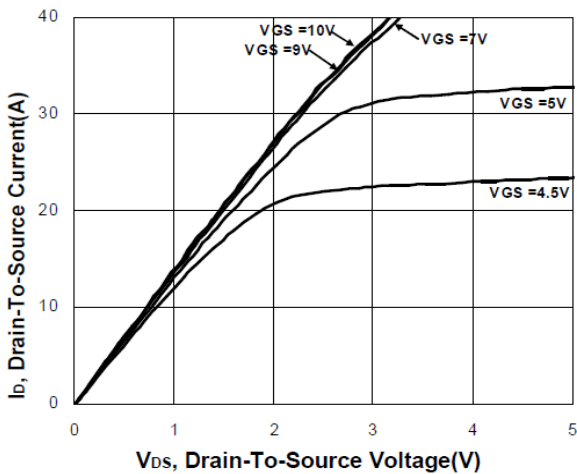
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

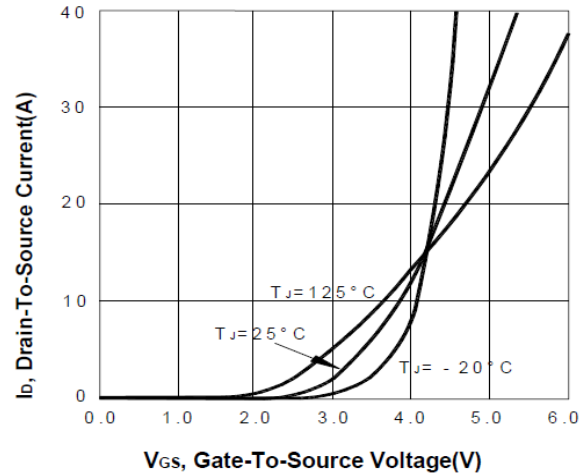
P1203BEA

N-Channel Enhancement Mode MOSFET

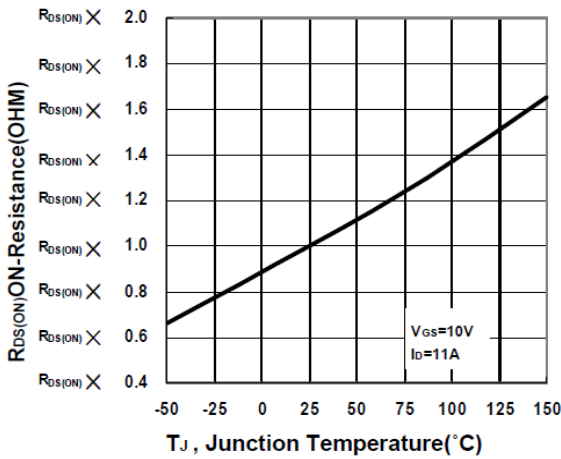
Output Characteristics



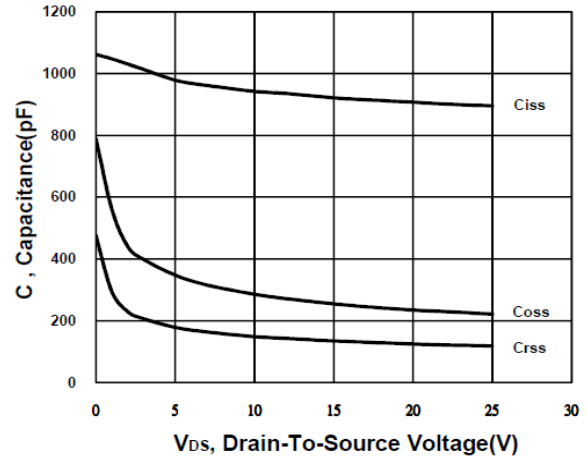
Transfer Characteristics



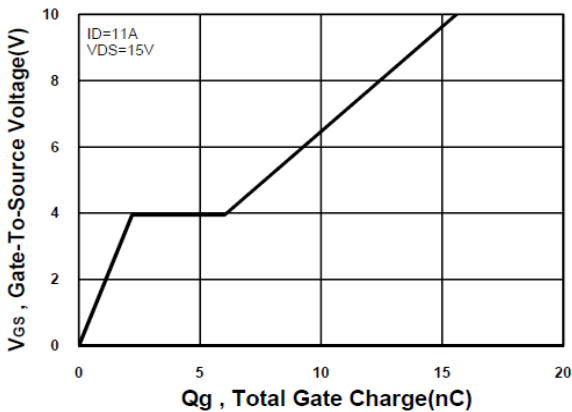
On-Resistance VS Temperature



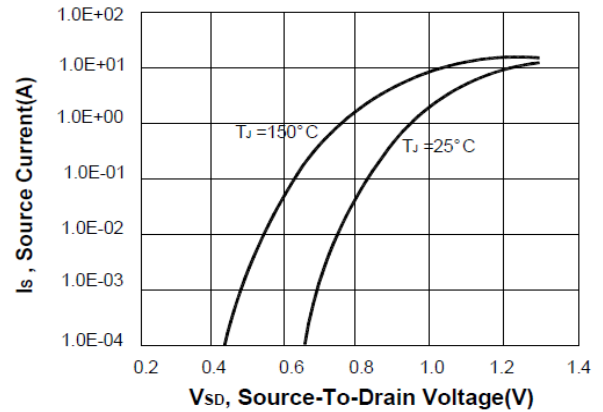
Capacitance Characteristic



Gate charge Characteristics



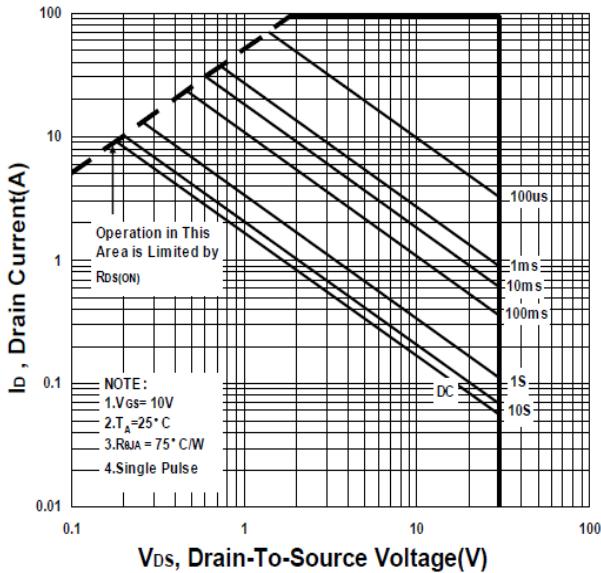
Source-Drain Diode Forward Voltage



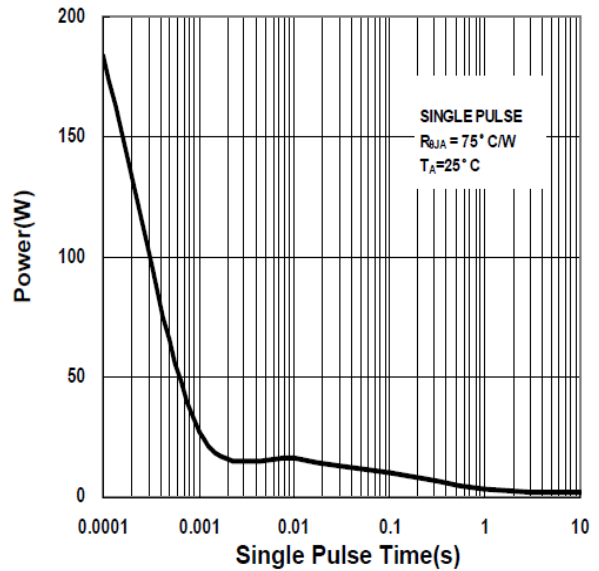
P1203BEA

N-Channel Enhancement Mode MOSFET

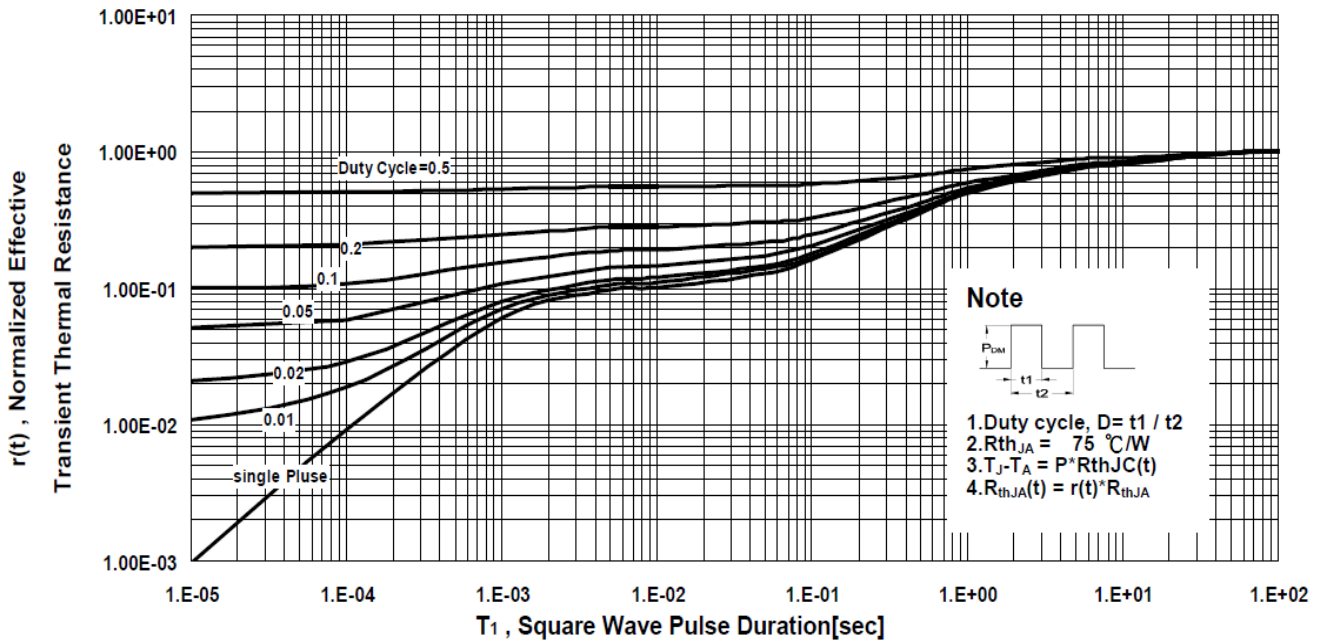
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



P1203BEA

N-Channel Enhancement Mode MOSFET

Package Dimension

PDFN 3x3P MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	3		3.6	I	0.7		1.12
B	2.88		3.2	J	0.1		0.33
C	2.9		3.2	K	0.6		
D	1.98		2.69	L	0°	10°	12°
E	3		3.6	M	0.14		0.41
F	0		0.455	N	0.6		0.7
G	1.47		2.2	O	0.12		0.36
H	0.15		0.56	P	0		0.2

