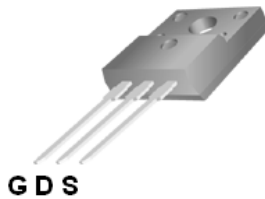


# P1260ETF / P1260ETFS

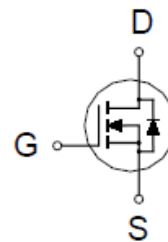
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
600V	670mΩ @ $V_{GS} = 10V$	12A



TO-220F  
TO-220FS



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	600	V
Gate-Source Voltage		$V_{GS}$	±30	
Continuous Drain Current <sup>2</sup>	$T_C = 25\text{ °C}$	$I_D$	12	A
	$T_C = 100\text{ °C}$		7.6	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	48	
Avalanche Current <sup>3</sup>		$I_{AS}$	7.3	
Avalanche Energy <sup>3</sup>		$E_{AS}$	264	mJ
Power Dissipation	$T_C = 25\text{ °C}$	$P_D$	48	W
	$T_C = 100\text{ °C}$		19	
Operating Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.6	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Ensure that the channel temperature does not exceed 150°C.

<sup>3</sup> $V_{DD} = 50V, L = 10mH, \text{ starting } T_J = 25\text{ °C}$

# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	600			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	2.8	4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±30V			±100	nA
Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V, T <sub>C</sub> = 25 °C			1	μA
		V <sub>DS</sub> = 480V, V <sub>GS</sub> = 0V, T <sub>C</sub> = 100 °C			10	
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A		518	670	mΩ
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 6A		15		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz		2023		pF
Output Capacitance	C <sub>oss</sub>			172		
Reverse Transfer Capacitance	C <sub>rss</sub>			9		
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	V <sub>DD</sub> = 480V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 12A		44		nC
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>			9		
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>			14		
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 300V, I <sub>D</sub> = 12A, R <sub>G</sub> = 25Ω		41		nS
Rise Time <sup>2</sup>	t <sub>r</sub>			54		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>			169		
Fall Time <sup>2</sup>	t <sub>f</sub>			93		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current <sup>3</sup>	I <sub>S</sub>				12	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 12A, V <sub>GS</sub> = 0V			1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 12A, dI <sub>F</sub> /dt = 100A / μS		449		nS
Reverse Recovery Charge	Q <sub>rr</sub>				6	uC

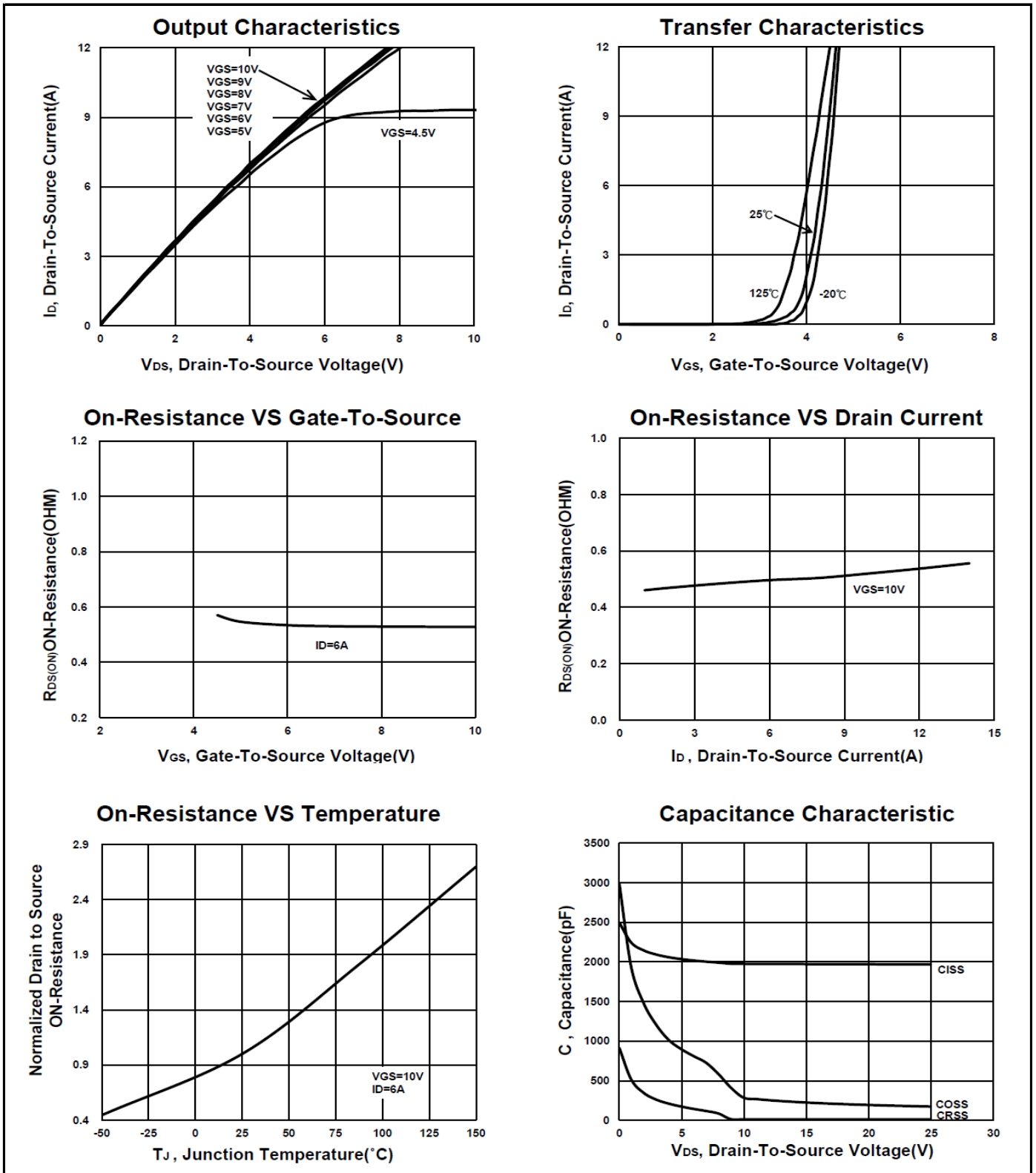
<sup>1</sup>Pulse test : Pulse Width ≤ 380 μsec, Duty Cycle ≤ 2%.

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

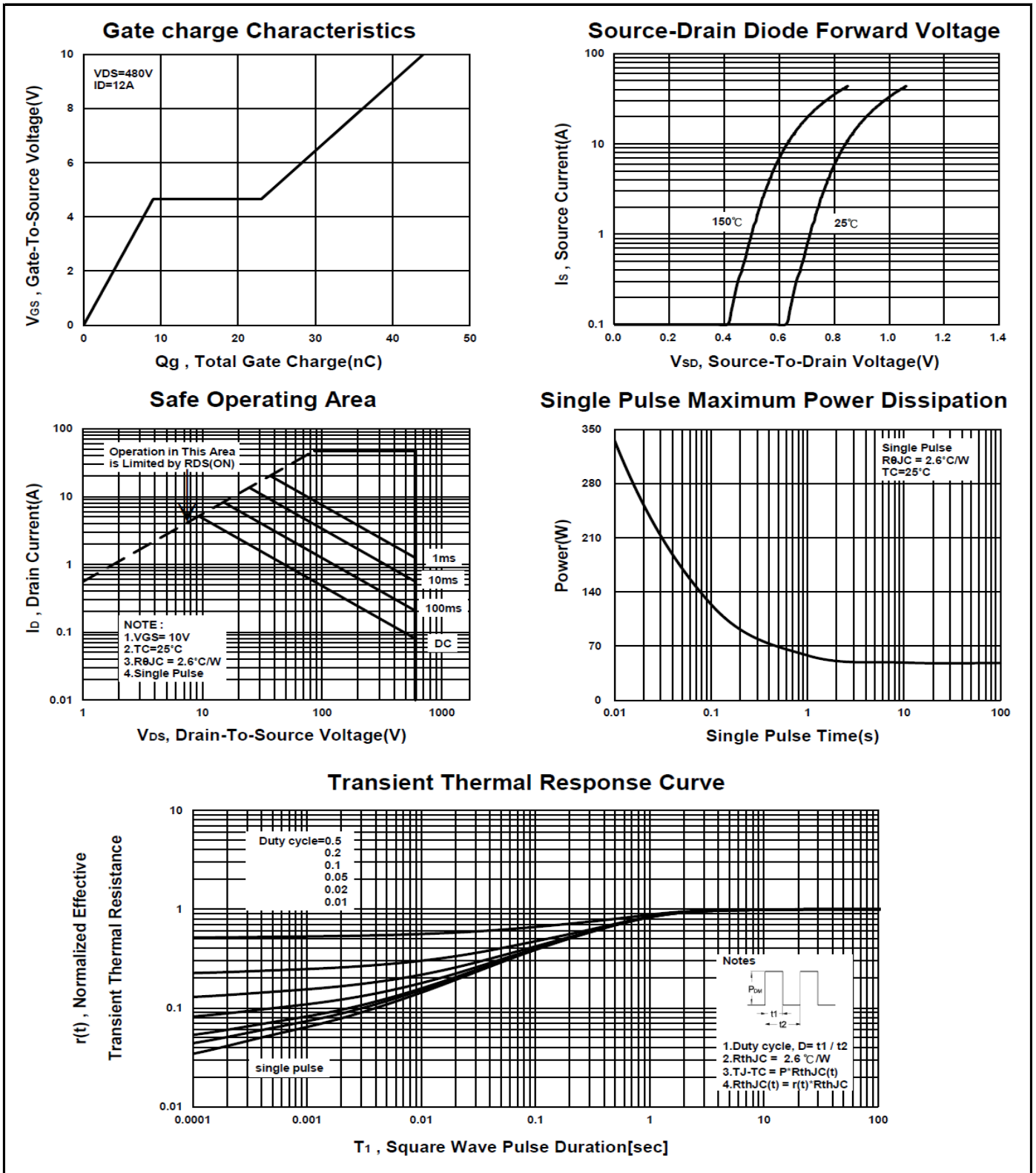
# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET



# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET



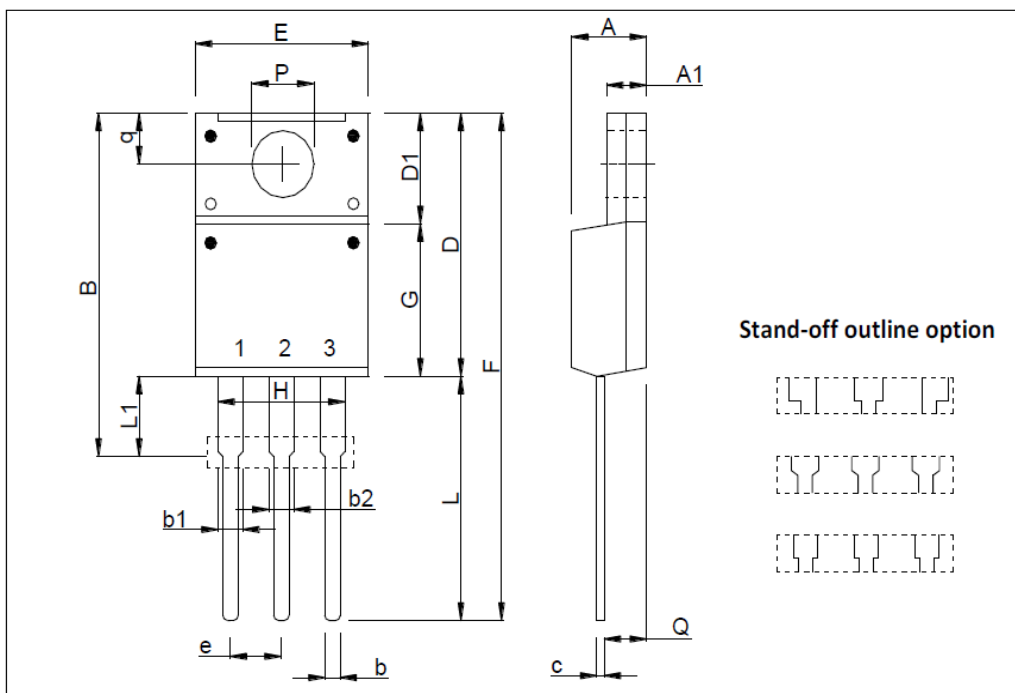
# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-220F (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.4		4.93	e	2.34		2.74
A1	2.34		3.1	F	27.2		30.6
B	18.8		20	G	7.7		9.39
b	0.65		1	H	6.18		6.82
b1	0.93		1.6	L	12.7		14.2
b2	0.95		1.6	L1	2.88		3.7
c	0.4		1	P	2.98		3.7
D	13.5		16.4	Q	2.3		2.96
D1	6.48		6.95	q	3.1		3.8
E	9.8		10.4				



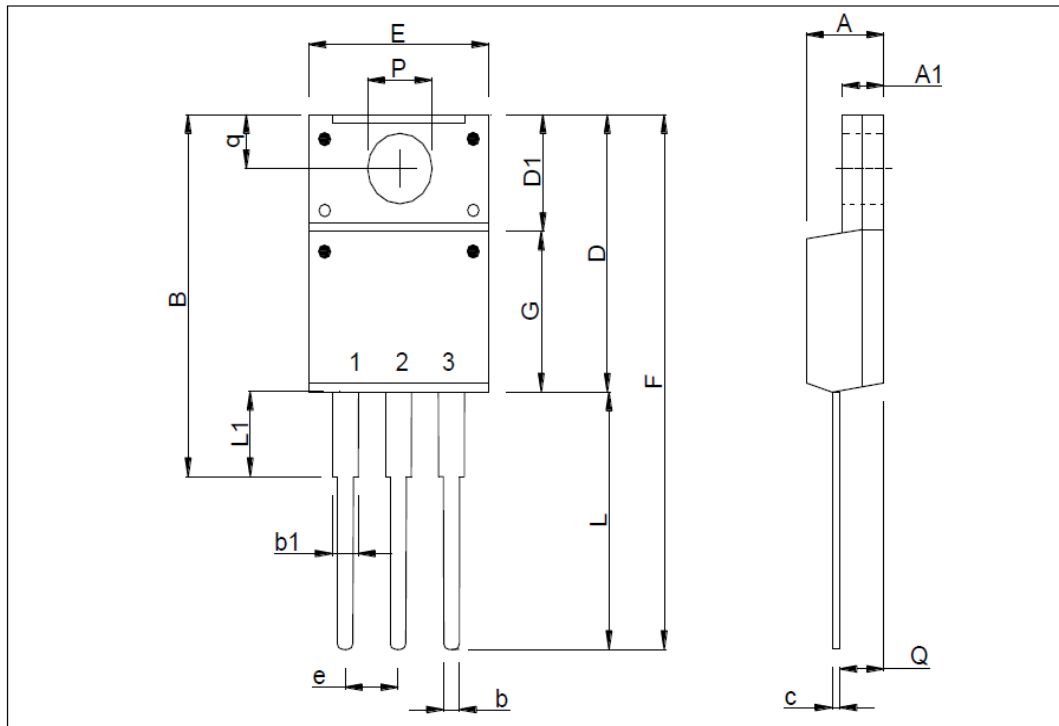
# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-220FS (3-Lead) MECHANICAL DATA

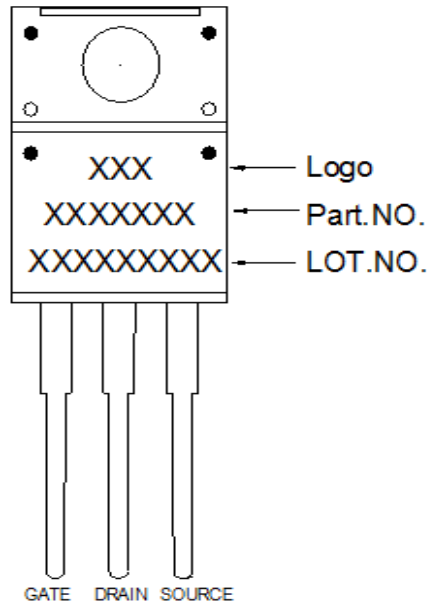
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2	4.7	4.93	e	2.05	2.54	3.05
A1	2.34	2.745	3.15	F	28.00		30.3
B	16.82		20.3	G	8.2	8.87	9.57
b	0.5	0.775	1.05	L	12.37		14.3
b1	0.8	1.15	1.5	L1	1.4	2.3	2.5
c	0.4	0.7	1.0	P	2.98	3.24	3.5
D	14.80		16.3	Q	2.1	2.6	2.96
D1	5.5		7.5	q	2.7	3.25	3.8
E	9.7	10.16	10.36				



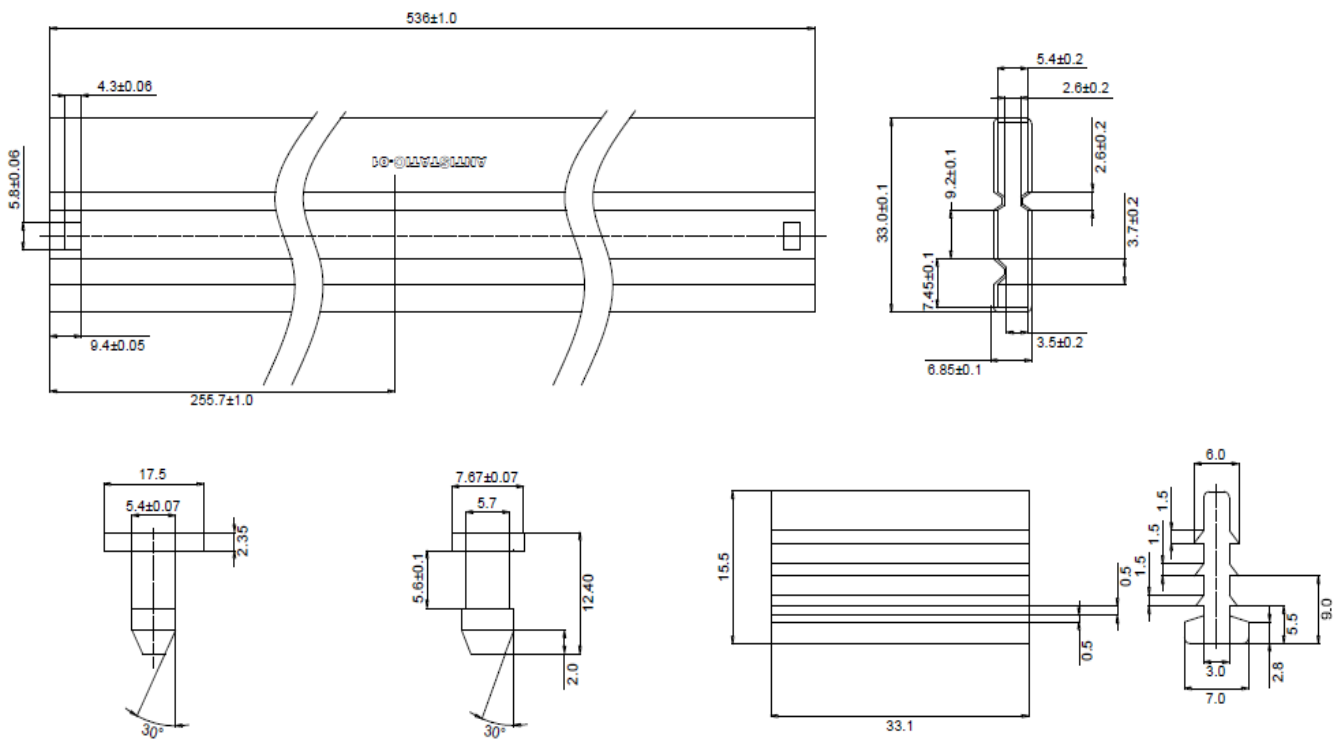
# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET

### A. Marking Information



### B. Tape&Reel Information:50pcs/Tube(2000pcs/Box)

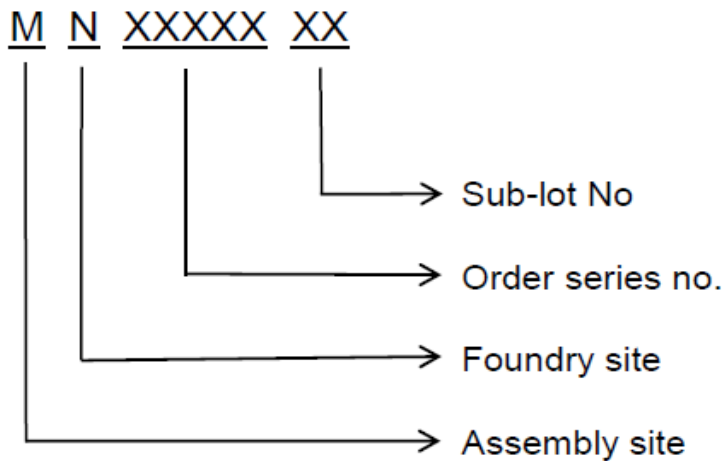


# P1260ETF / P1260ETFS

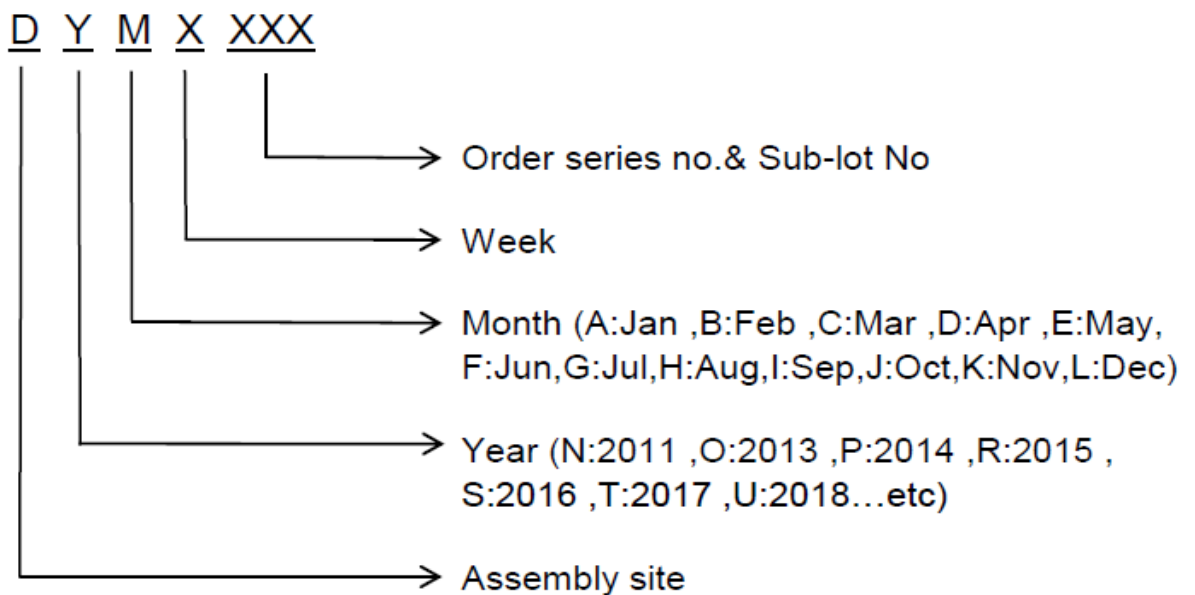
## N-Channel Enhancement Mode MOSFET

### C. Lot No.&Date Code rule

#### 1.Lot No.



#### 2.Date Code







# P1260ETF / P1260ETFS

## N-Channel Enhancement Mode MOSFET

### D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文”0”和数字”0”，”G和”Q”的字型即可)
3	U-NIKC	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	RoHS label	 long axis: 12 mm minor axis:6 mm bottom color: White Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial
12	Scan information	Device / Lot / D/C / QTY , Insert “ / “ between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least