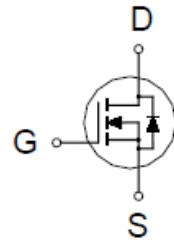
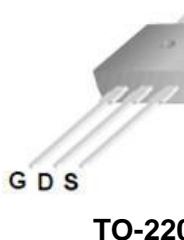


P1350AT

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
500V	0.52Ω @ $V_{GS} = 10V$	13A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current ²	I_D	13	A
$T_C = 100^\circ C$		10	
Pulsed Drain Current ^{1,2}	I_{DM}	45	
Avalanche Current ³	I_{AS}	8.8	
Avalanche Energy ³	E_{AS}	387	mJ
Power Dissipation ^A	P_D	192	W
$T_C = 100^\circ C$		77	
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		0.65	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Limited only by maximum temperature allowed.

³ $V_{DD} = 50V$, starting $T_J = 25^\circ C$

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	500			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.5		4.5	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 30\text{V}$			± 100	nA
Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 500\text{V}, V_{\text{GS}} = 0\text{V}, T_C = 25^\circ\text{C}$			25	μA
		$V_{\text{DS}} = 500\text{V}, V_{\text{GS}} = 0\text{V}, T_C = 100^\circ\text{C}$			250	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 6.5\text{A}$		0.4	0.52	Ω
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 40\text{V}, I_D = 6.5\text{A}$		8.5		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		2185		pF
Output Capacitance	C_{oss}			204		
Reverse Transfer Capacitance	C_{rss}			59		
Total Gate Charge ²	Q_g	$V_{\text{DD}} = 250\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 6\text{A}$		40		nC
Gate-Source Charge ²	Q_{gs}			11.5		
Gate-Drain Charge ²	Q_{gd}			12.5		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 250\text{V}, I_D = 6\text{A}, R_G = 4.7\Omega$		30		nS
Rise Time ²	t_r			25		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			43		
Fall Time ²	t_f			15		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current ³	I_S				10	A
Forward Voltage ¹	V_{SD}	$I_F = 4\text{A}, V_{\text{GS}} = 0\text{V}$			1.7	V
Reverse Recovery Time	t_{rr}	$I_F = 12\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}, V_{\text{GS}} = 0\text{V}$		320		nS
Reverse Recovery Charge	Q_{rr}			4.9		nC

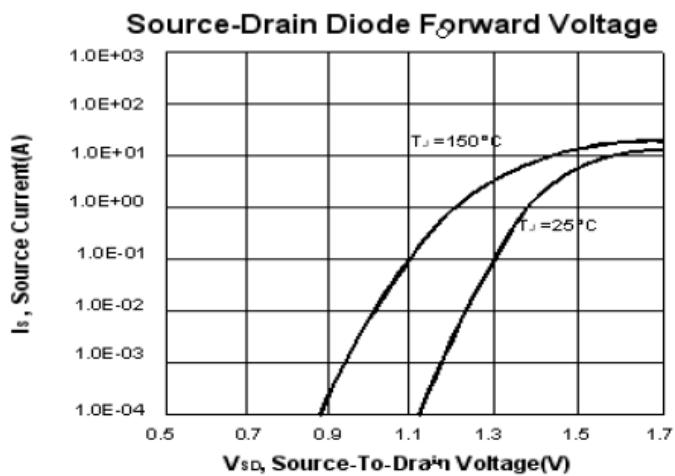
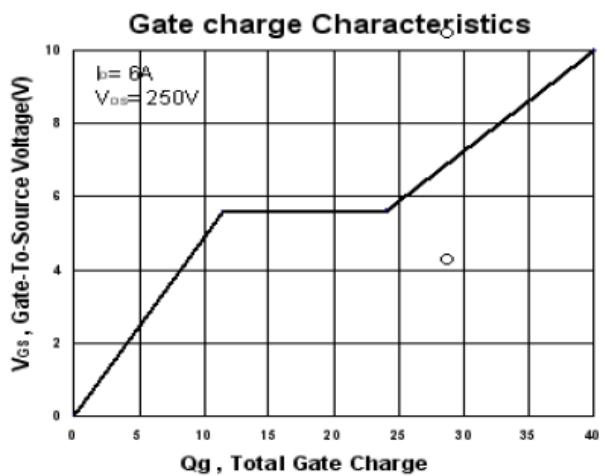
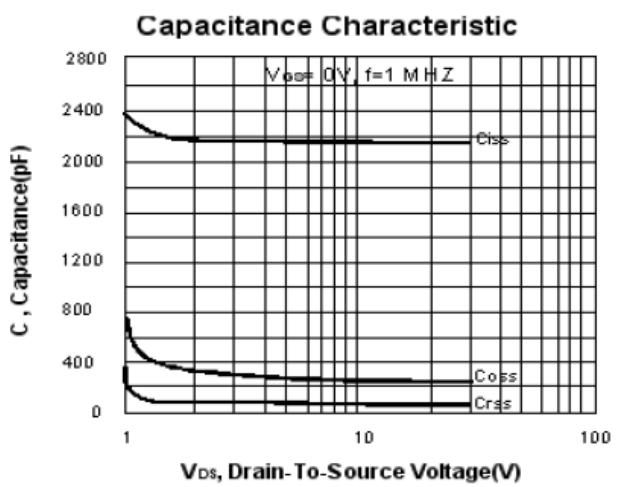
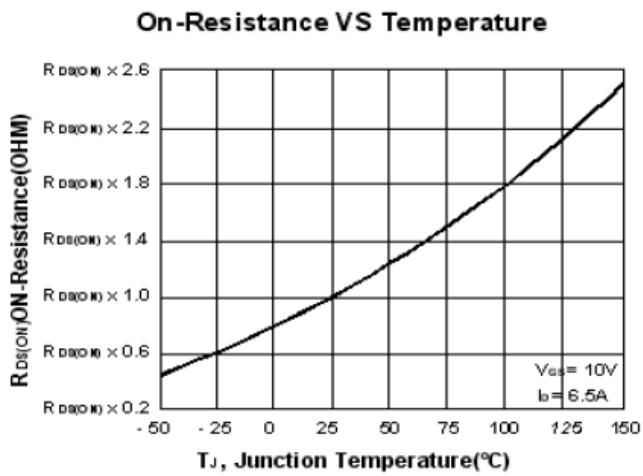
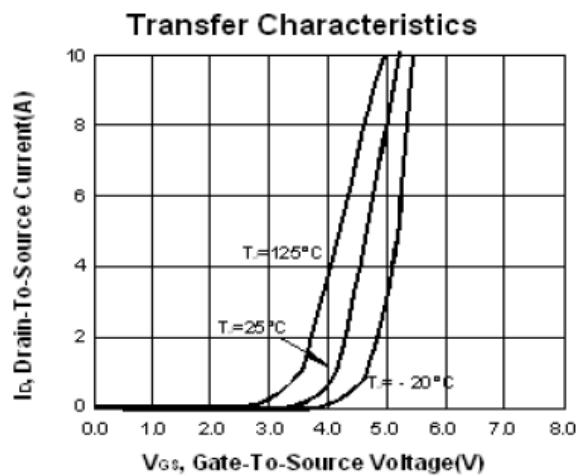
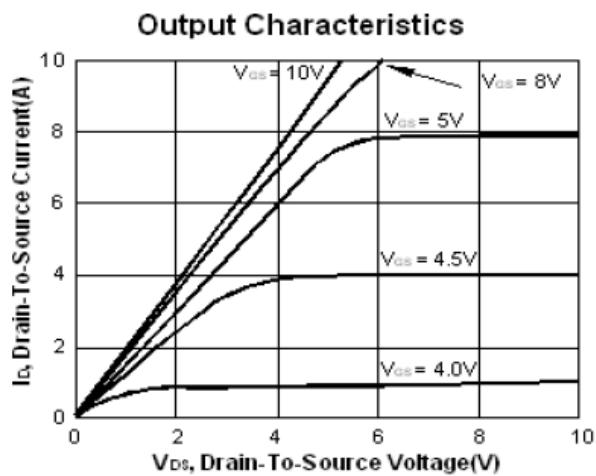
¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

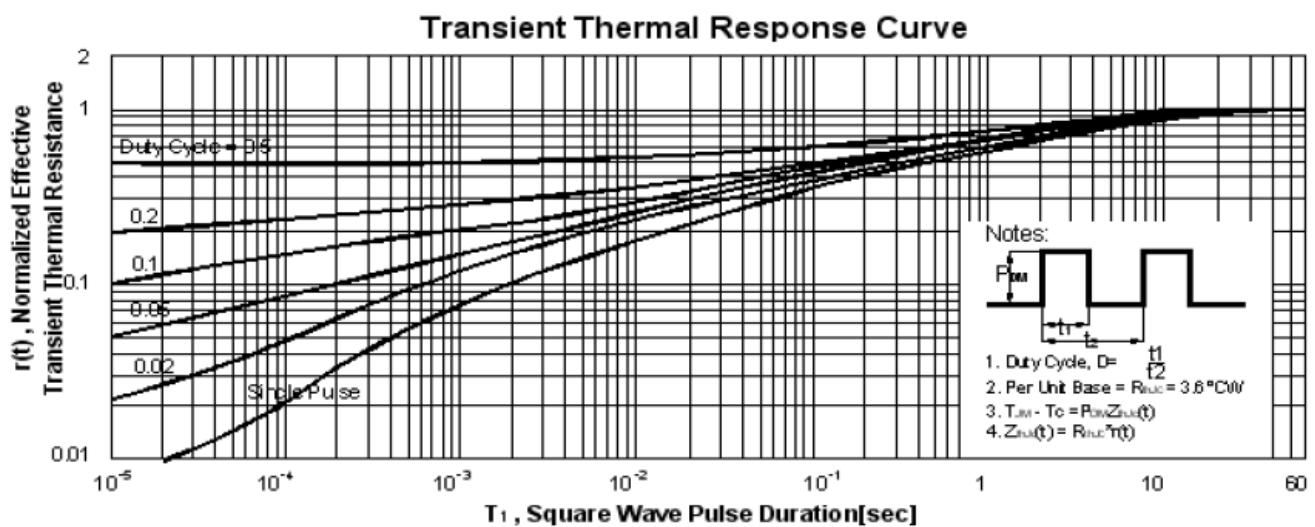
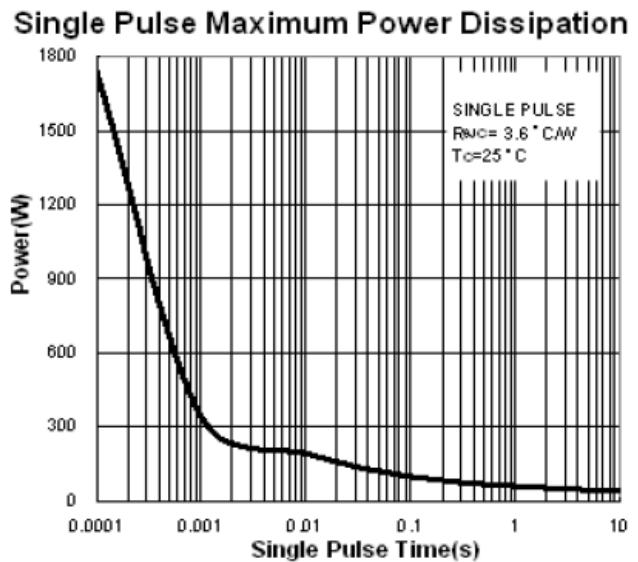
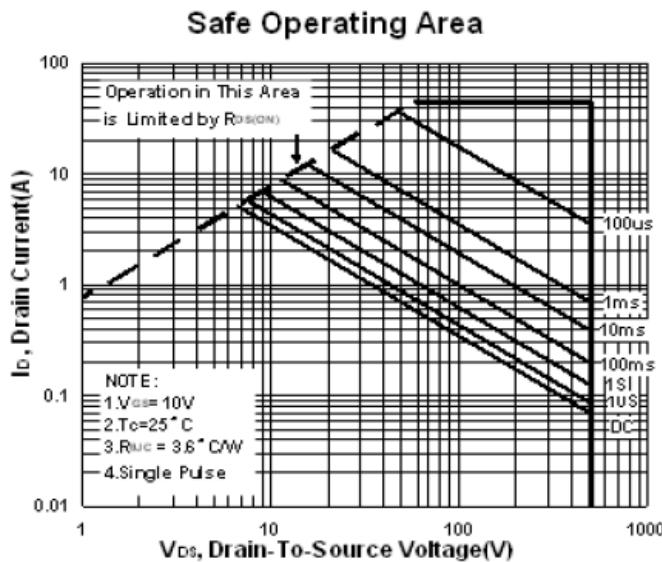
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Package Dimension

TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

