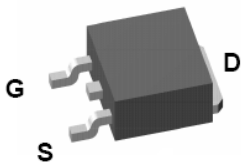


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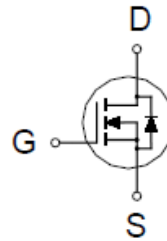
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	14m Ω @ $V_{GS} = 4.5V$	45A



TO-252



ABSOLUTE MAXIMUM RATINGS ($T_C = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 12	V
Continuous Drain Current	$T_C = 25\text{ }^\circ\text{C}$	I_D	45	A
	$T_C = 100\text{ }^\circ\text{C}$		19	
Pulsed Drain Current ¹		I_{DM}	140	
Avalanche Current		I_{AS}	33	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	52	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	P_D	48	W
	$T_C = 100\text{ }^\circ\text{C}$		19	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.6	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		110	

¹Pulse width limited by maximum junction temperature.

²Duty cycle $\leq 1\%$

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ELECTRICAL CHARACTERISTICS (T_C = 25 °C, Unless Otherwise Noted)

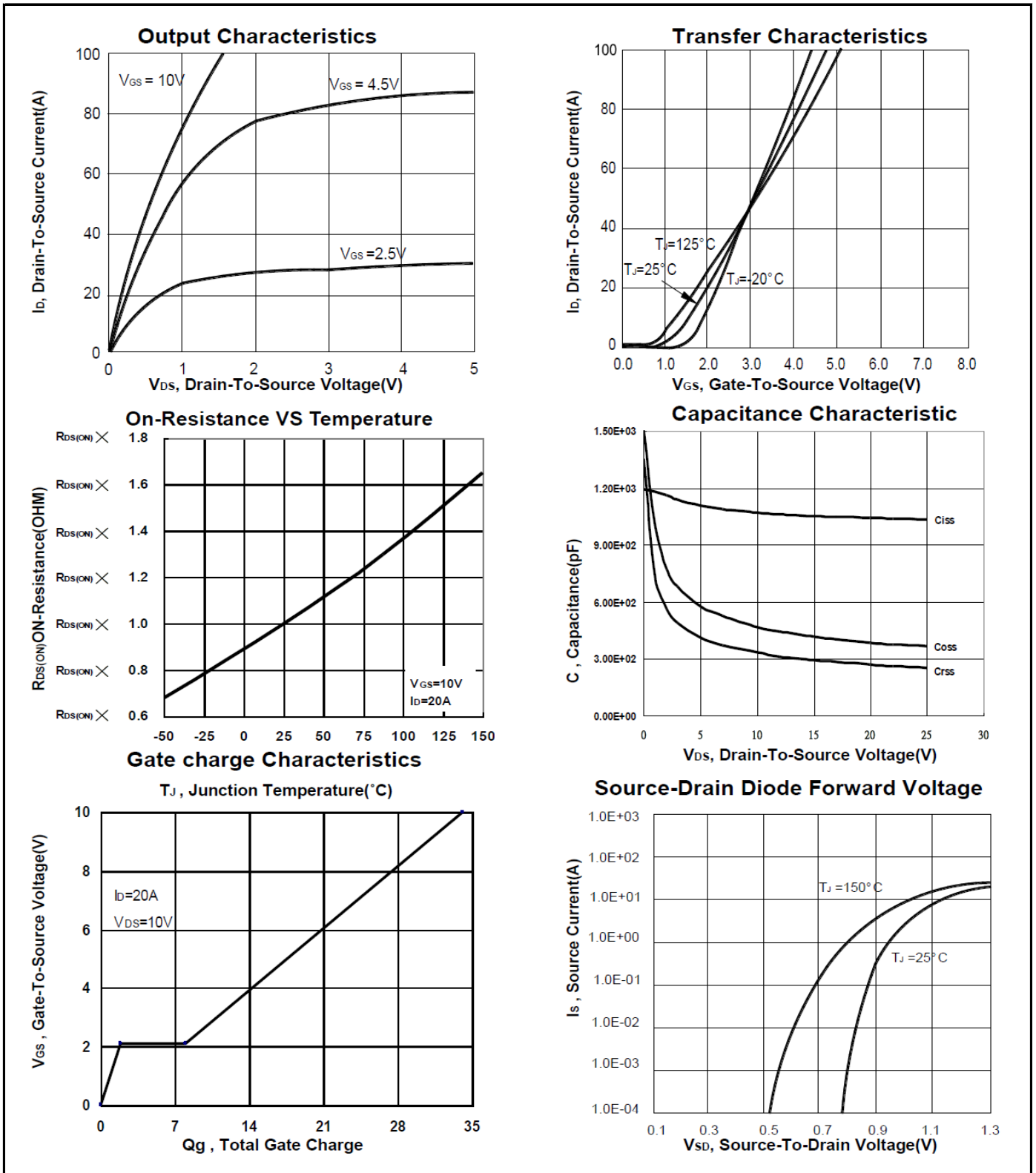
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.45	0.63	1.25	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16V, V _{GS} = 0V			1	μA
		V _{DS} = 13.2V, V _{GS} = 0V, T _J = 125 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 4.5V	140			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 2.5V, I _D = 9A		12.5	28	mΩ
		V _{GS} = 4.5V, I _D = 18A		9.3	14	
		V _{GS} = 10V, I _D = 20A		7.9	12	
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 20A		26		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 10V, f = 1MHz		1030		pF
Output Capacitance	C _{oss}			364		
Reverse Transfer Capacitance	C _{rss}			253		
Total Gate Charge ²	Q _g	V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = 5V, I _D = 18A		34		nC
Gate-Source Charge ²	Q _{gs}			2		
Gate-Drain Charge ²	Q _{gd}			6		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 10V, I _D ≅ 18A, V _{GS} = 5V, R _{GS} = 3.3Ω		7.5		nS
Rise Time ²	t _r			83		
Turn-Off Delay Time ²	t _{d(off)}			18		
Fall Time ²	t _f			23		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_C = 25 °C)						
Continuous Current	I _S				22	A
Forward Voltage ¹	V _{SD}	I _F = I _S , V _{GS} = 0V			1.3	V
Reverse Recovery Time	t _{rr}			37		nS
Reverse Recovery Charge	Q _{rr}			43		nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

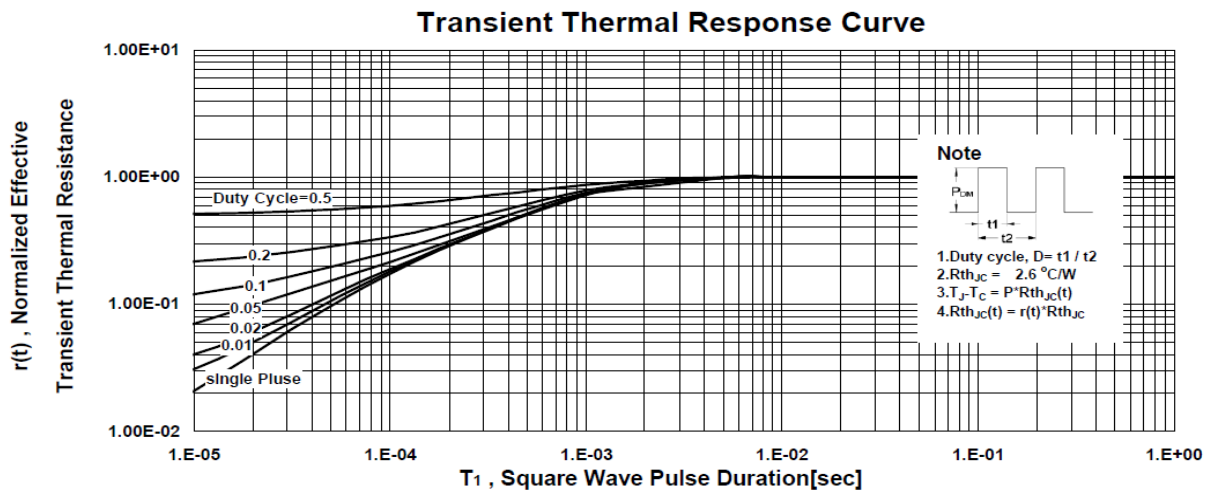
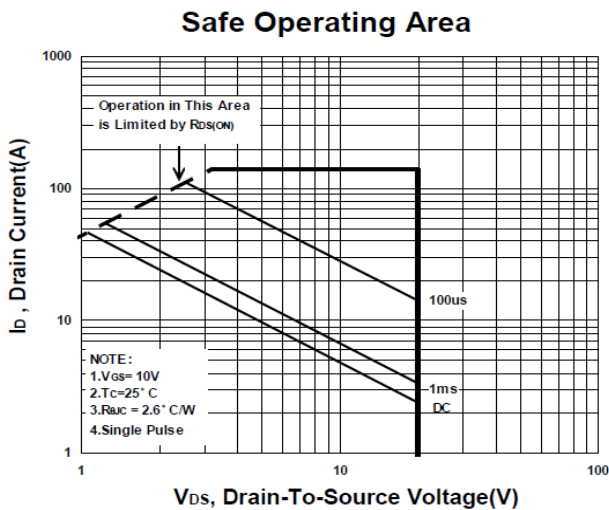
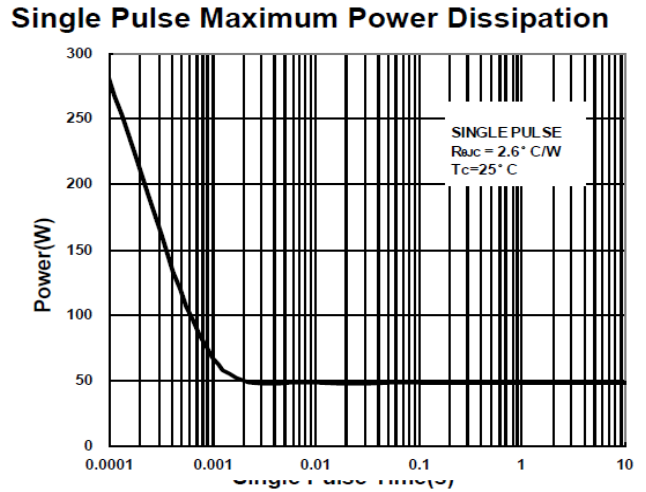
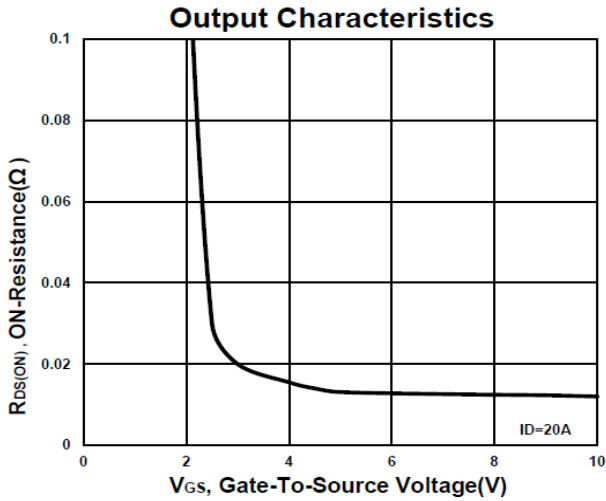
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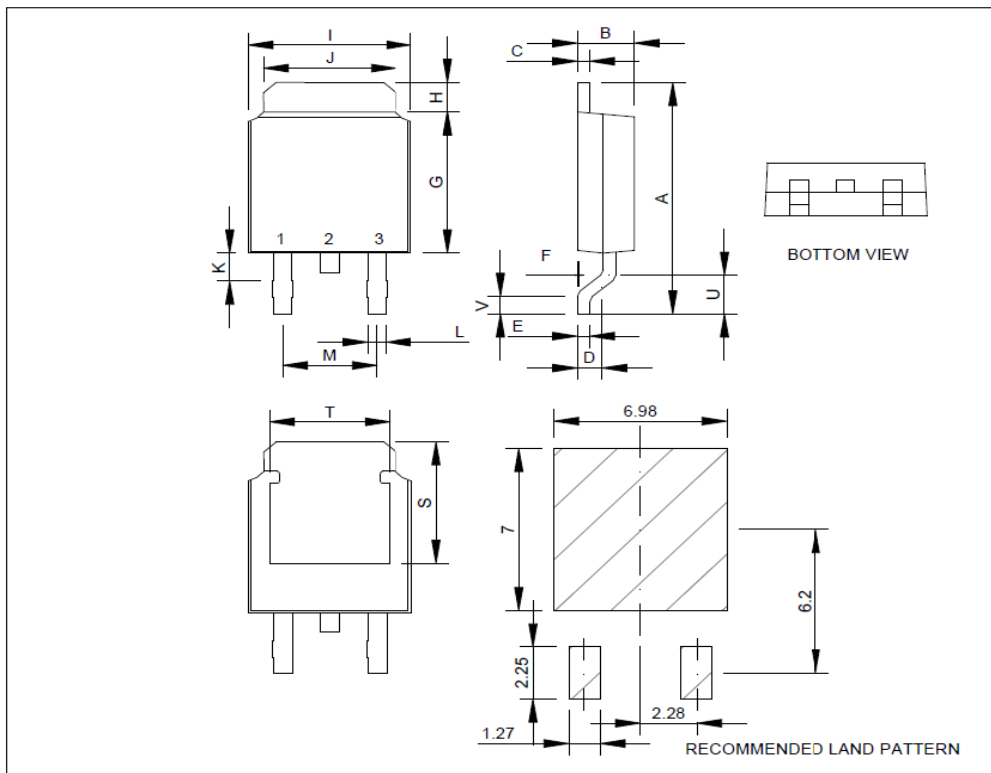
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N-Channel Enhancement Mode MOSFET

Package Dimension

TO-252 (DPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	10	10.41	J	4.8		5.64
B	2.1	2.2	2.4	K	0.15		1.1
C	0.4	0.5	0.61	L	0.4	0.76	0.89
D	0.82	1.2	1.5	M	4.2	4.58	5
E	0.4	0.5	0.61	S	4.9	5.1	5.3
F	0		0.2	T	4.6	4.75	5.44
G	5.3	6.1	6.3	U	1.4		1.78
H	0.9		1.7	V	0.55	1.25	1.7
I	6.3	6.5	6.8				



*因为各家封装模具不同而外观略有所差异，不影响电性及Layout。