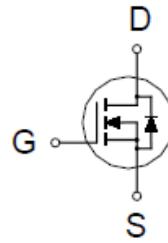
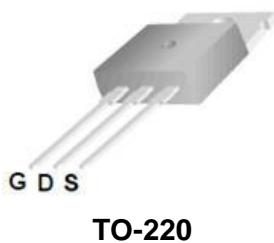


## P1615ATA

### N-Channel Enhancement Mode MOSFET

#### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
150V	16.5mΩ @ $V_{GS} = 10V$	68A



#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	150	V
Gate-Source Voltage		$V_{GS}$	$\pm 25$	
Continuous Drain Current	$T_C = 25^\circ C$	$I_D$	68	A
	$T_C = 100^\circ C$		43	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	270	A
Avalanche Current		$I_{AS}$	30	
Avalanche Energy	$L = 1mH$	$E_{AS}$	435	mJ
Power Dissipation	$T_C = 25^\circ C$	$P_D$	192	W
	$T_C = 100^\circ C$		77	
Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	°C

#### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		0.65	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.

## P1615ATA

### N-Channel Enhancement Mode MOSFET

#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

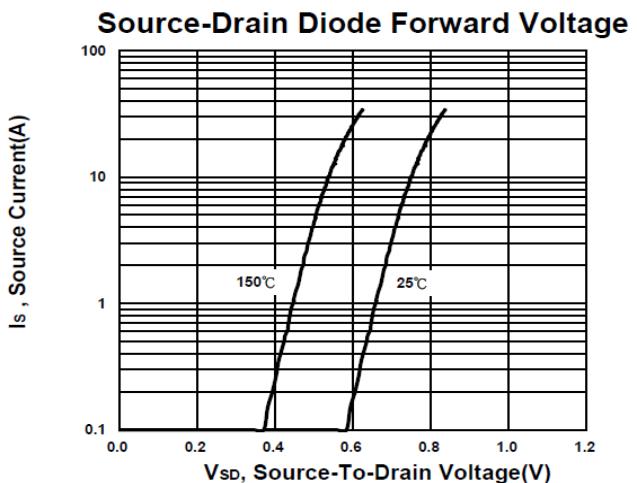
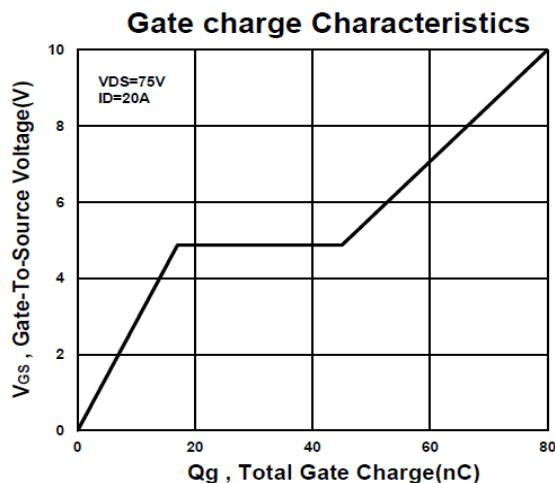
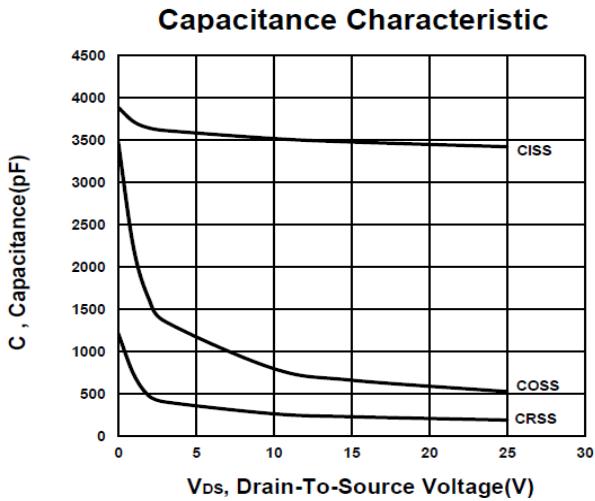
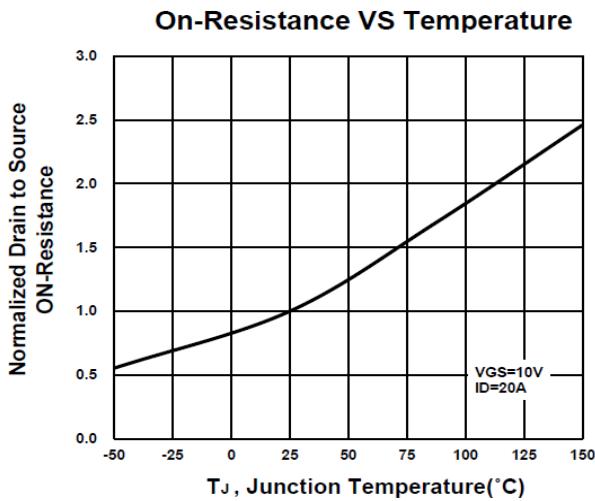
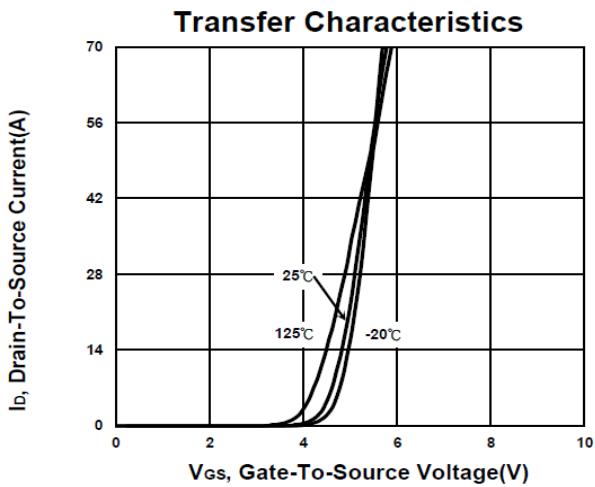
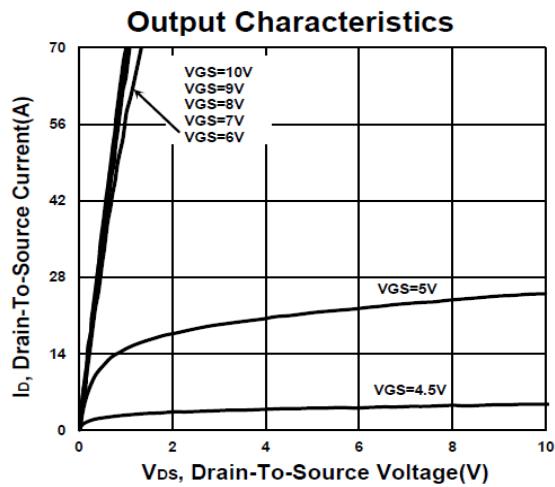
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	150			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.5	3.5	4.5	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 25\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 120\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 7\text{V}, I_D = 20\text{A}$		14	18.5	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		13	16.5	
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 10\text{V}, I_D = 20\text{A}$		34		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		3452		pF
Output Capacitance	$C_{\text{oss}}$			530		
Reverse Transfer Capacitance	$C_{\text{rss}}$			209		
Gate Resistance	$R_g$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		1.4		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 75\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}$		80		nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			17		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			28		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d(on)}}$	$V_{\text{DS}} = 75\text{V}, I_D \geq 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		23		nS
Rise Time <sup>2</sup>	$t_r$			56		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d(off)}}$			55		
Fall Time <sup>2</sup>	$t_f$			67		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$				68	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 20\text{A}, dI_{\text{SD}}/dt = 100\text{A}/\mu\text{s}$		80		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			225		nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

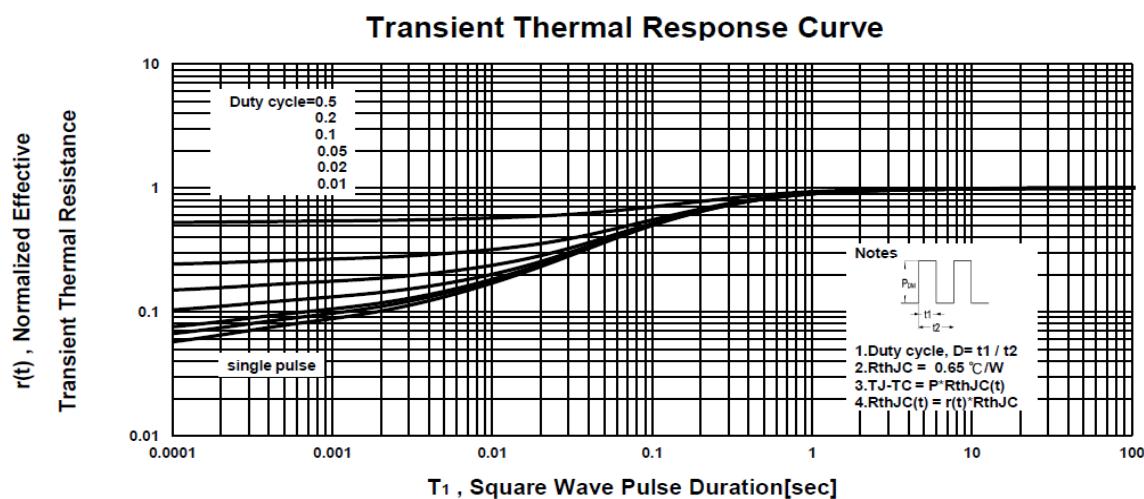
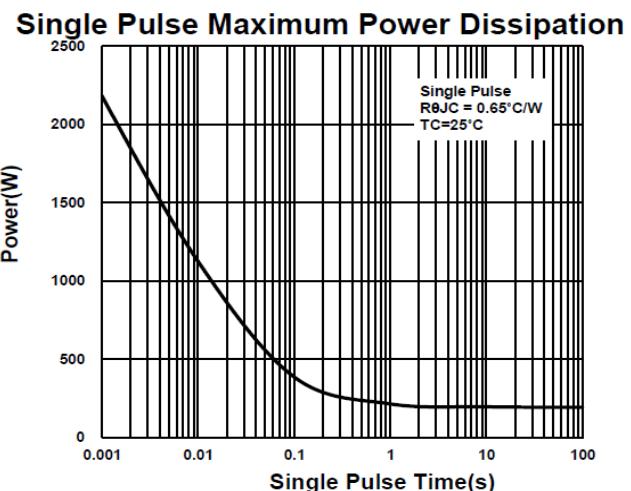
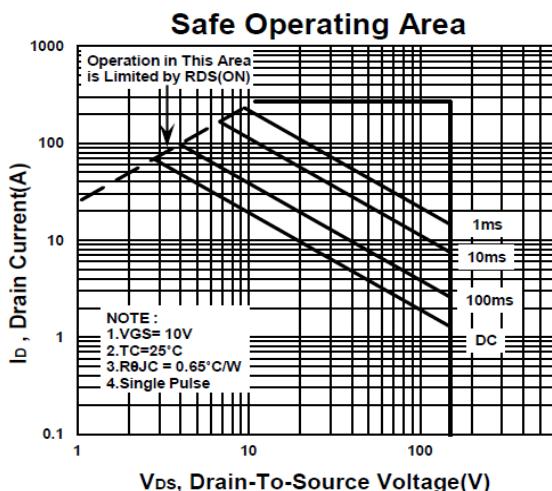
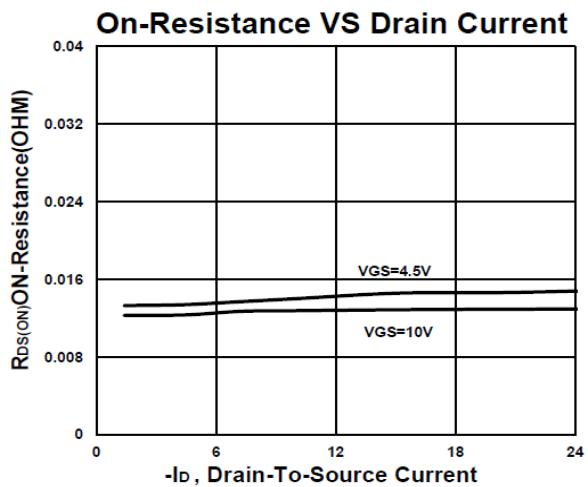
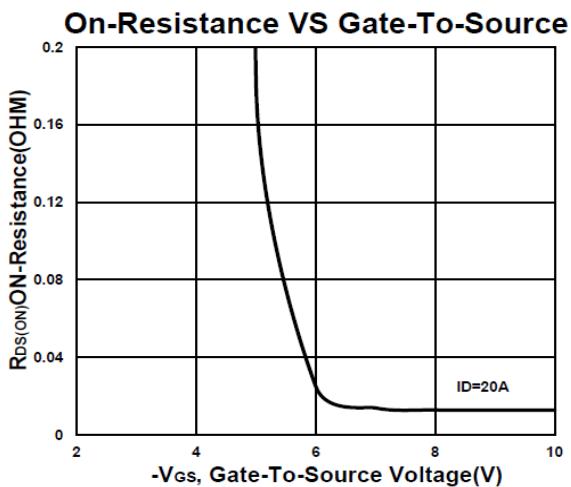
# P1615ATA

## N-Channel Enhancement Mode MOSFET



## P1615ATA

### N-Channel Enhancement Mode MOSFET



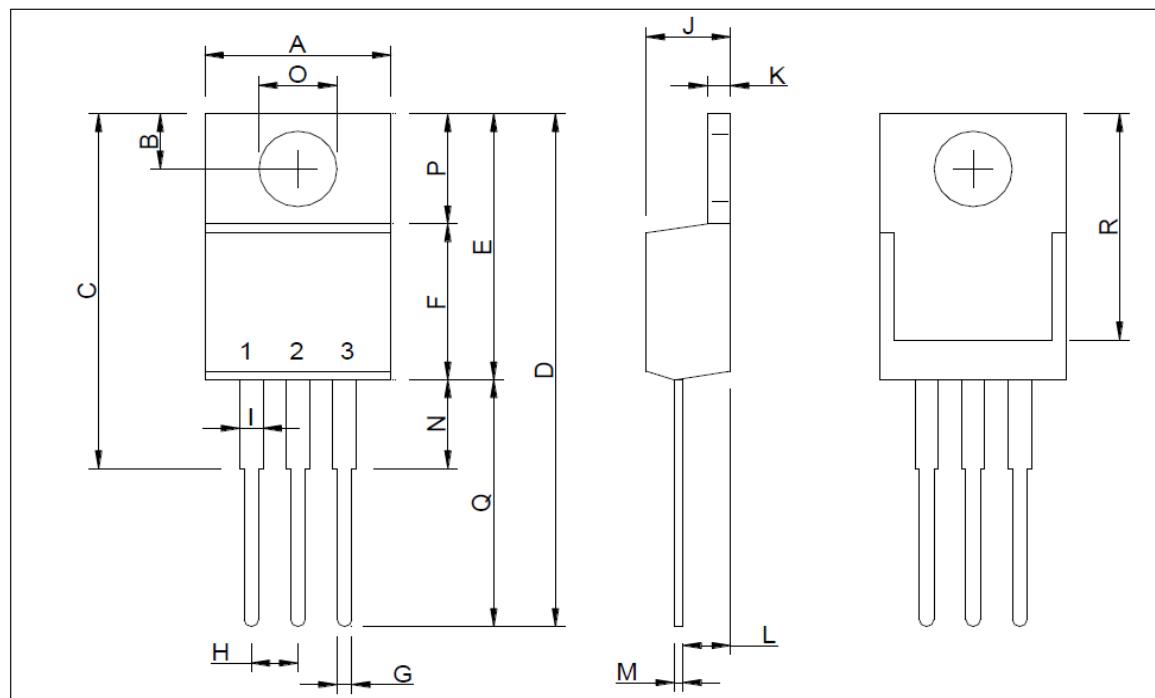
## P1615ATA

### N-Channel Enhancement Mode MOSFET

#### Package Dimension

#### TO-220 (3-Lead) MECHANICAL DATA

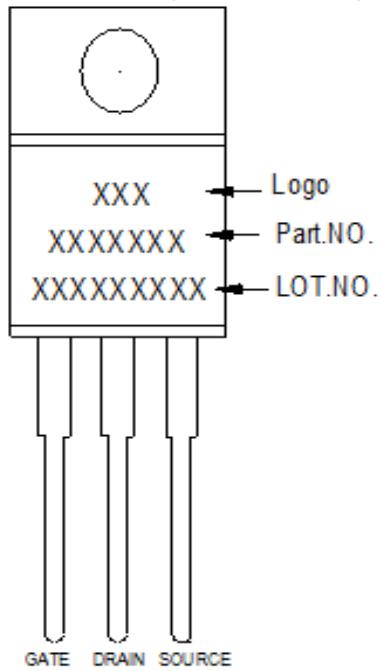
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.65		11.5	J	3.55		4.83
B		2.54		K	1.11		1.45
C	18.1		22.86	L	1.89		3.09
D	26.9		31.24	M	0.34		0.61
E	14.32		16.51	N	2.6		4.06
F	8.38		9.3	O		3.7	
G	0.38		1.02	P	5.84		6.85
H	2.04	2.54	3.04	Q	12.5		14.73
I	1.14		1.8	R	11.3		13.31



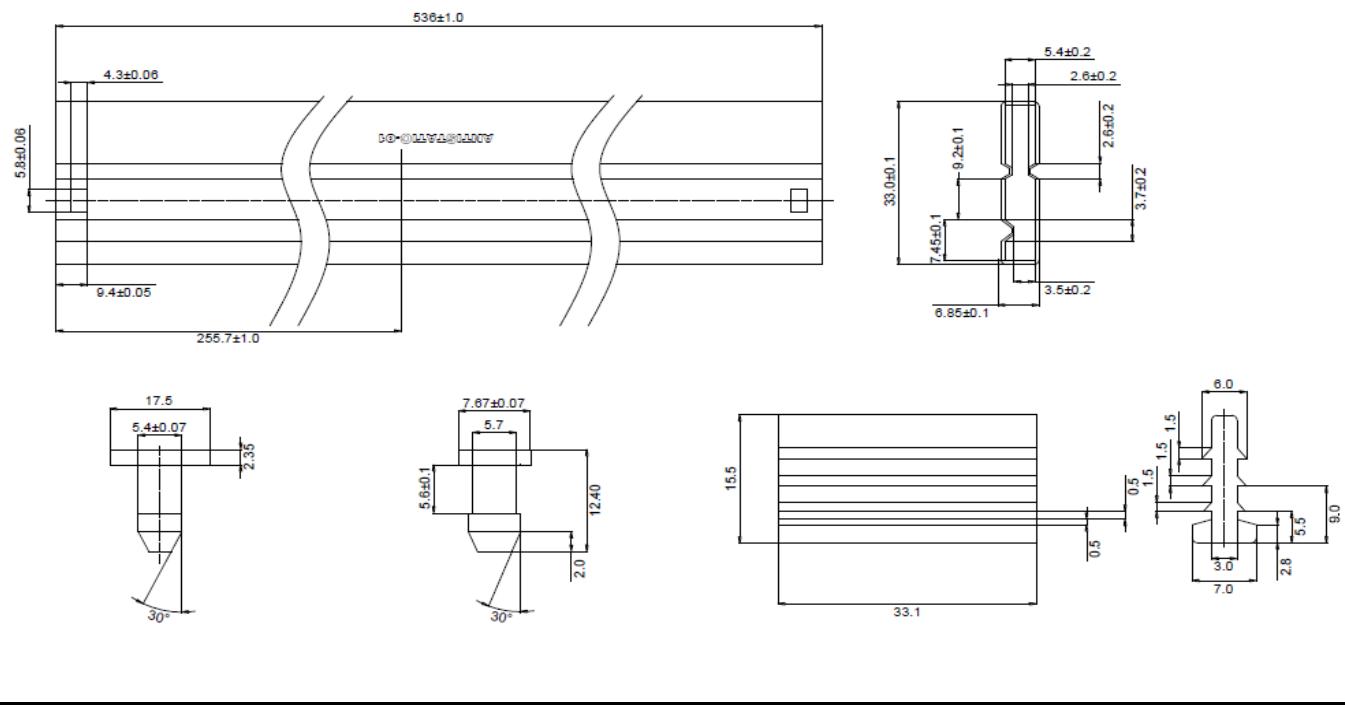
## P1615ATA

### N-Channel Enhancement Mode MOSFET

#### A. Marking Information



#### B. Tape&Reel Information: 50pcs/Tube

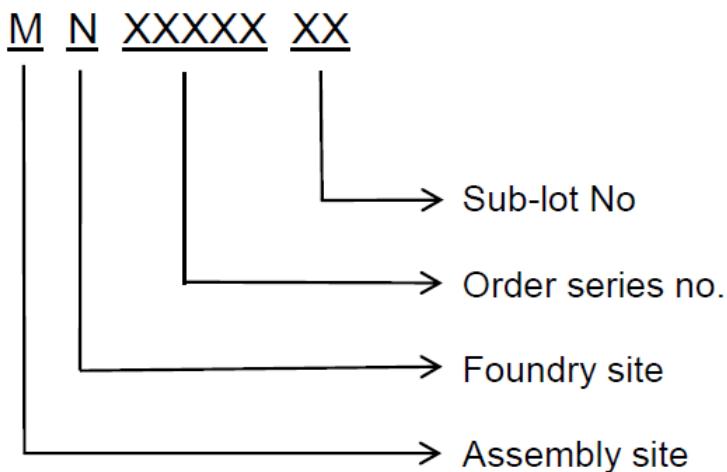


## P1615ATA

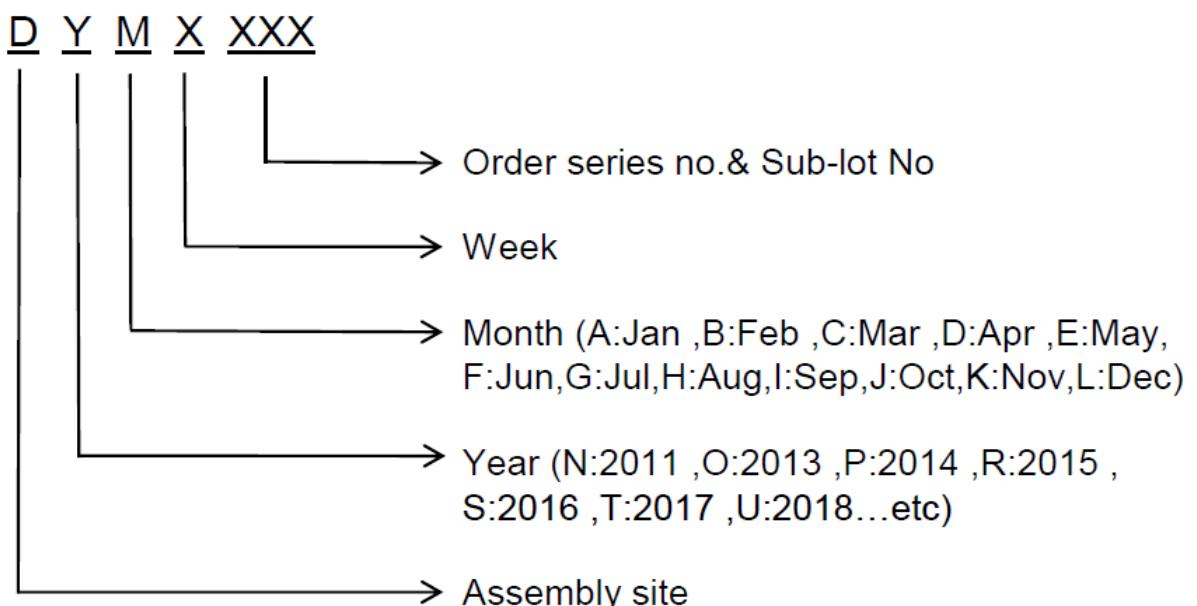
### N-Channel Enhancement Mode MOSFET

#### C. Lot No.&Date Code rule

##### 1. Lot No.



##### 2. Date Code



## P1615ATA

### N-Channel Enhancement Mode MOSFET

#### D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm		
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)		
3	U-NIKC	Height: 4 mm		
4	Package	Height: 2 mm		
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12		
6	Device	Height: 3 mm (Max: 16 Digit)		
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot		
8	D/C	Height: 3 mm (Max: 7 Digit)		
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed		
10	RoHS label	 long axis: 12 mm      minor axis: 6 mm bottom color: White Font color: Black      Font style: Arial		
11	Halogen Free label	 Diameter: 10 mm      bottom color: Green Font color: Black      Font style: Arial		
12	Scan information	Device / Lot / D/C / QTY , Insert “ / ” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least		