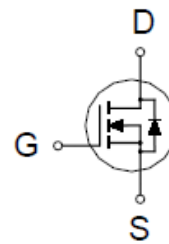
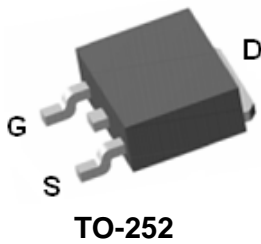


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N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
200V	157mΩ @ $V_{GS} = 10V$	18A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	200	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current	$T_C = 25\text{ °C}$	I_D	18	A
	$T_C = 100\text{ °C}$		9	
Pulsed Drain Current ¹		I_{DM}	30	
Avalanche Current		I_{AS}	12	
Avalanche Energy	$L = 1\text{mH}$	E_{AS}	72	mJ
Power Dissipation	$T_C = 25\text{ °C}$	P_D	83	W
	$T_C = 100\text{ °C}$		33	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

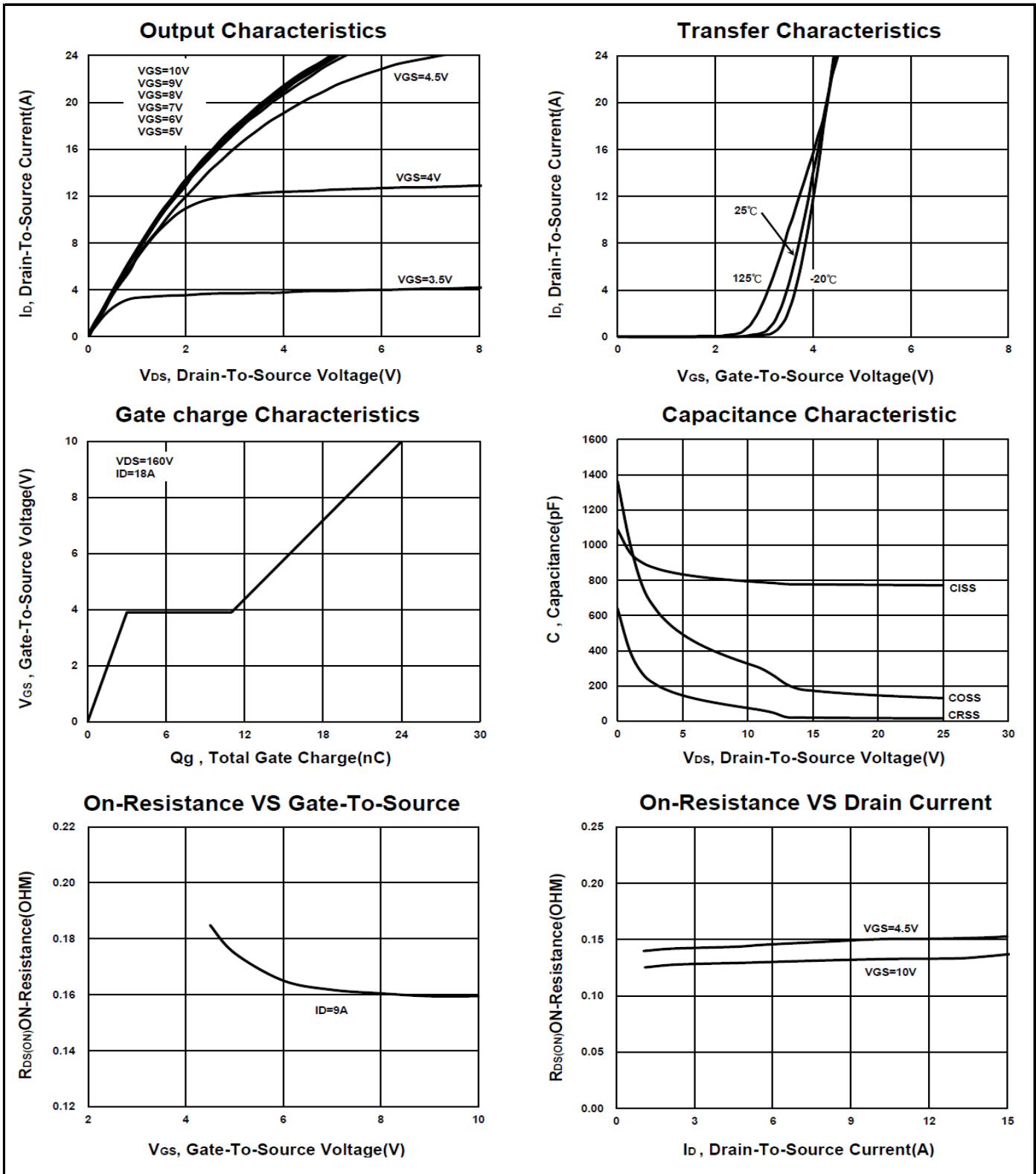
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	200			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	2	3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200V, V _{GS} = 0V			1	μA
		V _{DS} = 160V, V _{GS} = 0V, T _J = 125 °C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 9A		148	188	mΩ
		V _{GS} = 10V, I _D = 9A		131	157	
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 9A		21		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		780		pF
Output Capacitance	C _{oss}			132		
Reverse Transfer Capacitance	C _{rss}			16		
Total Gate Charge ²	Q _g (V _{GS} = 10V)	V _{DS} = 160V, I _D = 18A		24		nC
	Q _g (V _{GS} = 4.5V)			12		
Gate-Source Charge ²	Q _{gs}			3		
Gate-Drain Charge ²	Q _{gd}			8		
Turn-On Delay Time ²	t _{d(on)}		V _{DS} = 100V, I _D ≅ 18A, V _{GS} = 10V, R _{GEN} = 25Ω		20	
Rise Time ²	t _r			135		
Turn-Off Delay Time ²	t _{d(off)}			176		
Fall Time ²	t _f			128		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				18	A
Forward Voltage ¹	V _{SD}	I _F = 18A, V _{GS} = 0V			1	V
Diode Reverse Recovery Time	t _{rr}	I _F = 18A, di/dt = 100A / μS		122		nS
Diode Reverse Recovery Charge	Q _{rr}			0.67		uC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

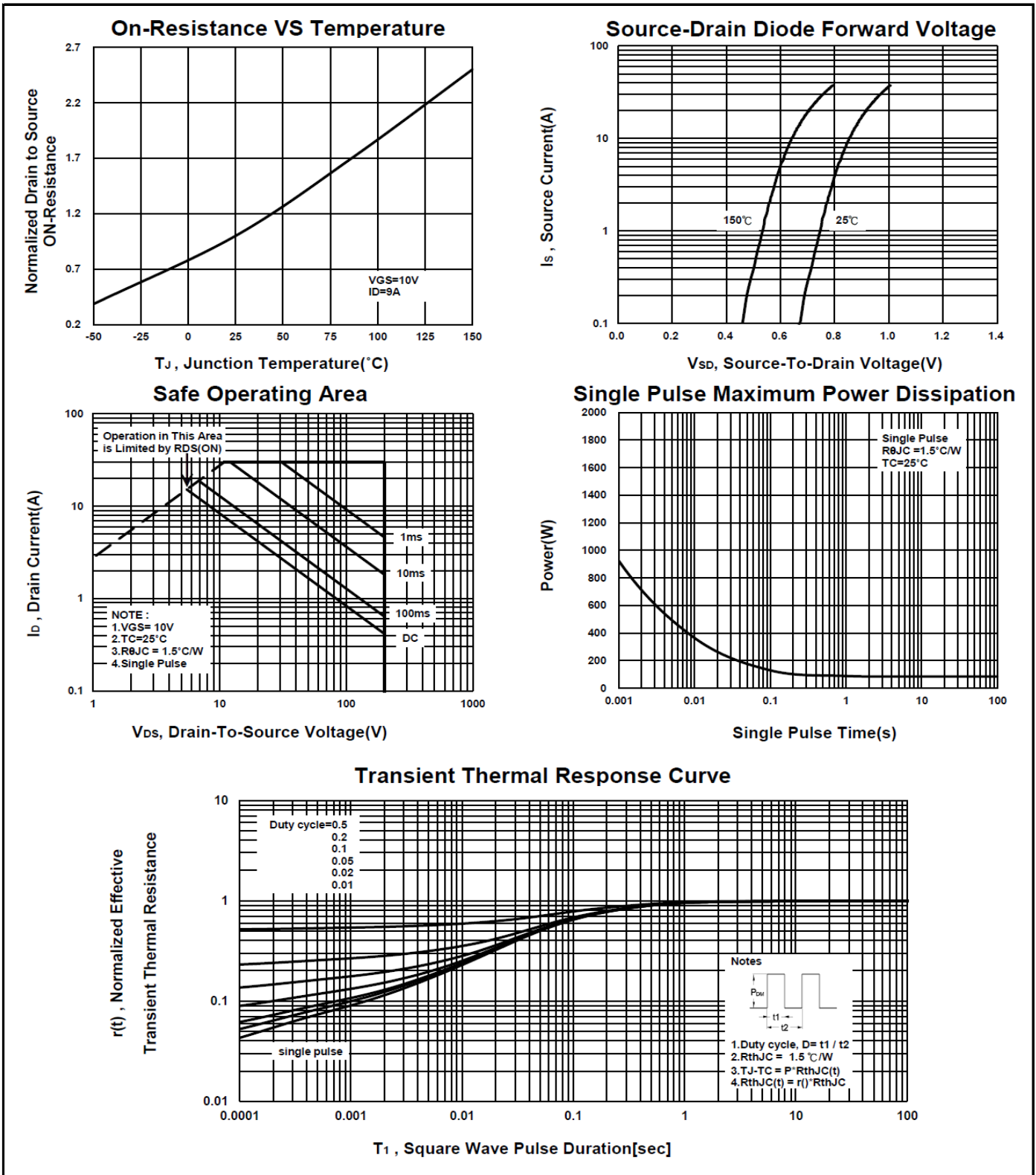
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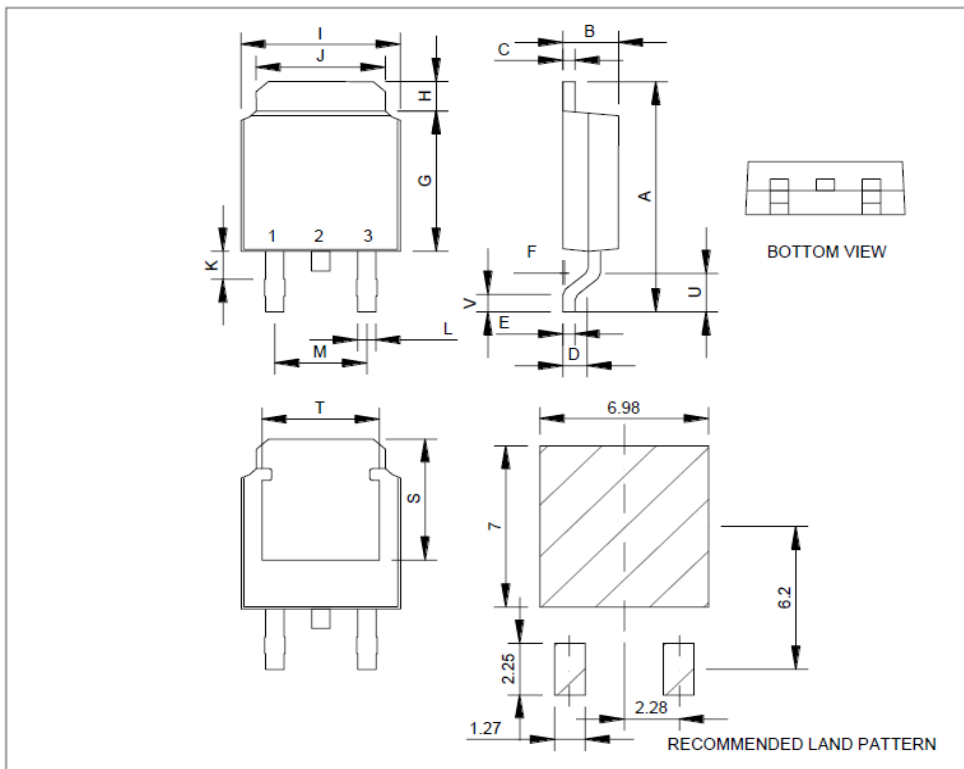
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Package Dimension

TO-252 (DPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	10	10.41	J	4.8		5.64
B	2.1	2.2	2.4	K	0.15		1.1
C	0.4	0.5	0.61	L	0.4	0.76	0.89
D	0.82	1.2	1.5	M	4.2	4.58	5
E	0.4	0.5	0.61	S	4.9	5.1	5.3
F	0		0.2	T	4.6	4.75	5.44
G	5.3	6.1	6.3	U	1.4		1.78
H	0.9		1.7	V	0.55	1.25	1.7
I	6.3	6.5	6.8				

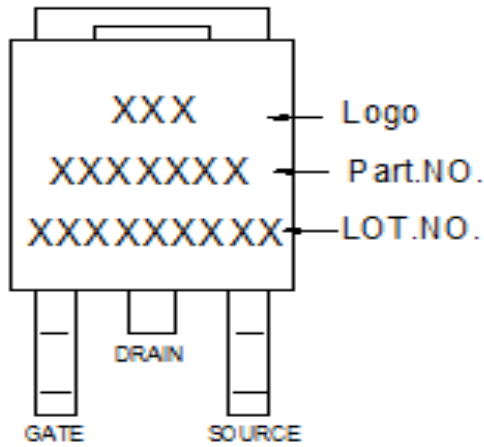


*因为各家封装模具不同而外观略有所差异，不影响电性及Layout。

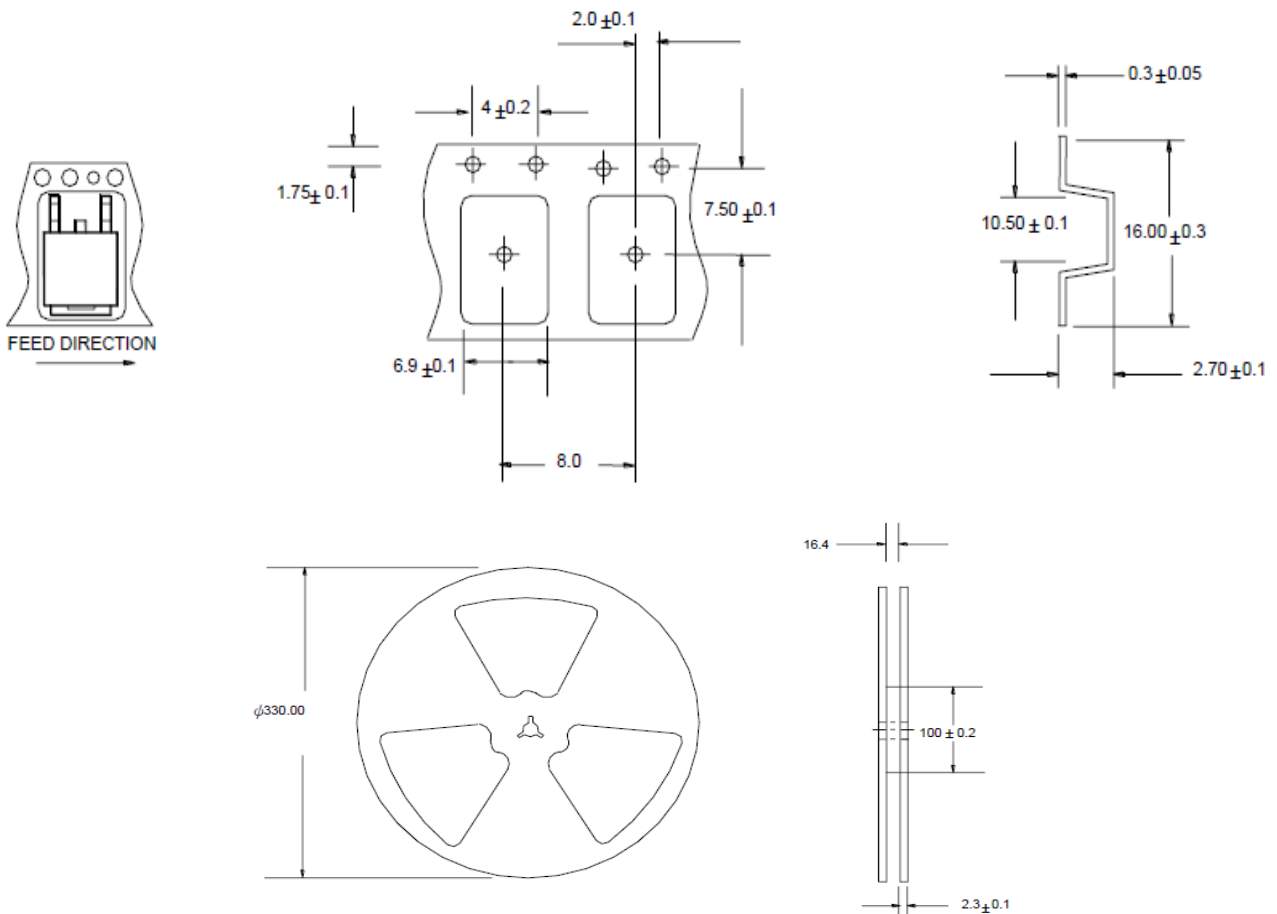
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N-Channel Enhancement Mode MOSFET

A. Marking Information



B. Tape & Reel Information: 2500pcs/Reel

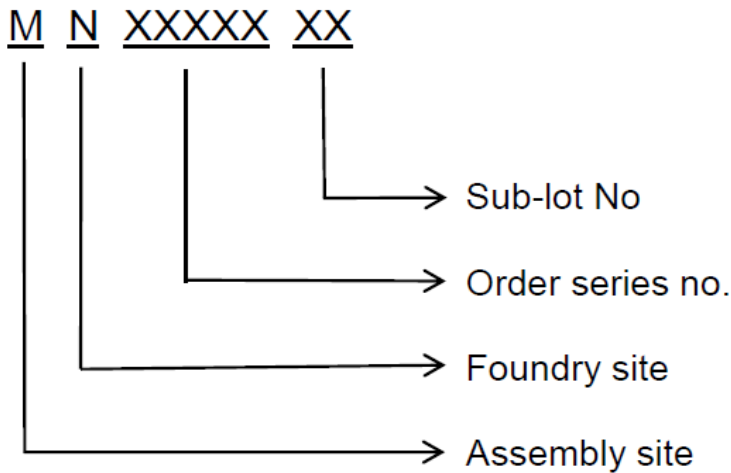


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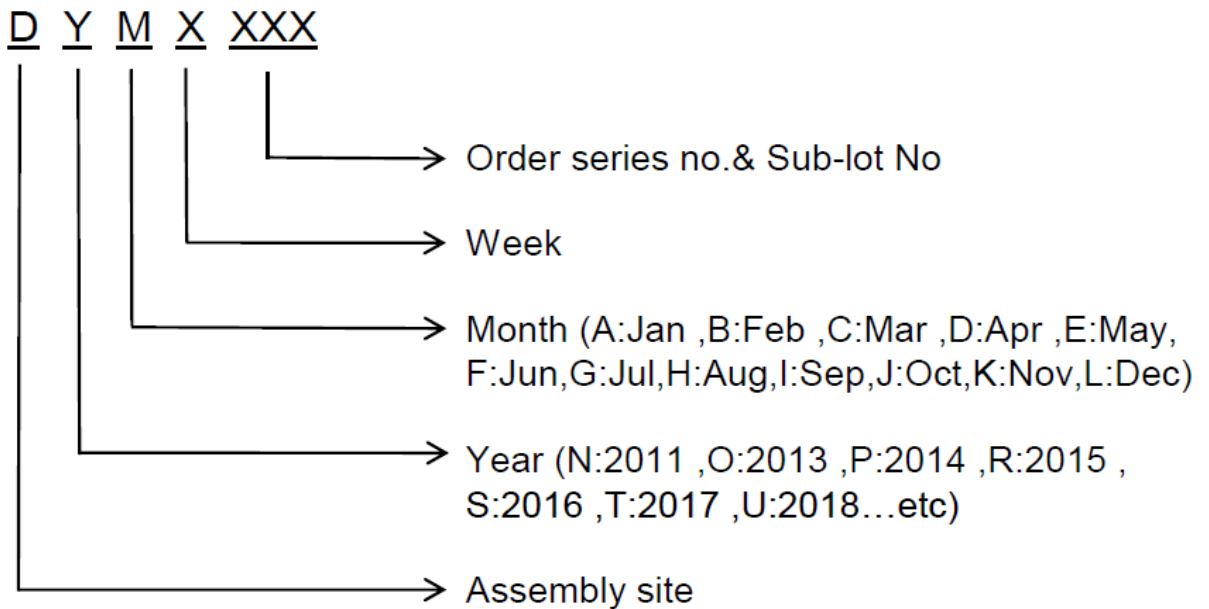
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C. Lot No.&Date Code rule

1.Lot No.



2.Date Code





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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文”0”和数字”0”，”G和”Q”的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert “/” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least