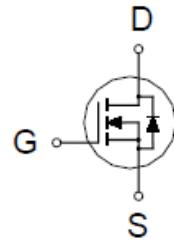
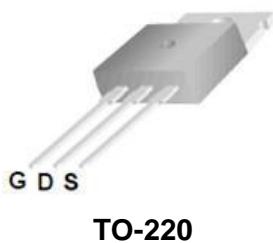


# P1825AT

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
250V	0.2Ω @ $V_{GS} = 10V$	18A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current <sup>1</sup>	$I_D$	18	A
$T_C = 100^\circ C$		8.5	
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	72	
Avalanche Current	$I_{AS}$	15	
Avalanche Energy	$E_{AS}$	106	mJ
Power Dissipation	$P_D$	90	W
$T_C = 100^\circ C$		36	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{0JC}$	1.39	62.5	°C / W
Junction-to-Ambient	$R_{0JA}$			

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Limited by package.

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#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	250			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	2.7	4	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 250\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 200\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 9\text{A}$		0.18	0.2	$\Omega$
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 10\text{V}, I_D = 18\text{A}$		20		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		1810		pF
Output Capacitance	$C_{\text{oss}}$			153		
Reverse Transfer Capacitance	$C_{\text{rss}}$			33		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 200\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 9\text{A}$		57		nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			9		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			26		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 125\text{V}, I_D \approx 18\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		31		nS
Rise Time <sup>2</sup>	$t_r$			215		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			73		
Fall Time <sup>2</sup>	$t_f$			118		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current <sup>3</sup>	$I_S$				18	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 18\text{A}, V_{\text{GS}} = 0\text{V}$			1.6	V
Reverse Recovery Time	$t_{\text{rr}}$	$V_{\text{GS}} = 0\text{V}, I_F = 18\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		210		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			1.3		$\mu\text{C}$

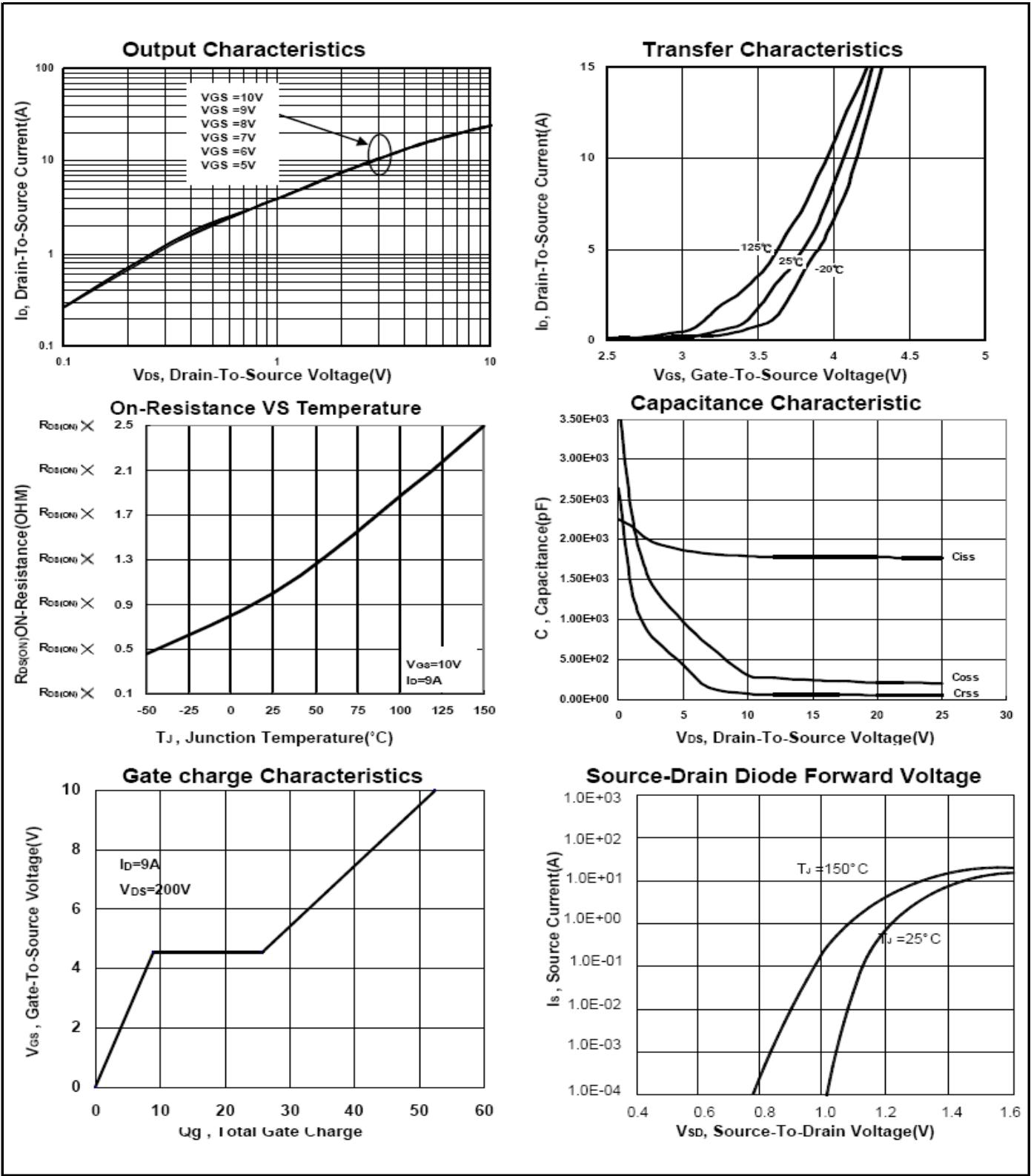
<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

<sup>3</sup> Limited by maximum junction temperature.

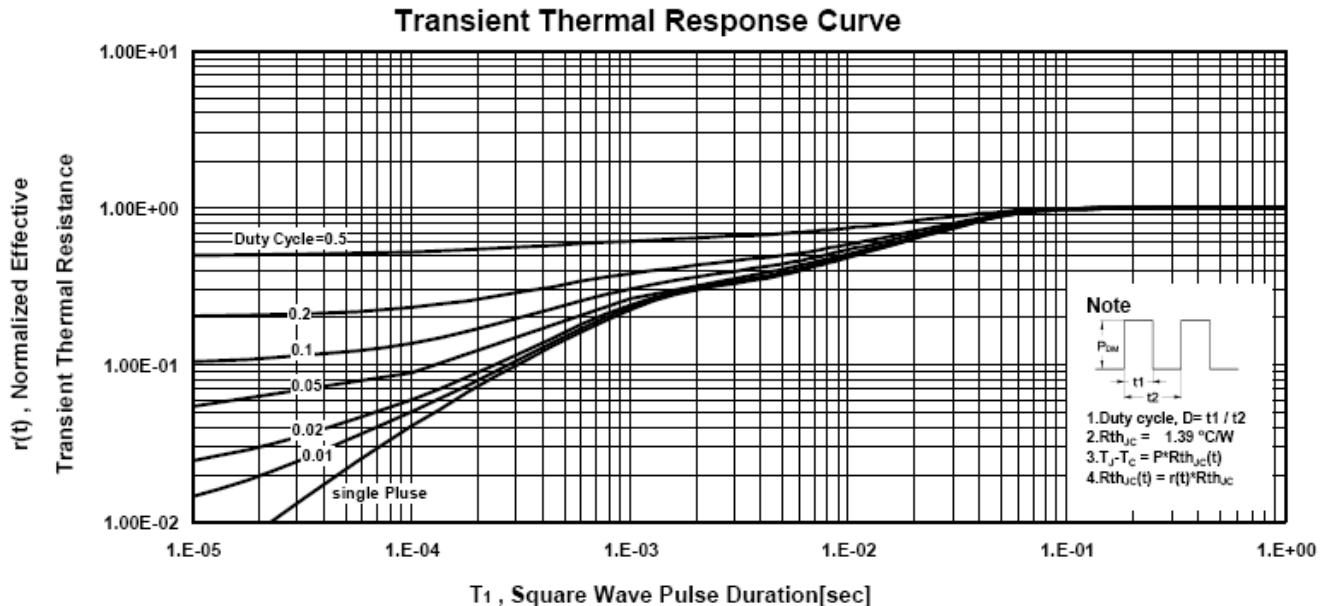
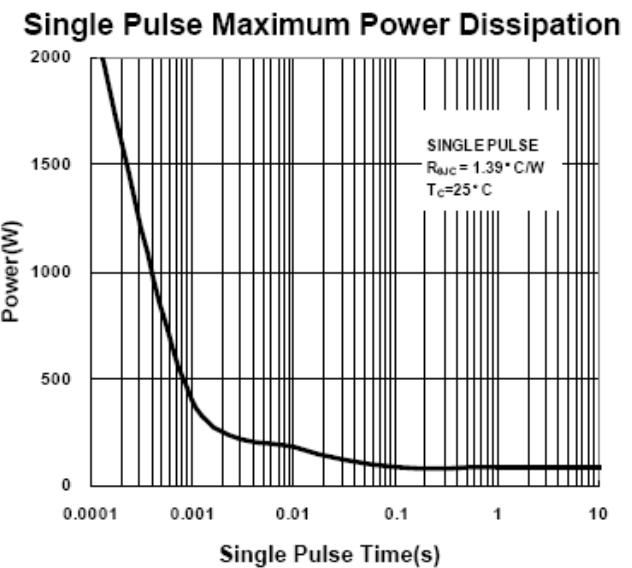
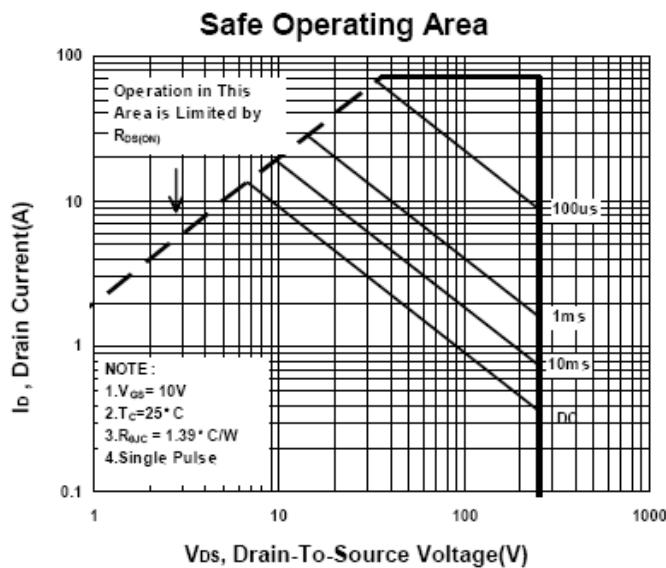
## P1825AT

### N-Channel Enhancement Mode MOSFET



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## P1825AT

### N-Channel Enhancement Mode MOSFET

#### Package Dimension

#### TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

