NIKO-SEM

N-Channel Enhancement Mode Field Effect Transistor

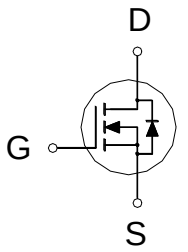
P1825HTFB

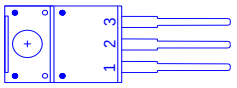
TO-220F

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
250V	230mΩ	18A





1: GATE
2: DRAIN
3: SOURCE

RoHS Compliant

ABSOLUTE MAXIMUM RATINGS (T_A = 25 ° C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	250	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C = 25 ° C	I_D	18	A
	T_C = 100 ° C		5	
Pulsed Drain Current[1]		I_{DM}	32	
Avalanche Current		I_{AS}	8	
Avalanche Energy	L = 1mH	E_{AS}	32	mJ
Power Dissipation	T_C = 25 ° C	P_D	35	W
	T_C = 100 ° C		14	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	° C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		62.5	° C / W
Junction-to-Case	$R_{\theta JC}$		3.6	

[1]Pulse width limited by maximum junction temperature.

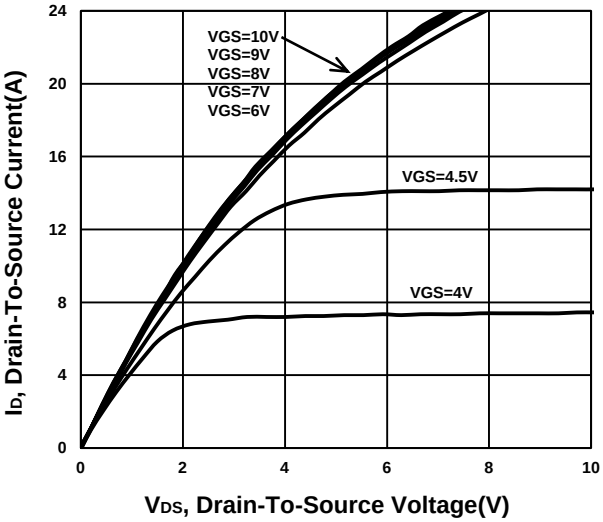
.ELECTRICAL CHARACTERISTICS (T_J = 25 ° C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS MIN	LIMITS TYP	LIMITS MAX	UNIT
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	250			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1	2	3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 250V, V_{GS} = 0V			1	μA
		V_{DS} = 200V, V_{GS} = 0V, T_J = 125 ° C			10	
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 4.5V, I_D = 9A		255	298	mΩ
		V_{GS} =10V, I_D = 9A		182	230	

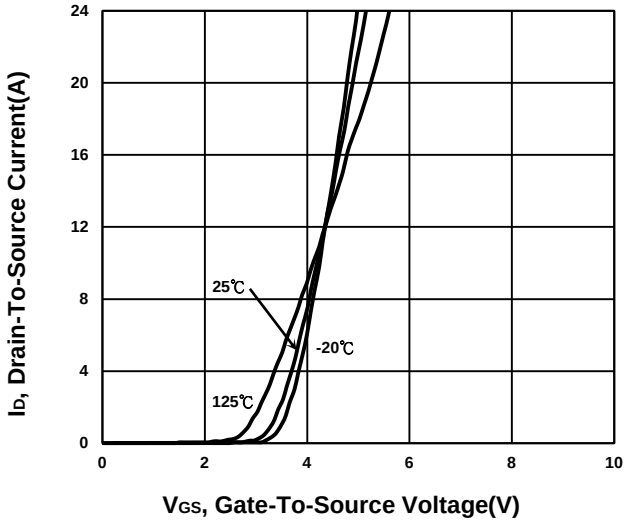
Forward Transconductance[1]	g_{fs}	V_{DS} = 10V, I_D = 9A		13		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		835		pF
Output Capacitance	C_{oss}			119		
Reverse Transfer Capacitance	C_{rss}			17		
Total Gate Charge[2]	Q_g	V_{GS} = 10V , V_{DS} = 200V I_D =18A		25		nC
Gate-Source Charge[2]	Q_{gs}			4		
Gate-Drain Charge[2]	Q_{gd}			11		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} = 125V , $I_D \cong$ 18A, V_{GS} = 10V, R_{GEN} =6Ω		14		nS
Rise Time[2]	t_r			50		
Turn-Off Delay Time[2]	$t_{d(off)}$			40		
Fall Time[2]	t_f			70		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 ° C)						
Continuous Current	I_S				18	A
Forward Voltage[1]	V_{SD}	I_F = 18A, V_{GS} = 0V			1.2	V
Diode Reverse Recovery Time	t_{rr}	I_F= 18A, dI/dt=100A/μs		194		nS
Diode Reverse Recovery Charge	Q_{rr}			1.26		uC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.

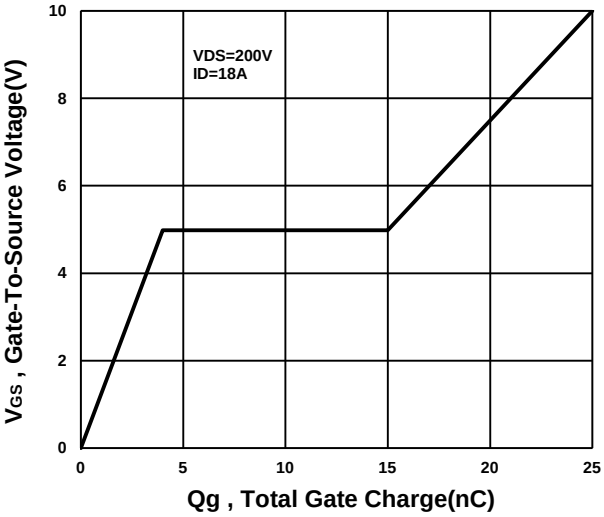
Output Characteristics



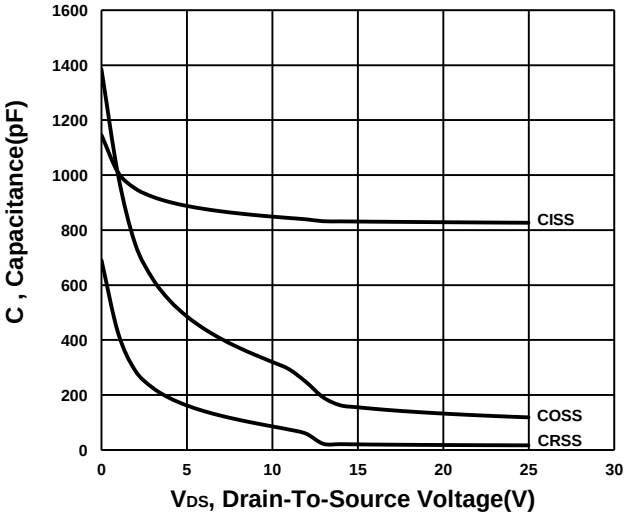
Transfer Characteristics



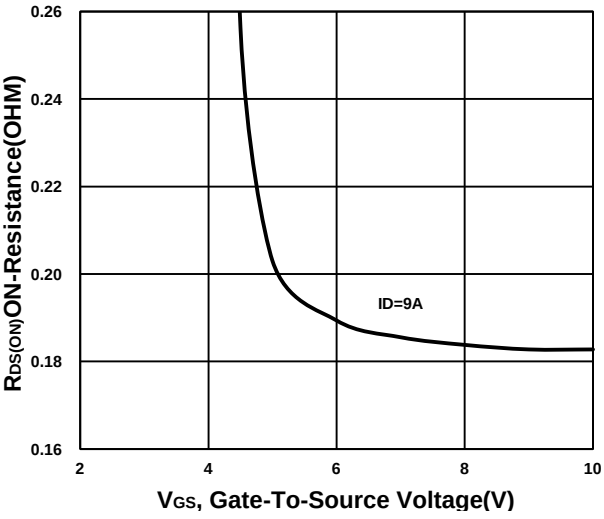
Gate charge Characteristics



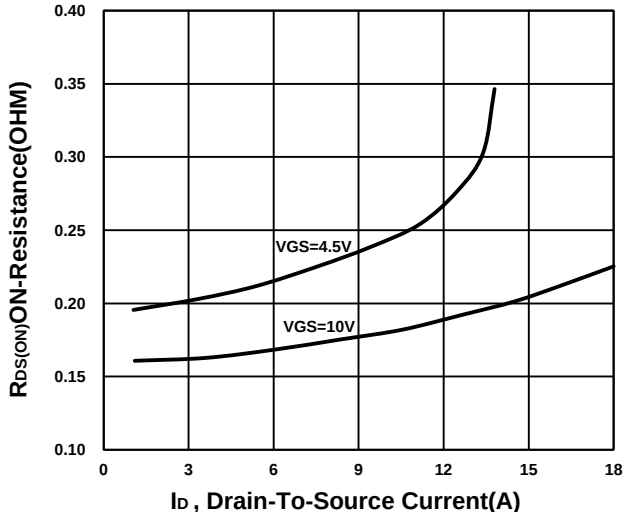
Capacitance Characteristic



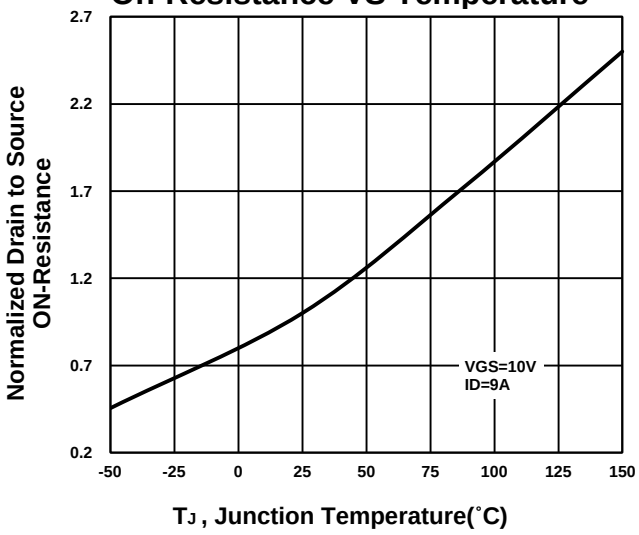
On-Resistance VS Gate-To-Source



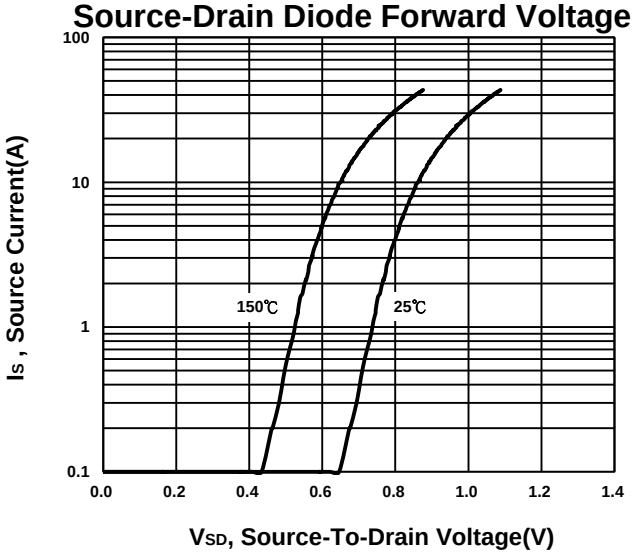
On-Resistance VS Drain Current



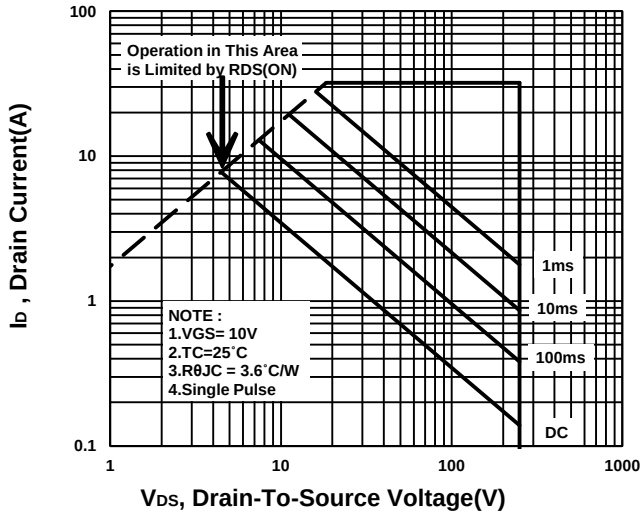
On-Resistance VS Temperature



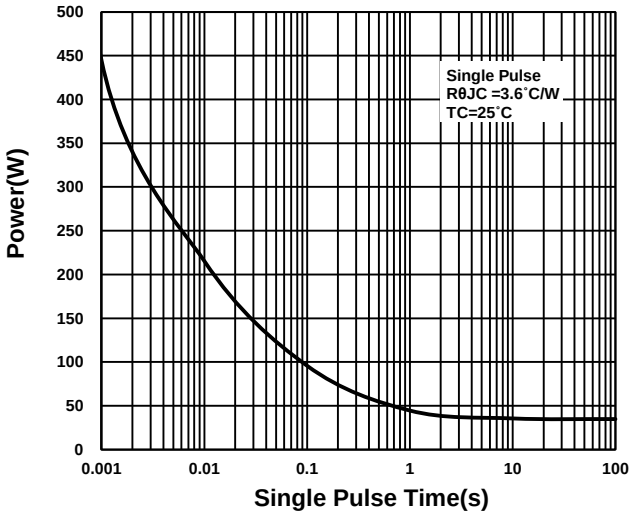
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve