

P18LF6QLK

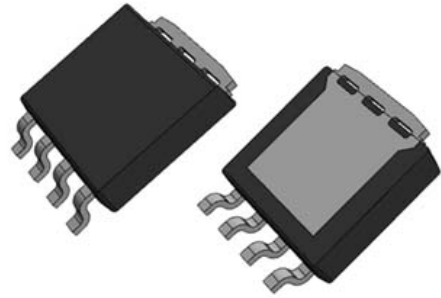
Power MOSFETs
60V, 18A, N-channel

Feature

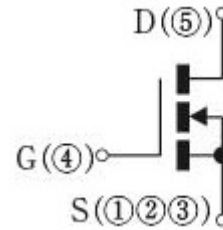
- N-channel
- Small SMD
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Based on AEC-Q101
- Halogen free
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): LF
Package (JEDEC Code): MO-235B similar



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel temperature	Tch		-55 to 175	°C
Drain-source voltage	V _{DSS}		60	V
Gate-source voltage	V _{GSS}		±20	V
Continuous drain current(DC)	I _D		18	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10μs, duty=1/100	54	A
Total power dissipation	P _T		50	W
Single avalanche current	I _{AS}	Starting Tch=25°C Tch≤150°C	12	A
Single avalanche energy	E _{AS}	Starting Tch=25°C Tch≤150°C	16	mJ

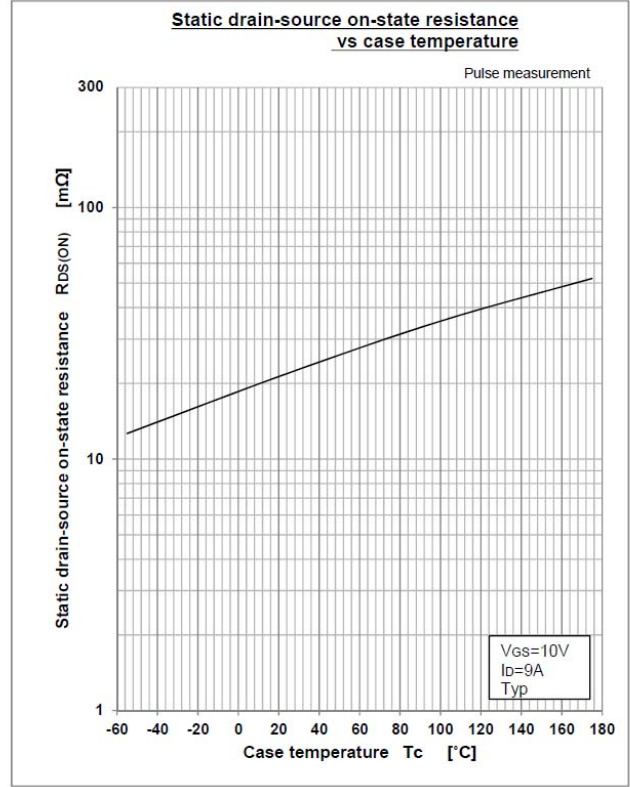
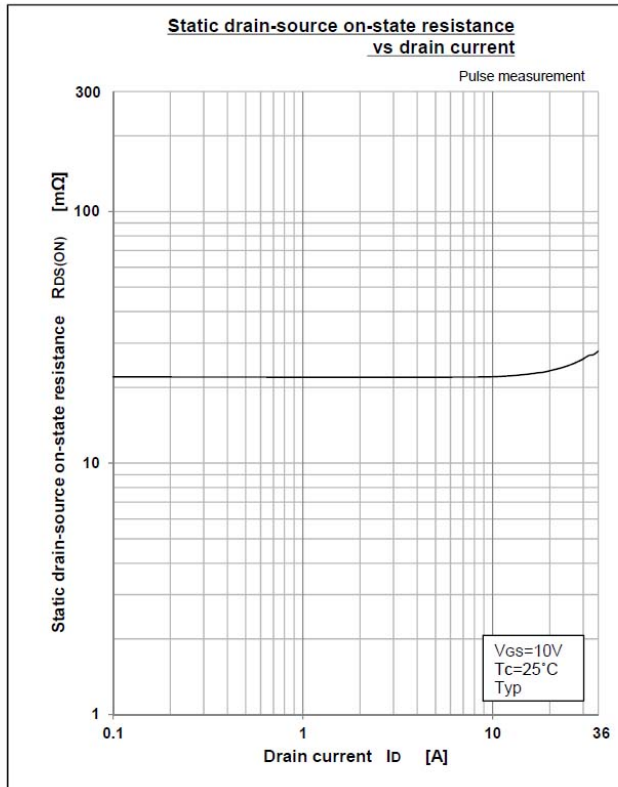
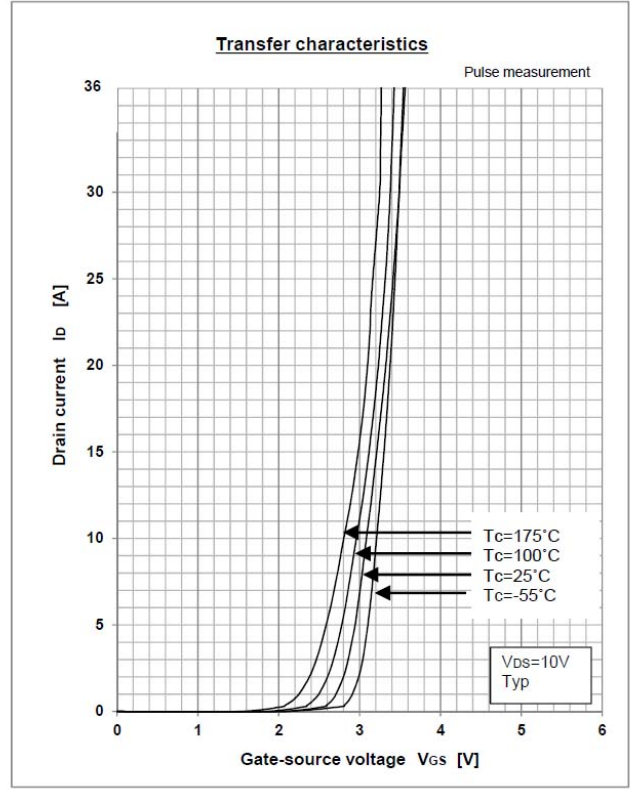
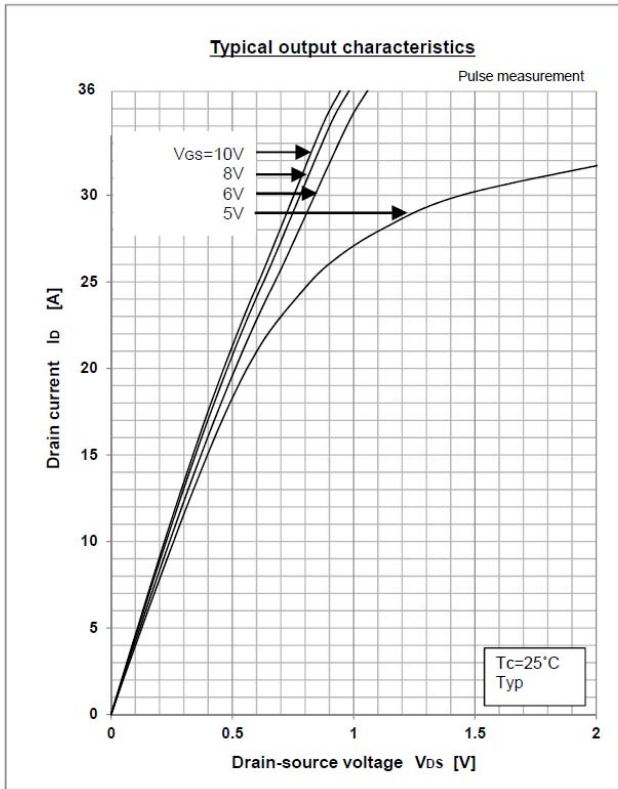
* : See the original Specifications

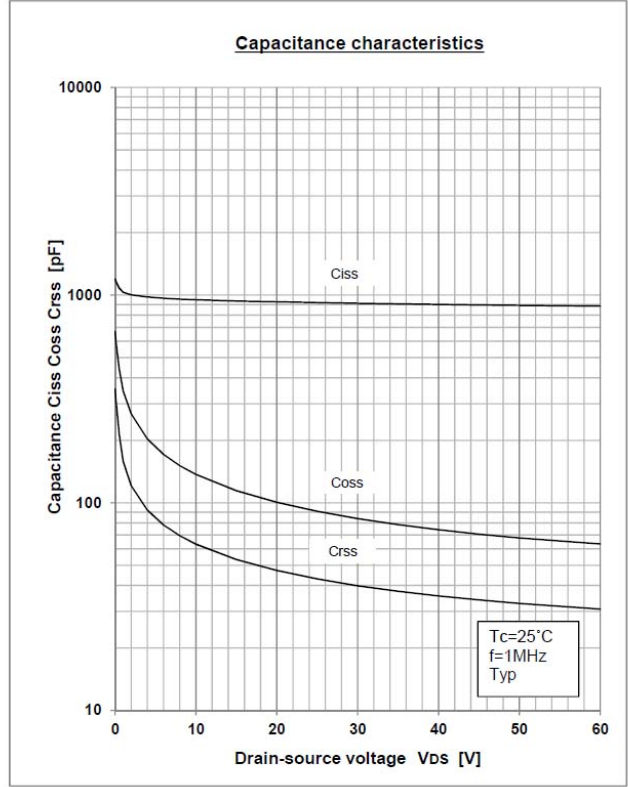
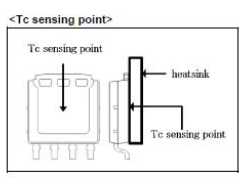
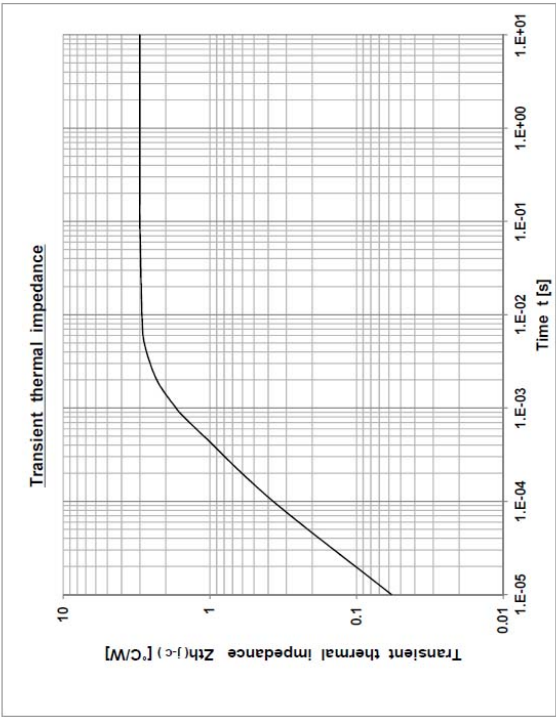
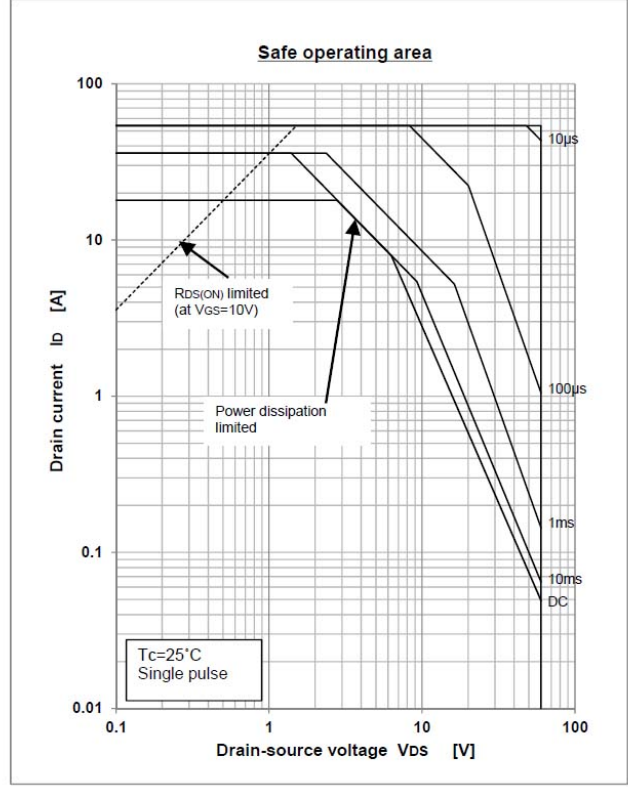
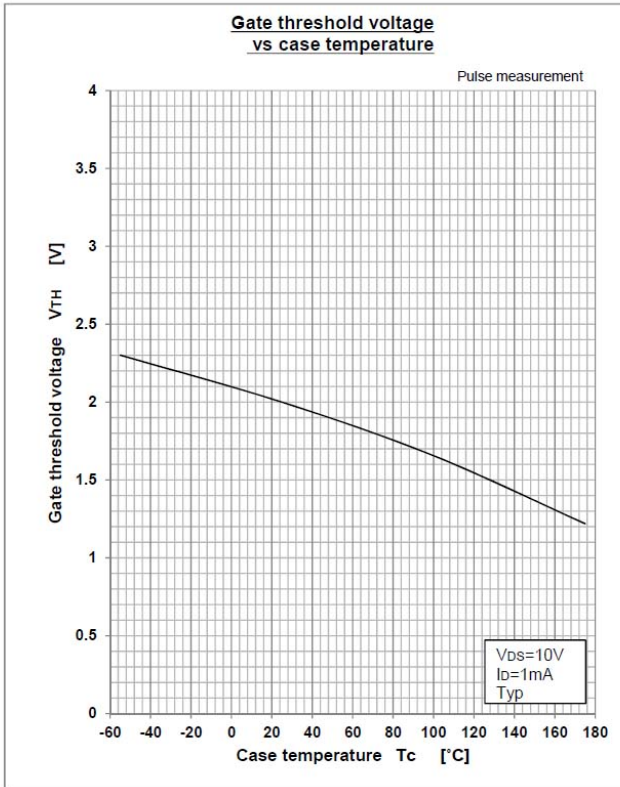
Electrical Characteristics (unless otherwise specified : Tc=25°C)

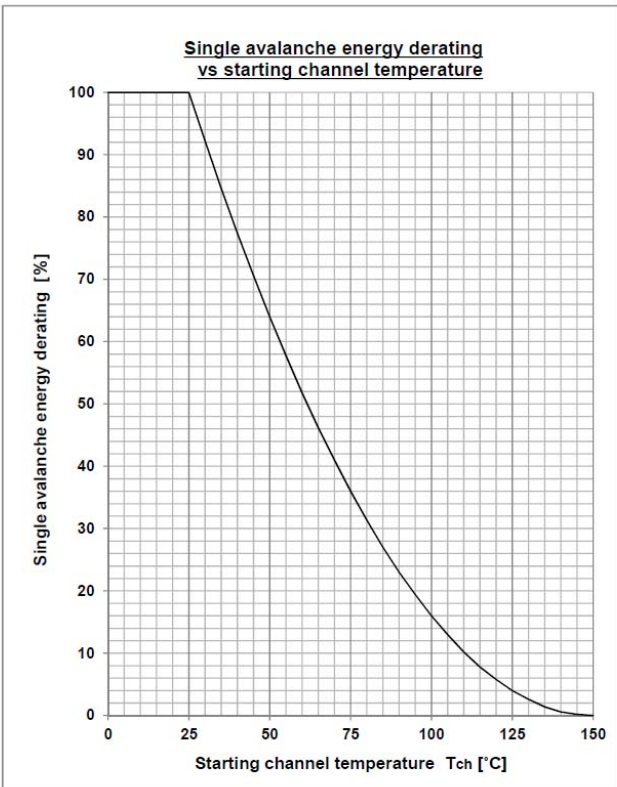
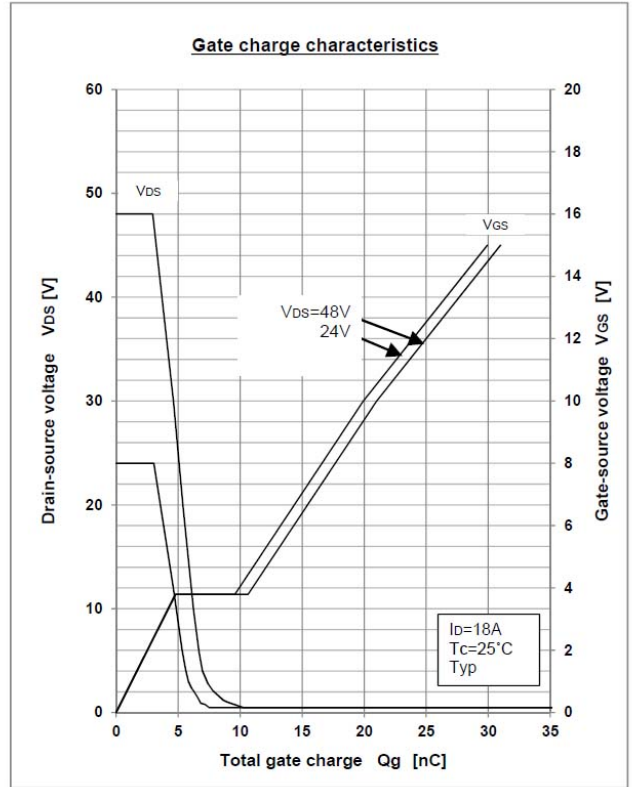
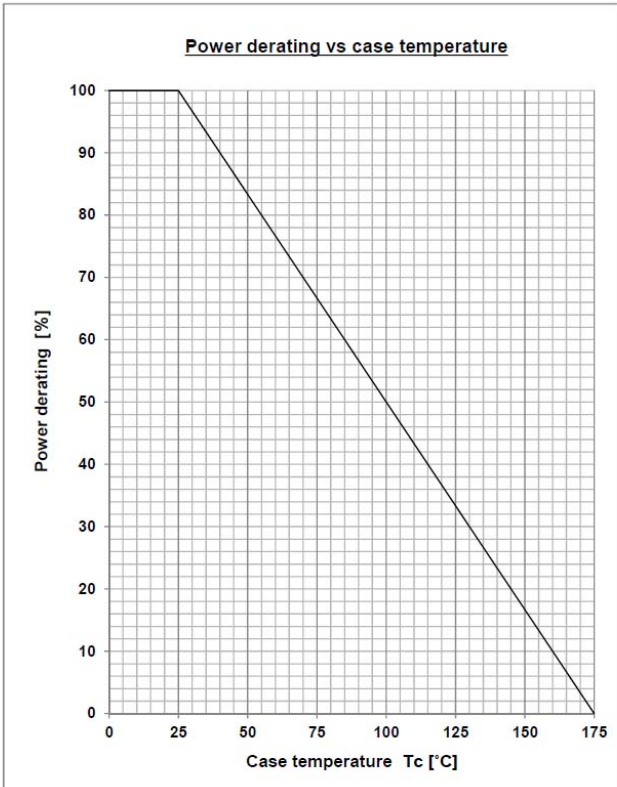
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	60			V
Zero gate voltage drain current	I_{DSS}	VDS=60V, VGS=0V			1	μA
Gate-source leakage current	I_{GSS}	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	g_{fs}	ID=9A, VDS=10V	5			S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=9A, VGS=10V		0.022	0.028	Ω
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=9A, VGS=4.5V		0.027	0.036	Ω
Gate threshold voltage	V_{th}	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	V_{SD}	IS=18A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case, with heatsink			3	°C/W
Total gate charge	Q_g	VDD=48V, VGS=10V, ID=18A		21		nC
Gate to source charge	Q_{gs}	VDD=48V, VGS=10V, ID=18A		5		nC
Gate to drain charge	Q_{gd}	VDD=48V, VGS=10V, ID=18A		5		nC
Input capacitance	C_{iss}	VDS=25V, VGS=0V, f=1MHz		920		pF
Reverse transfer capacitance	C_{rss}	VDS=25V, VGS=0V, f=1MHz		43		pF
Output capacitance	C_{oss}	VDS=25V, VGS=0V, f=1MHz		91		pF
Turn-on delay time	$t_{d(on)}$	ID=9A, RL=3.33Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		3.3		ns
Rise time	t_r	ID=9A, RL=3.33Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		7		ns
Turn-off delay time	$t_{d(off)}$	ID=9A, RL=3.33Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		15		ns
Fall time	t_f	ID=9A, RL=3.33Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		3.3		ns
Diode reverse recovery time	t_{rr}	IF=18A, VGS=0V, di/dt=100A/μs		36		ns
Diode reverse recovery charge	Q_{rr}	IF=18A, VGS=0V, di/dt=100A/μs		37		nC

* : See the original Specifications

CHARACTERISTIC DIAGRAMS

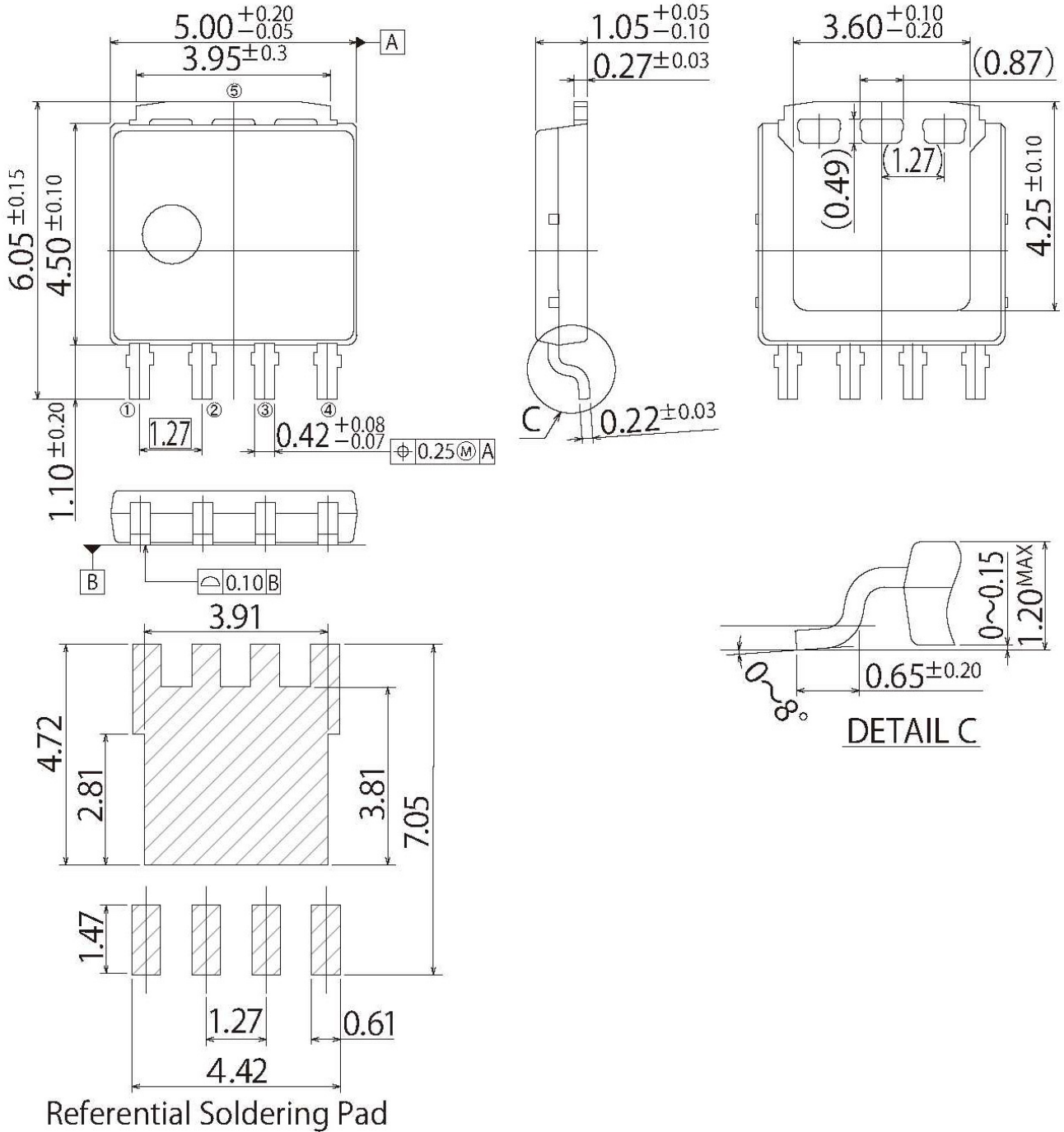






G7

JEDEC Code	MO-235B similar
JEITA Code	—
House Name	LF



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