

P1B90VX3K

Power MOSFETs

900V, 1A, N-channel

Feature

- N-channel
- SMD
- High Voltage (900V)
- Low Capacitance
- High Avalanche Durability, High di/dt Durability
- Based on AEC-Q101
- Halogen free
- Pb free terminal
- RoHS:Yes

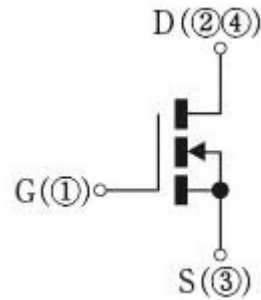
OUTLINE

Package (House Name): FB

Package (JEDEC Code): TO-252AA



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Channel temperature	T _{ch}		-55 to 150	°C
Drain-source voltage	V _{DSS}		900	V
Gate-source voltage	V _{GSS}		±30	V
Continuous drain current(DC)	I _D		1	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10μs, duty=1/100	4	A
Continuous source current(DC)	I _S		1	A
Total power dissipation	P _T		36	W
Repetitive avalanche current	I _{AR}	Starting T _{ch} =25°C T _{ch} ≤150°C	1	A
Single avalanche energy	E _{AS}	Starting T _{ch} =25°C T _{ch} ≤150°C	10	mJ
Repetitive avalanche energy	E _{AR}	Starting T _{ch} =25°C T _{ch} ≤150°C	1	mJ
Drain-source diode di/dt strength	di/dt	I _S =1A, T _c =25°C	350	A/μs

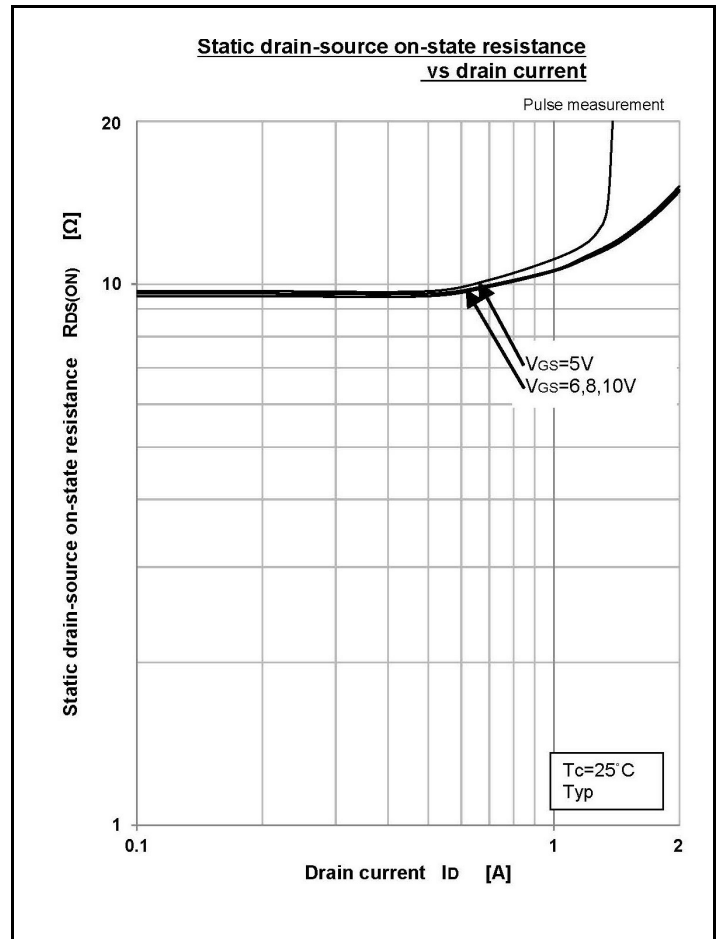
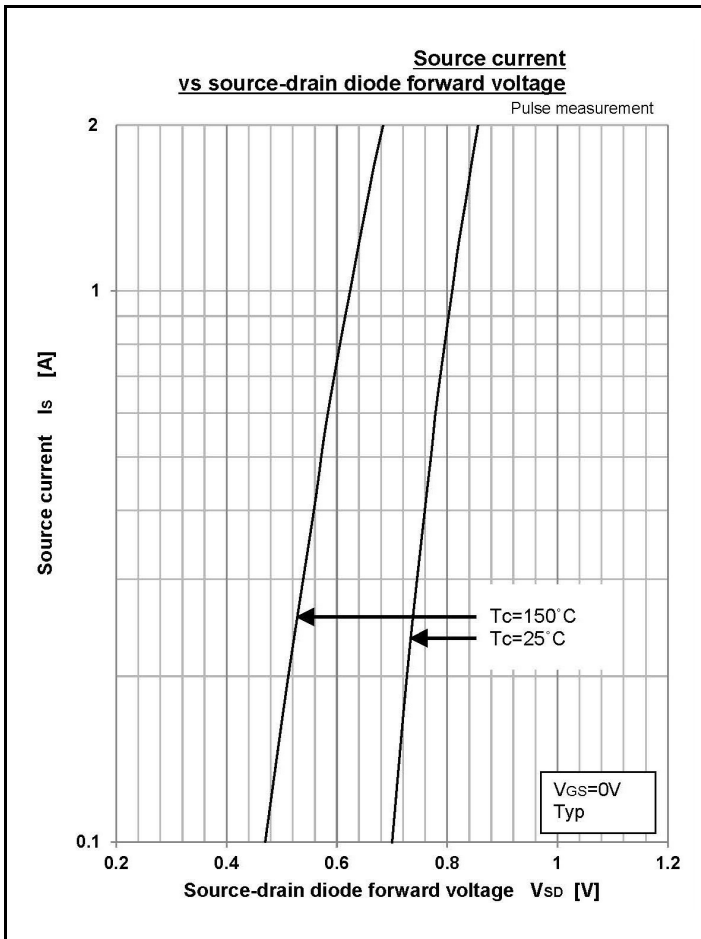
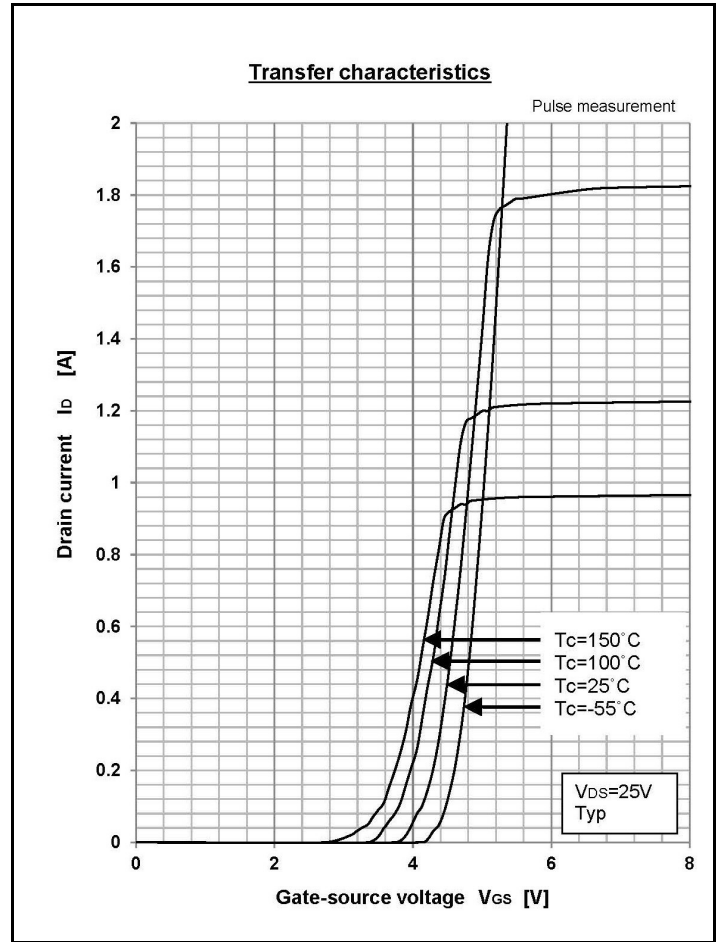
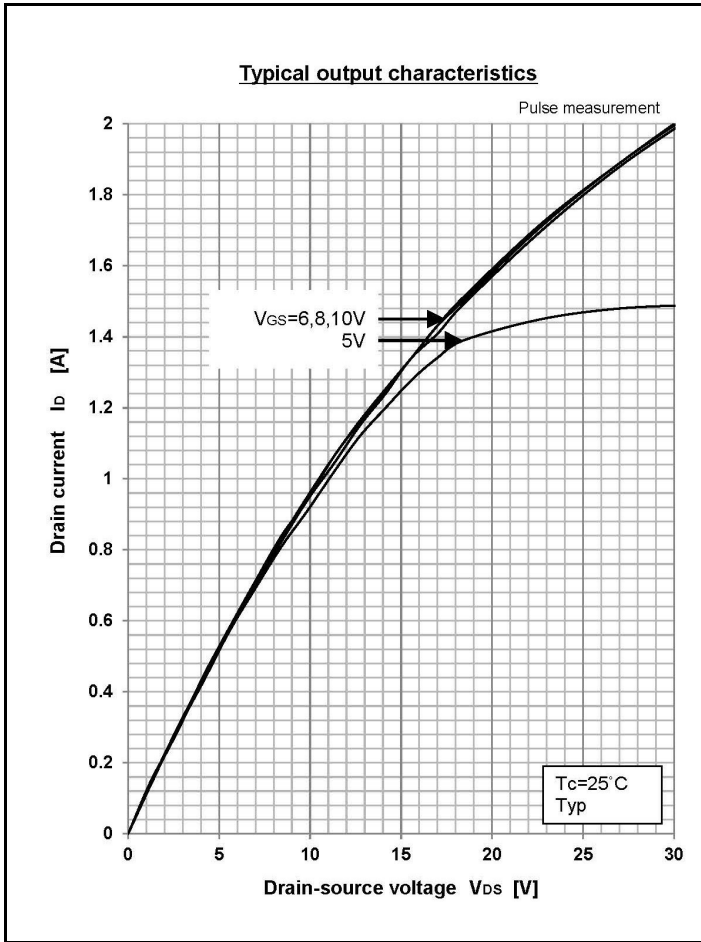
* :See the original Specifications

Electrical Characteristics (unless otherwise specified : Tc=25°C)

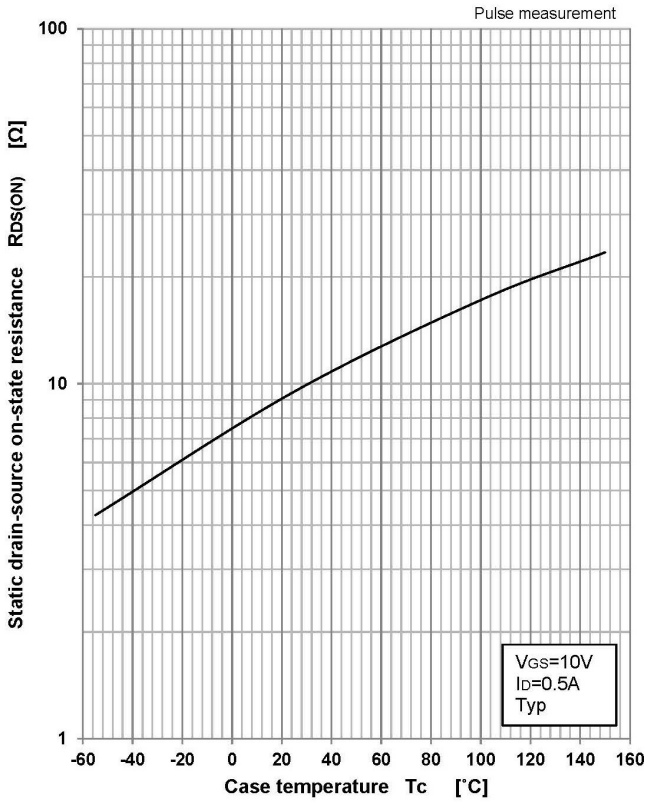
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	900			V
Zero gate voltage drain current	I_{DSS}	VDS=900V, VGS=0V			100	μA
Gate-source leakage current	I_{GSS}	VGS=±25V, VDS=0V			±10	μA
Forward transconductance	g_{fs}	ID=0.5A, VDS=10V	0.7	1.5		S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=0.5A, VGS=10V		9.5	14	Ω
Gate threshold voltage	V_{th}	ID=0.2mA, VDS=10V	3	3.5	4	V
Source-drain diode forward voltage	V_{SD}	IS=0.5A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case, with heatsink			3.4	°C/W
Total gate charge	Q_g	VDD=400V, VGS=10V, ID=1A		10.5		nC
Input capacitance	C_{iss}	VDS=25V, VGS=0V, f=1MHz		201		pF
Reverse transfer capacitance	C_{rss}	VDS=25V, VGS=0V, f=1MHz		4.5		pF
Output capacitance	C_{oss}	VDS=25V, VGS=0V, f=1MHz		26.7		pF
Turn-on delay time	$t_{d(on)}$	ID=0.5A, RL=300Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		9		ns
Rise time	t_r	ID=0.5A, RL=300Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		22		ns
Turn-off delay time	$t_{d(off)}$	ID=0.5A, RL=300Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		38		ns
Fall time	t_f	ID=0.5A, RL=300Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		26		ns

* : See the original Specifications

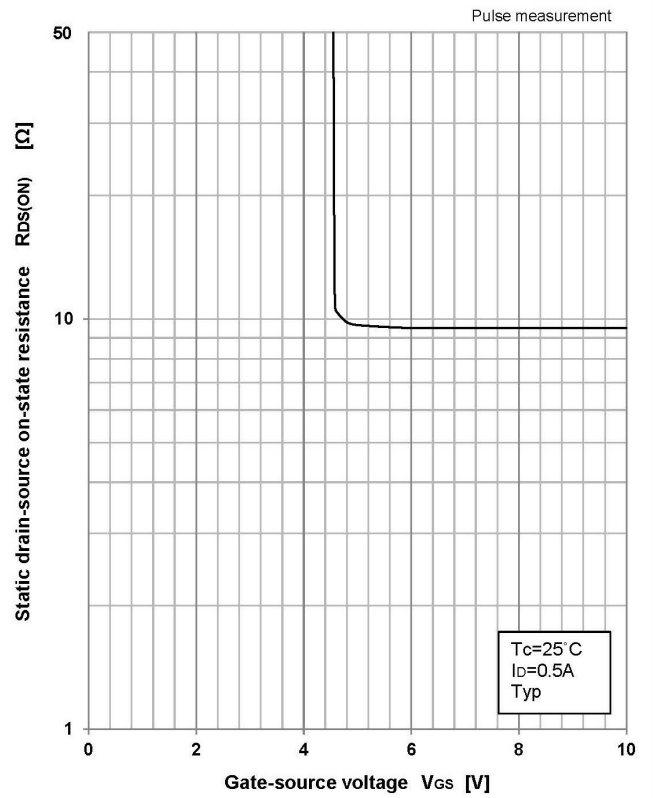
CHARACTERISTIC DIAGRAMS



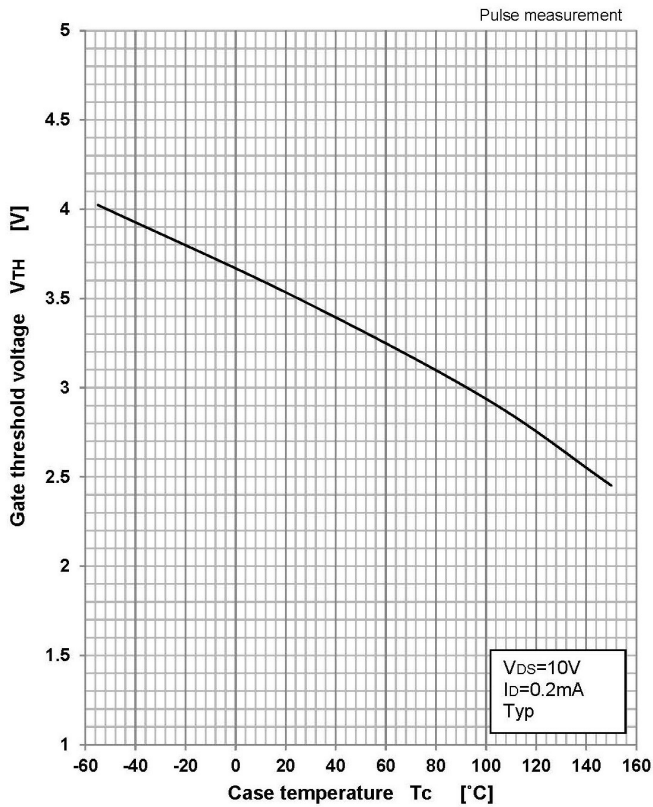
Static drain-source on-state resistance vs case temperature



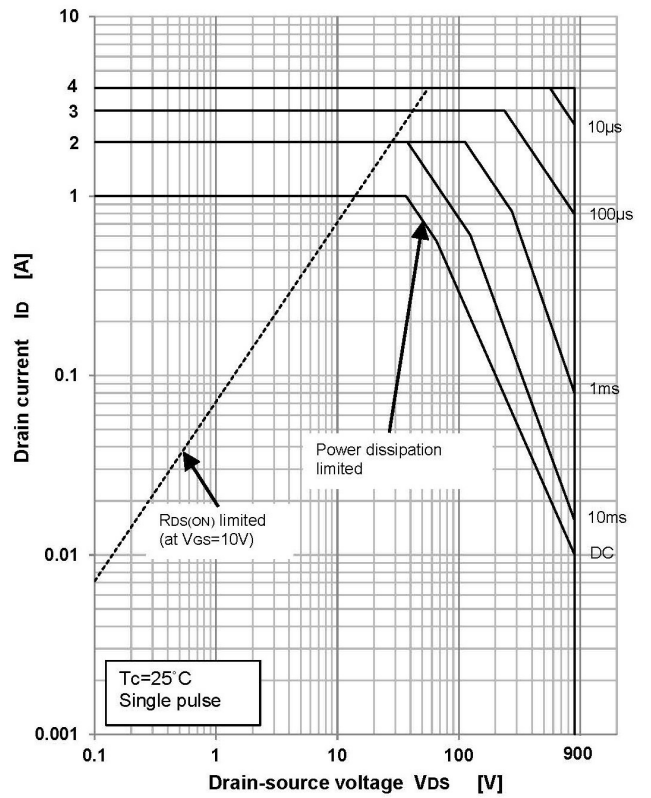
Static drain-source on-state resistance vs gate-source voltage

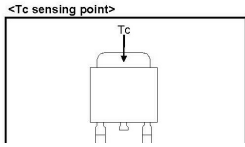
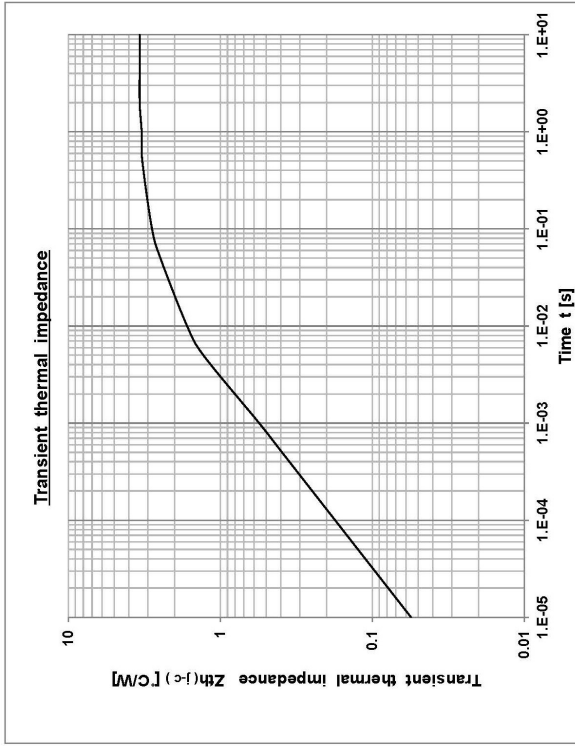


Gate threshold voltage vs case temperature



Safe operating area

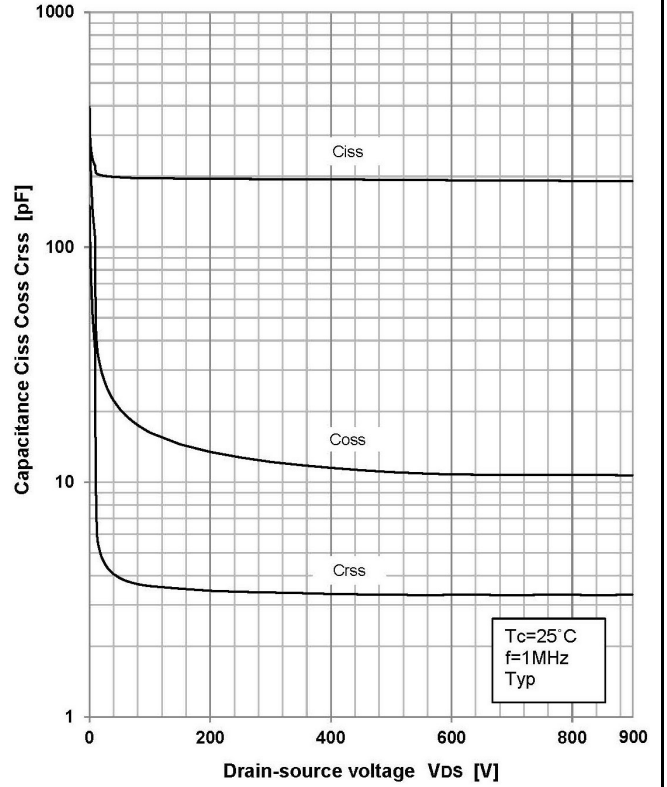




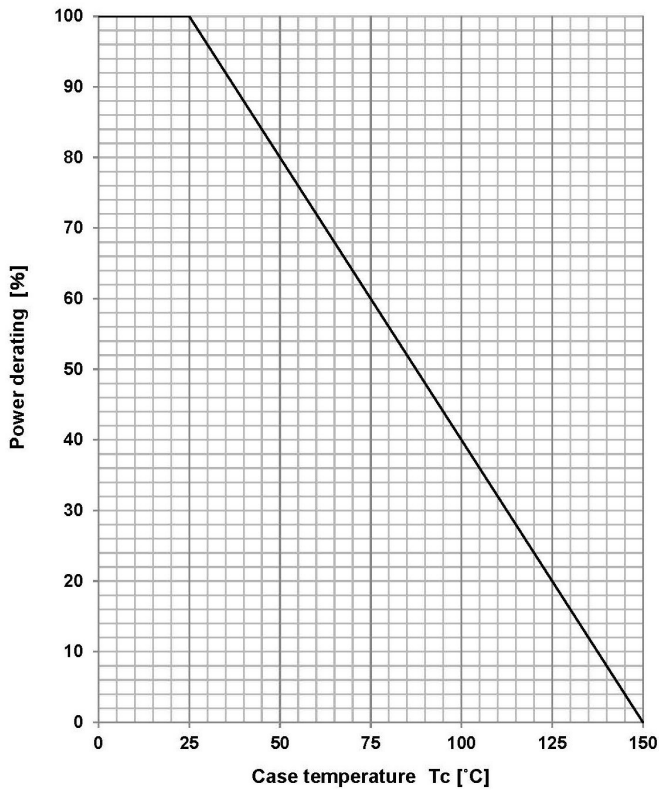
<Substrate detail>

Type	Alumina
Size	1 inch ²
Thickness	0.64 mm
Conductor thickness	20 μm
Pattern area	65 mm ²

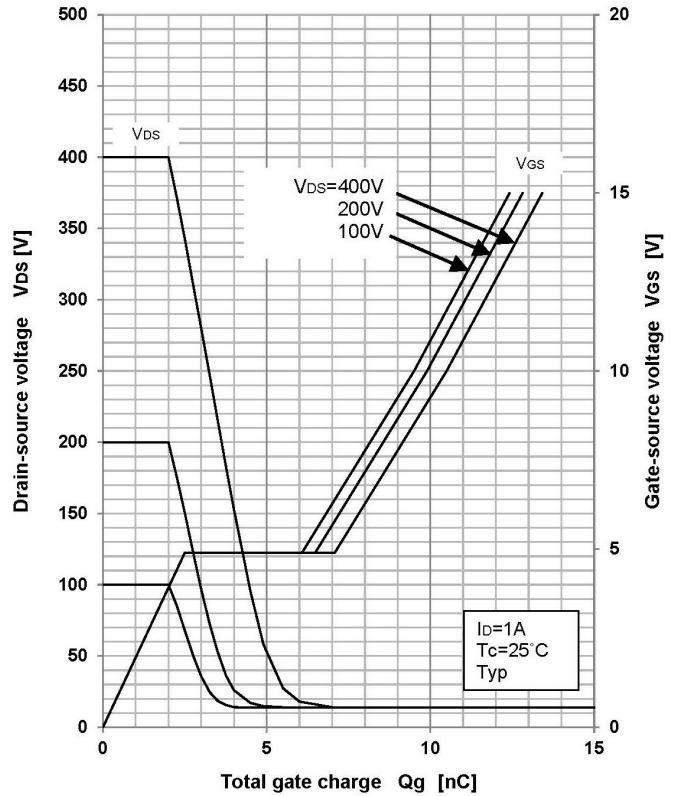
Capacitance characteristics

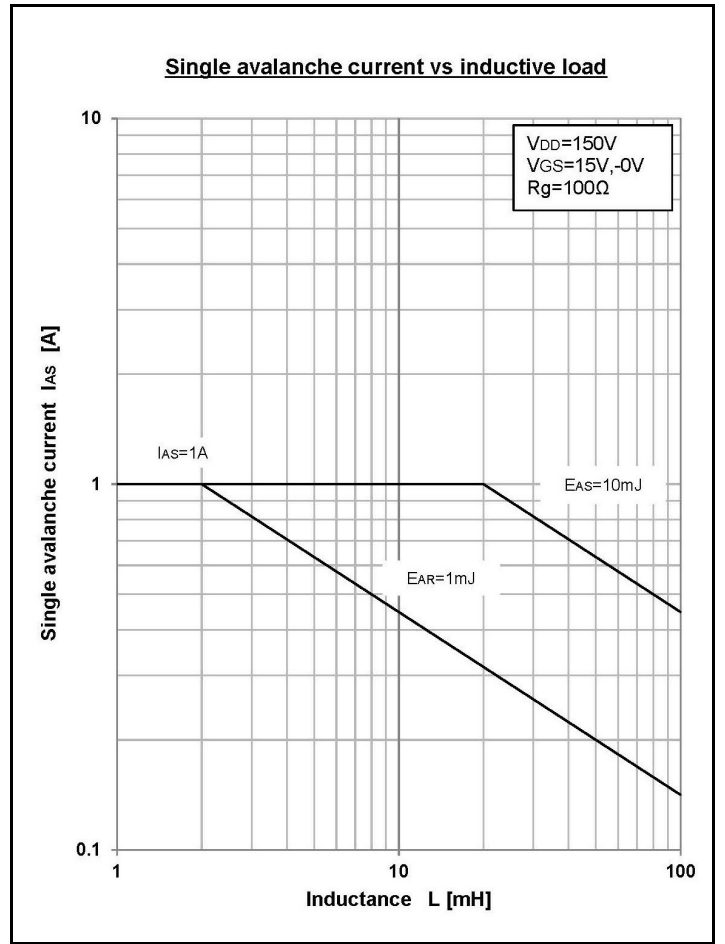
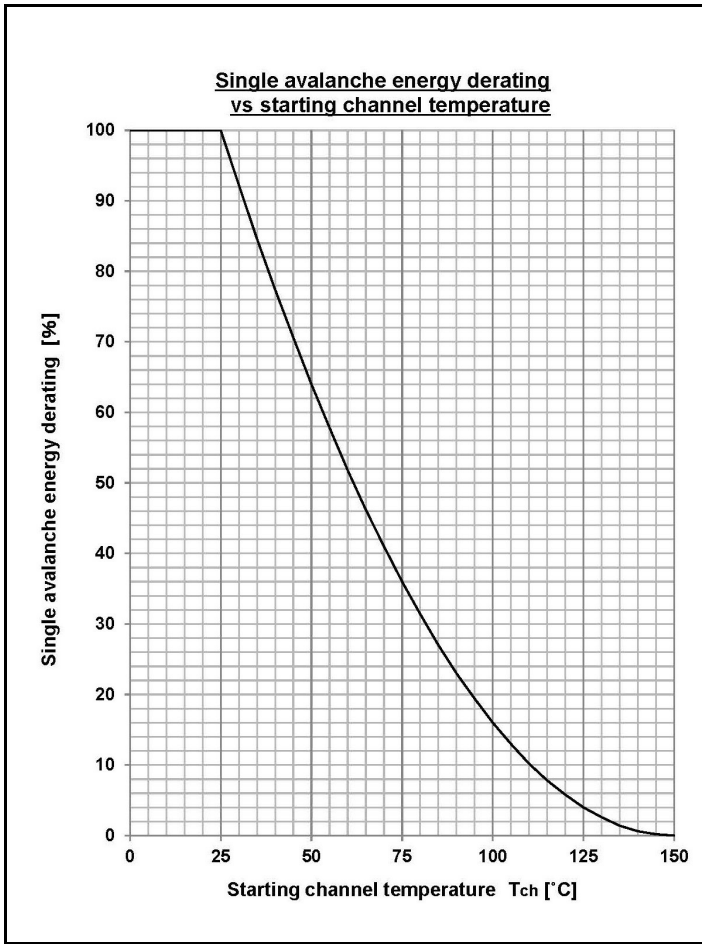


Power derating vs case temperature



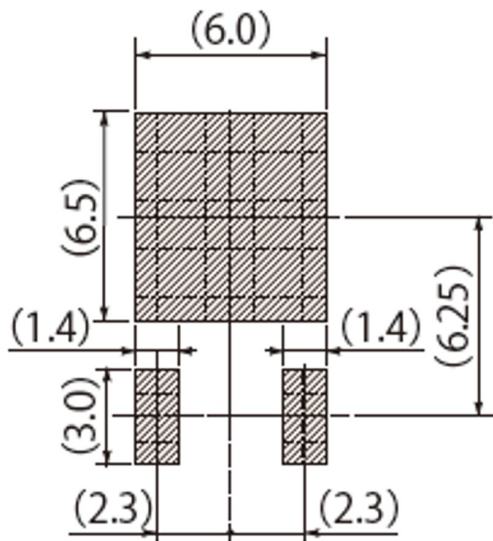
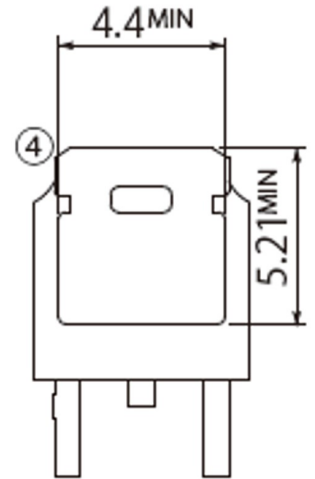
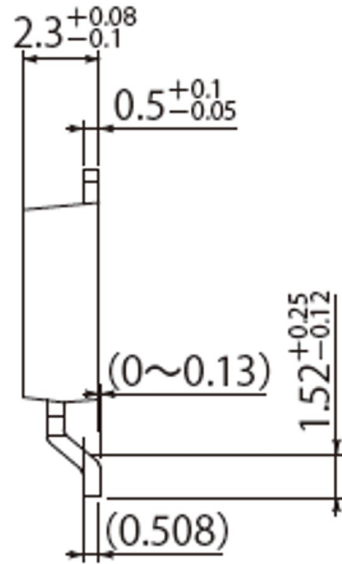
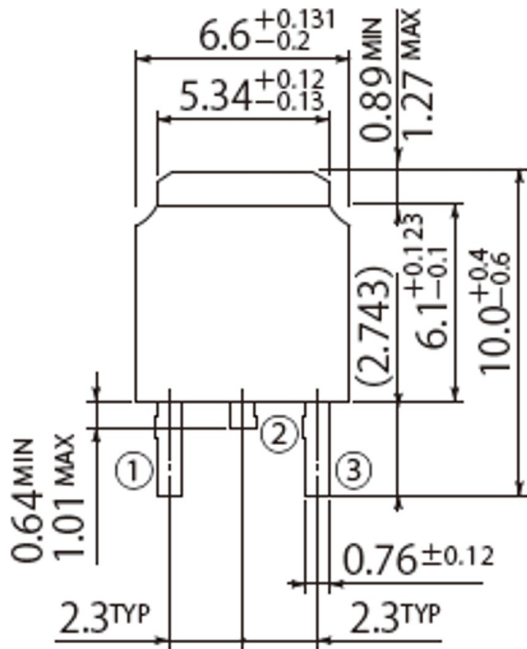
Gate charge characteristics





G2

JEDEC Code	TO-252AA
JEITA Code	-
House Name	FB



Referential Soldering Pad

Notes

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