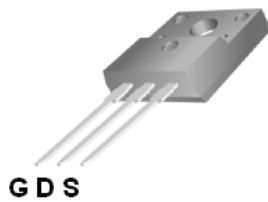


P2060ZTF / P2060ZTFS

N-Channel High Voltage Mode MOSFET

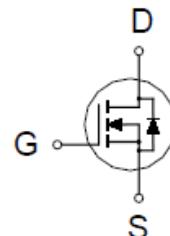
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
600V	190mΩ @ $V_{GS} = 10V$	20A



TO-220F

TO-220FS



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current ²	I_D	20 12	A
$T_C = 25^\circ C$			
$T_C = 100^\circ C$			
Pulsed Drain Current ¹	I_{DM}	59	
Avalanche Current ³	I_{AS}	4	A
Avalanche Energy ³	E_{AS}	320	mJ
Power Dissipation	P_D	48 19	W
$T_C = 25^\circ C$			
$T_C = 100^\circ C$			
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.6	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Ensure that the channel temperature does not exceed 150°C.

³ $V_{DD} = 50V$, $L = 40mH$, starting $T_J = 25^\circ C$

P2060ZTF / P2060ZTFS

N-Channel High Voltage Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	600			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3.1	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 30V$			± 100	nA
Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_C = 25^\circ\text{C}$			1	μA
		$V_{DS} = 480V, V_{GS} = 0V, T_C = 100^\circ\text{C}$			100	
Drain-Source On-State Resistance ¹	$R_{DS(\text{ON})}$	$V_{GS} = 10V, I_D = 10\text{A}$		160	190	$\text{m}\Omega$
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 10\text{A}$		15		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1\text{MHz}$		1728		pF
Output Capacitance	C_{oss}			1096		
Reverse Transfer Capacitance	C_{rss}			25		
Effective Output Capacitance ⁴	$C_{o(er)}$	$V_{GS} = 0V, V_{DS} = 0 \text{ to } 480V$		82		
Total Gate Charge ²	Q_g	$V_{DD} = 480V, V_{GS} = 10V, I_D = 10\text{A}$		60		
Gate-Source Charge ²	Q_{gs}			10		
Gate-Drain Charge ²	Q_{gd}			29		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 300V, I_D = 10\text{A}, R_G = 10\Omega$		39		nS
Rise Time ²	t_r			94		
Turn-Off Delay Time ²	$t_{d(off)}$			105		
Fall Time ²	t_f			54		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current ³	I_S				20	A
Forward Voltage ¹	V_{SD}	$I_F = 10\text{A}, V_{GS} = 0V$			1.5	V
Reverse Recovery Time	t_{rr}	$I_F = 10\text{A}, dI_F/dt = 100\text{A} / \mu\text{s}$		395		nS
Reverse Recovery Charge	Q_{rr}			4.3		μC

¹Pulse test : Pulse Width $\leq 380 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

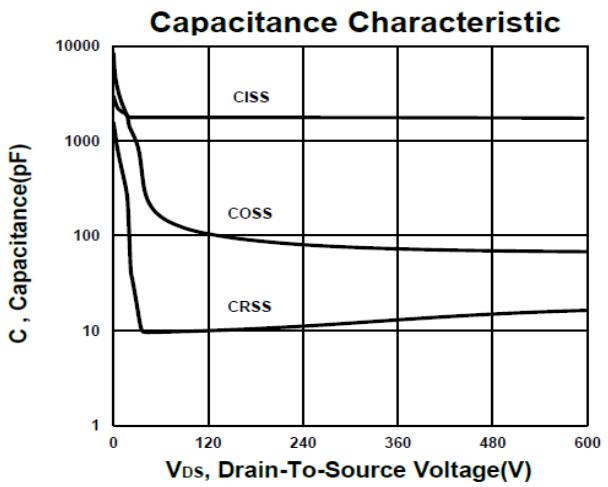
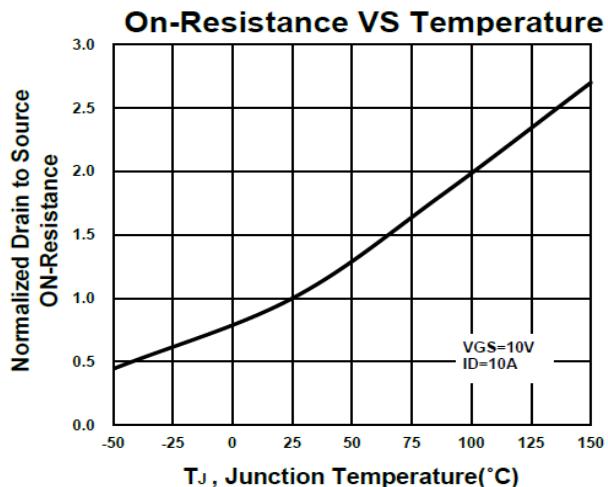
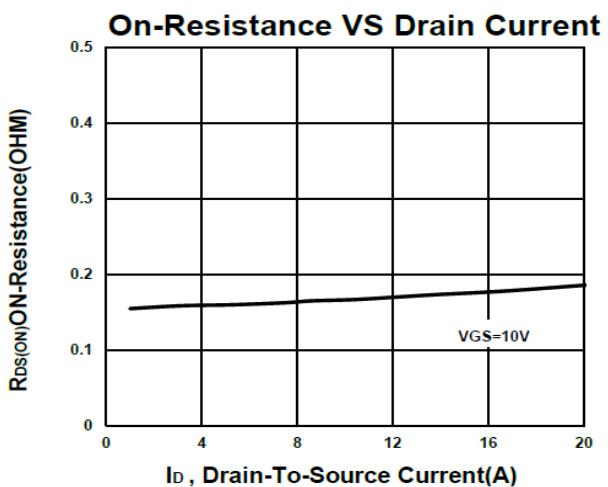
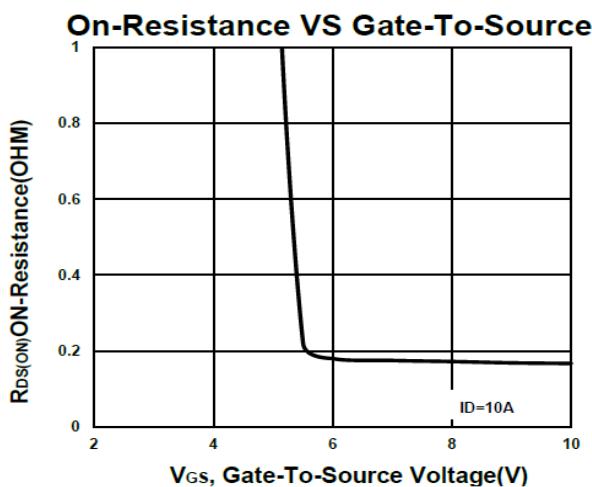
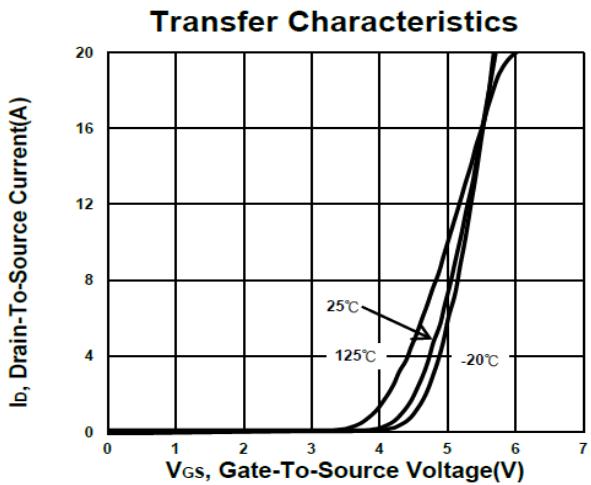
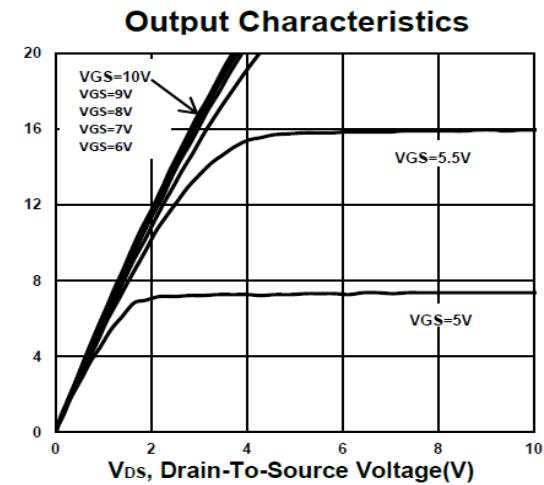
²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

⁴ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% $V_{(\text{BR})\text{DSS}}$.

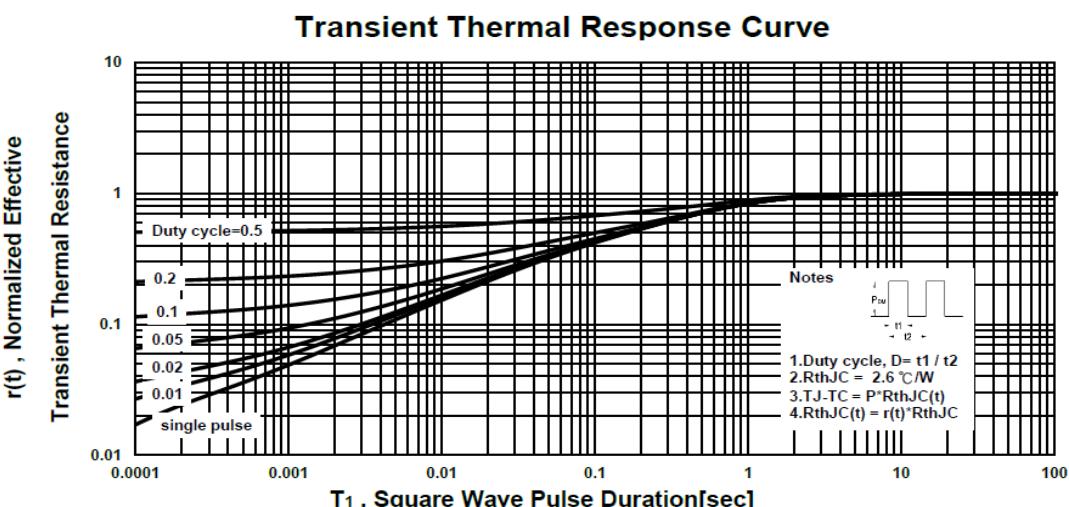
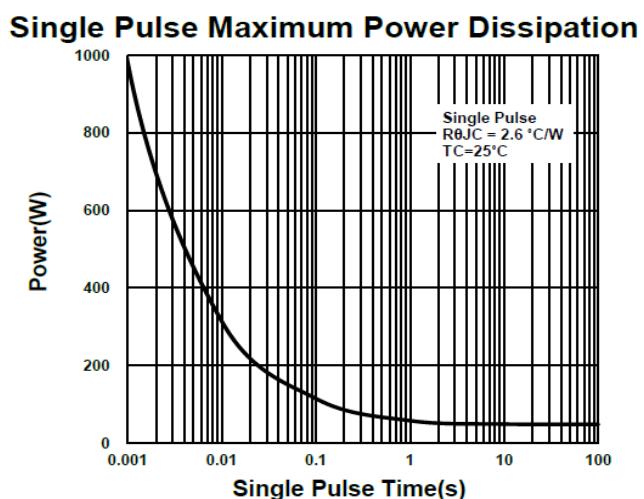
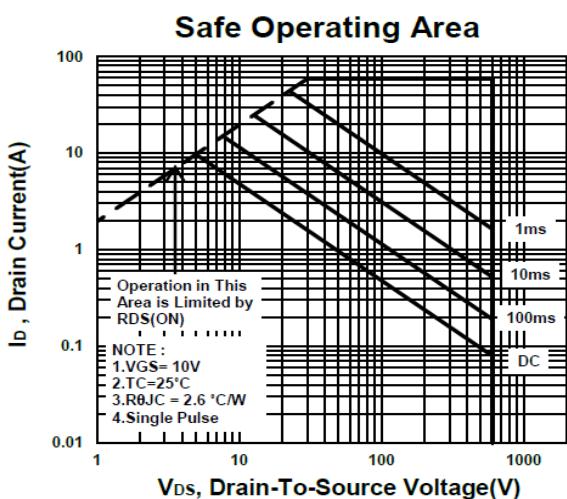
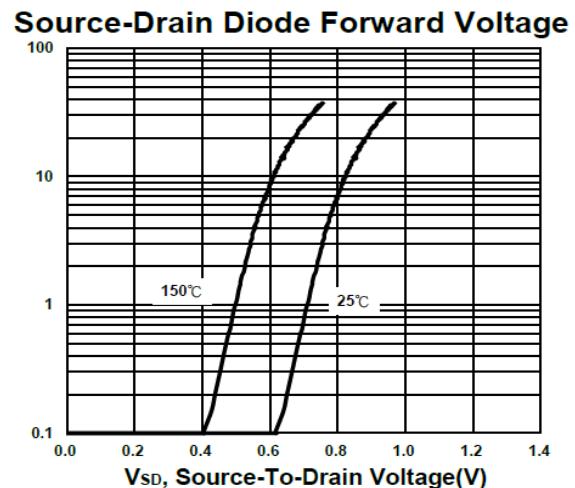
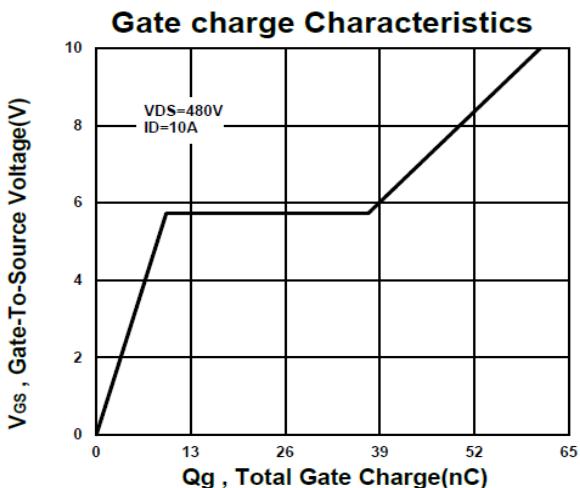
P2060ZTF / P2060ZTFS

N-Channel High Voltage Mode MOSFET



P2060ZTF / P2060ZTFS

N-Channel High Voltage Mode MOSFET

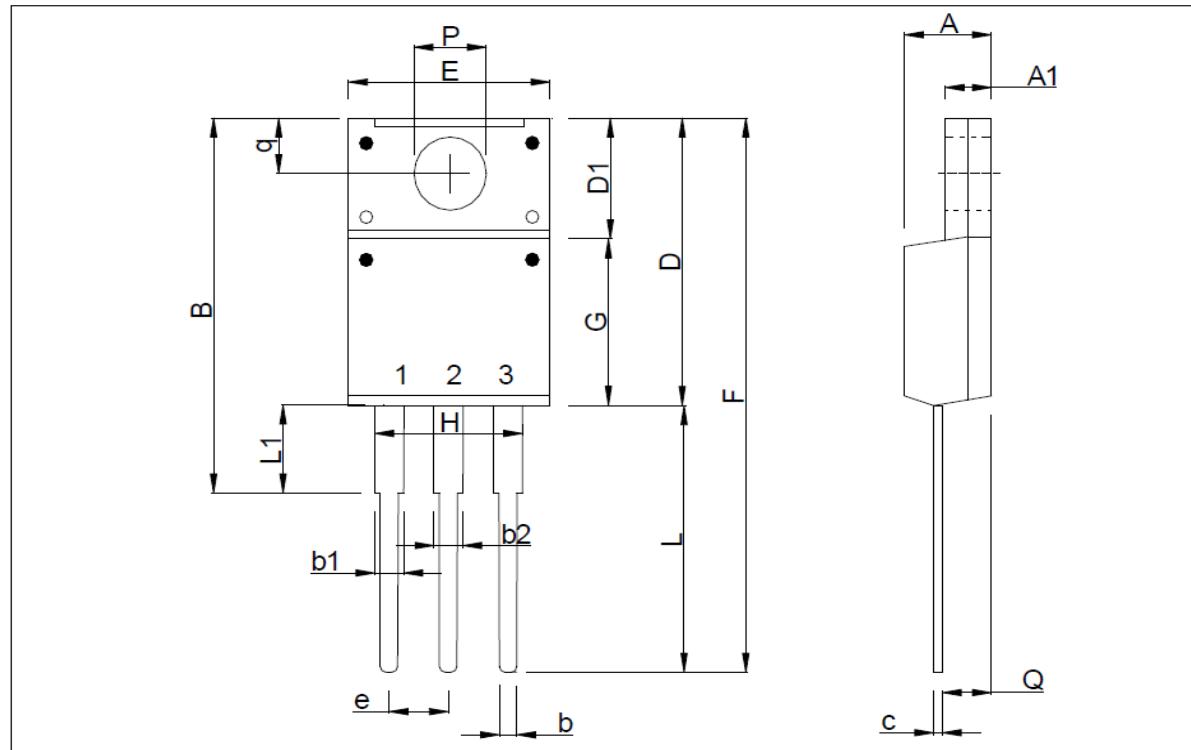


P2060ZTF / P2060ZTFS N-Channel High Voltage Mode MOSFET

Package Dimension

TO-220F (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.93	e	2.05	2.55	3.05
A1	2.34		3.1	F	27.45		30.6
B	17.77		20.3	G	7.72		9.3
b	0.6		1.05	H	6.1		7.1
b1	0.9	1.23	1.62	L	12.5		14.5
b2	0.6		1.9	L1	1.97		3.8
c	0.4		1.0	P	2.98		3.4
D	14.7		16.4	Q	2.1		2.96
D1	6.4		7.5	q	3.0		3.8
E	9.7		10.4				

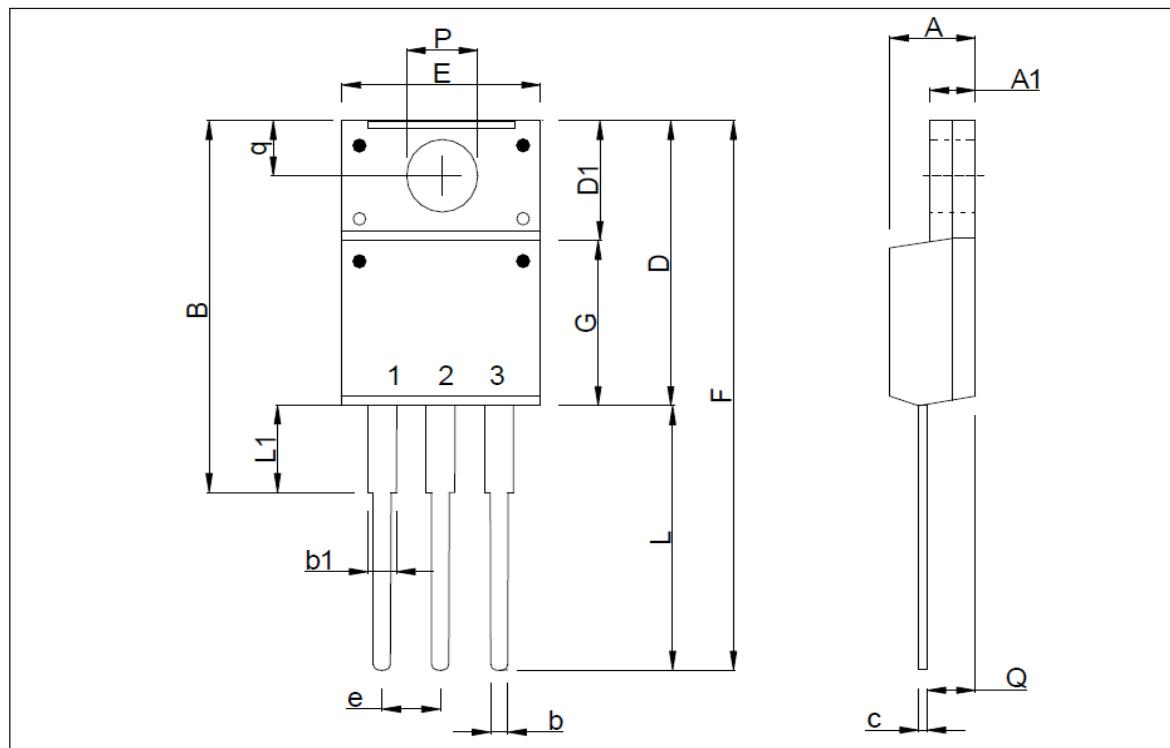


P2060ZTF / P2060ZTFS N-Channel High Voltage Mode MOSFET

Package Dimension

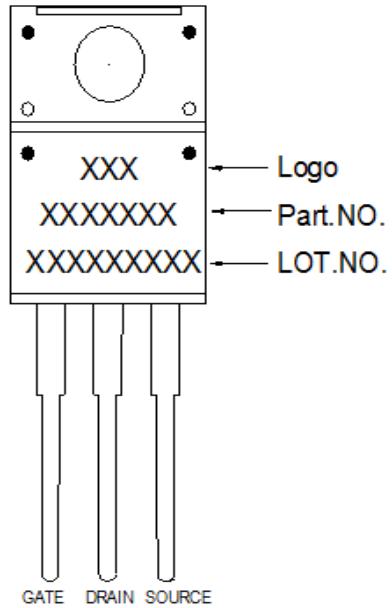
TO-220FS (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2	4.7	4.93	e	2.05	2.54	3.05
A1	2.34	2.8	3.1	F	28.04		30.3
B	17.7		20.3	G	8.2	8.87	9.57
b	0.65	0.8	1.05	L	12.37		14.3
b1	0.9	1.3	1.5	L1	1.4	2.3	2.5
c	0.4	0.7	1.0	P	2.98	3.2	3.4
D	15.37		16.3	Q	2.1	2.6	2.96
D1	5.5		7.5	q	3.0	3.5	3.8
E	9.7	10.16	10.36				

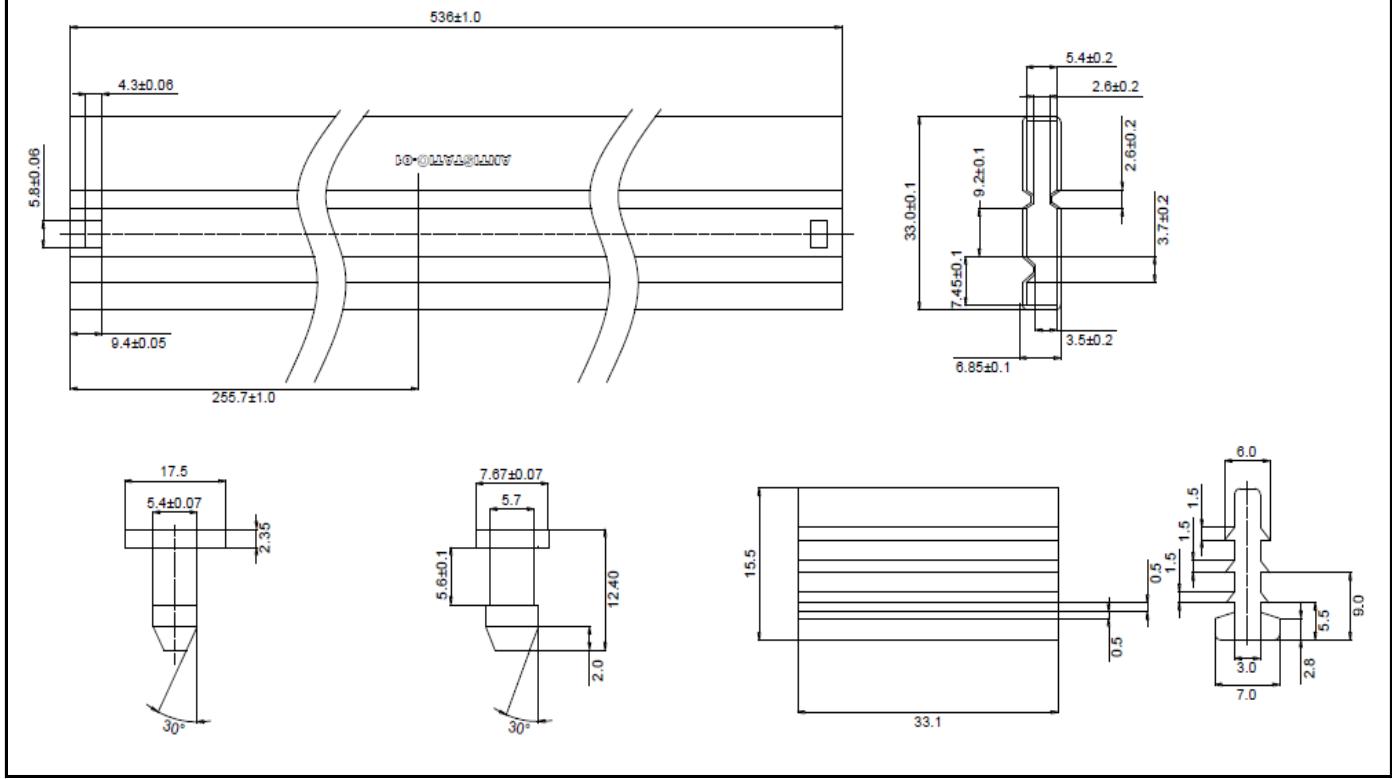


P2060ZTF / P2060ZTFS N-Channel High Voltage Mode MOSFET

A. Marking Information



B. Tape&Reel Information: 50pcs/Tube(2000pcs/Box)

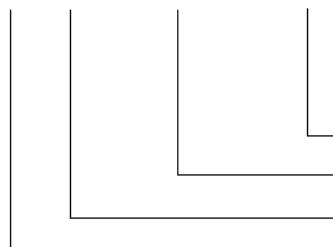


P2060ZTF / P2060ZTFS N-Channel High Voltage Mode MOSFET

C. Lot.No. & Date Code rule

1.LOT.NO.

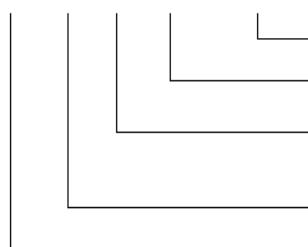
M N 15M21 03



- #8~9 Sub-lot No
- Order series no.
- Foundry site
- Assembly site

2.Date Code

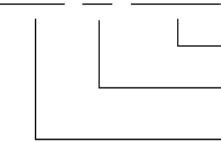
D Y M X XXX



- Order series no. & Sub-lot No
- Week
- M : Month (A:Jan , B:Feb , C:Mar ,D :Apr ,E:May ,F:Jun,G:Jul,H:Aug,I:Sep,J:Oct,K:Nov,L:Dec.)
- Y : Year (N : 2011, O : 2012 ...)
- Assembly site

3.Date Code (for Small package)

XX Y WW



- Week
- Y : Year (9: 2009,A : 2010, B : 2011 ...)
- Device Name

P2060ZTF / P2060ZTFS N-Channel High Voltage Mode MOSFET

D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert “ / ” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least