Model Name: P215HAN01.0

Issue Date : 2016/05/24

(＊)Preliminary Specifications
( )Final Specifications

Customer Signature	Date	AUO	Date
Approved By		Approval By PM Director Kelly Kao	
Note		Reviewed By RD Director Edward Lai Reviewed By Project Leader Jerry Lin	
		Prepared By PM Robyn Huang	

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Record of Revision

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0.0	2016/05/24		First release
0.1	2016/07	4	Power consumption value
		5	Is=65→55mA Modify value of Rx,Ry,Gx,Gy,Bx,By
0.2	2016/07/22	10	1.3.1. Placement Suggestions
		14	2.1. Block Diagram
		16	Modify Note 3-1: Inrush Current measurement
		28	LED String Current 3.3.2. Recommended Operating Condition Note 4-1 a. VF: LED chip forward voltage, VF (Min.)= 2.8V, VF(Typ.)=3.0V, VF(Max.)=3.2V Note 4-4: Definition of life time: b. Test condition: Is =55mA and 25℃ (Room Temperature) Change Is from 70mA to 55mA on “Vs and ΔVs”

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1. General Description

This specification applies to the 21.5 inch wide Color a-Si TFT-LCD Module P215HAN01.0. The display supports the Full HD - 1920(H) x 1080(V) screen format and 16.7M colors (8 bits RGB data input). The input interface is Dual channel LVDS and this module doesn't contain a driver board for backlights.

*** General Information**

1.1. Display Characteristics

The following items are characteristics summary on the table under 25℃ condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	546.86 (21.5")
Active Area	[mm]	476.064 (H) x 267.786 (V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	247.95 (per one triad) ×247.95
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	AHVA, normally Black
White Luminance (Center)	[cd/m2]	300 (Typ.)
Contrast Ratio	-	1000 (Typ.)
Response Time	[msec]	14 (Typ., G/G)
Power Consumption (LCD Module + Backligh unit)	[Watt]	13.5 (Typ.) LCD module : PDD (Typ.)= 2.3 @ White pattern,Fv=60Hz Backlight unit : P_{BLU} (Typ.) =11.2 @Is= 55 mA
Weight	[Grams]	1625 (TBD)
Outline Dimension	[mm]	489.3(H) × 287.0(V) ×12.3 (D) Typ (CNT 13.18)
Electrical Interface	-	Dual channel LVDS , 8-bit RGB data input
Support Color	-	16.7M colors
Surface Treatment	-	Anti-Glare, 3H
Temperature Range Operating Storage (Shipping)	 [℃] [℃]	 0 to +50 -20 to +60
RoHS Compliance	-	RoHS Compliance

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1.2. Optical Characteristics

The optical characteristics are measured on the following test condition.

Test Condition:

1. Equipment setup: Please refer to Note 1-1.
2. Panel Lighting time: 30 minutes
3. VDD=5.0V, Fv=60Hz,Is=55mA,Ta=25℃

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Lw	White Luminance (Center of screen)		240	300	-	[cd/m2]	Note 1-1 By SR-3
Luni	Luminance Uniformity (9 points)		75	80	-	[%]	Note 1-2 By SR-3
CR	Contrast Ratio (Center of screen)		600-	1000	-	-	Note 1-3 By SR-3
θR	Horizontal Viewing Angle (CR=10)	Right	75	89	-	[degree]	Note 1-4 By SR-3
θL		Left	75	89	-		
ΦH	Vertical Viewing Angle (CR=10)	Up	75	89	-		
ΦL		Down	75	89	-		
θR	Horizontal Viewing Angle (CR=5)	Right	75	89	-		
θL		Left	75	89	-		
ΦH	Vertical Viewing Angle (CR=5)	Up	75	89	-		
ΦL		Down	75	89	-		
T_{GTG}	Response Time	Gray to Gray	-	14	-	[msec]	Note 1-5 By TRD-100
Rx	Color Coordinates (CIE 1931)	Red x	0.617	0.647	0.677	-	By SR-3
Ry		Red y	0.304	0.334	0.364		
Gx		Green x	0.290	0.320	0.350		
Gy		Green y	0.595	0.625	0.655		
Bx		Blue x	0.125	0.155	0.185		
By		Blue y	0.020	0.050	0.080		
Wx		White x	0.283	0.313	0.343		
Wy		White y	0.299	0.329	0.359		
NTSC Area Ratio				72		[%]	By SR-3
CT	Crosstalk		-	-	1.5	[%]	Note 1-6 By SR-3

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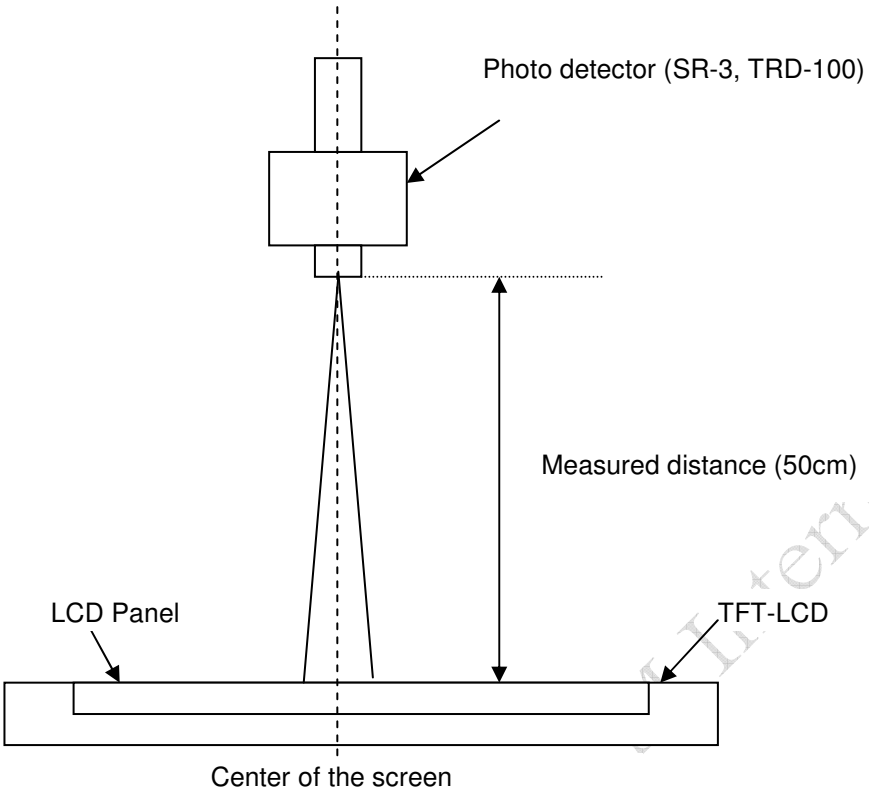
Note 1-1: Equipment setup :

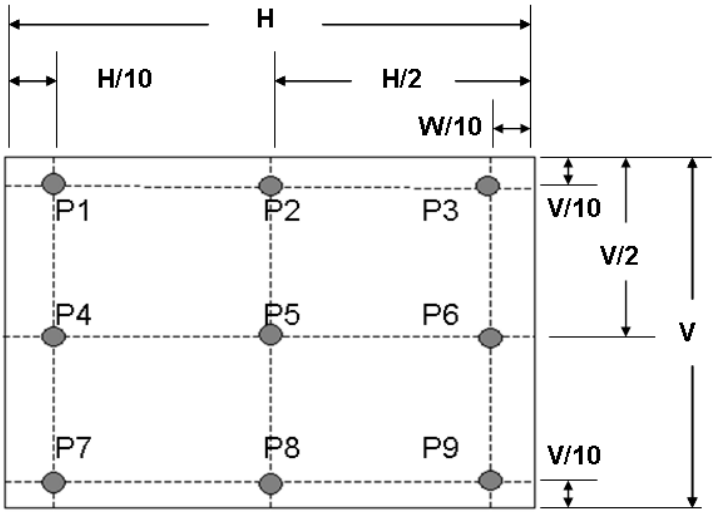


Note 1-2*:* Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 Points (P1 ~ P9)}}{\text{Maximum Luminance of 9 Points (P1 ~ P9)}}$$

a. Test pattern: White Pattern



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Note 1-3: Contrast Ratio Measurement

Definition:

$$\text{Contrast Ratio} = \frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$$

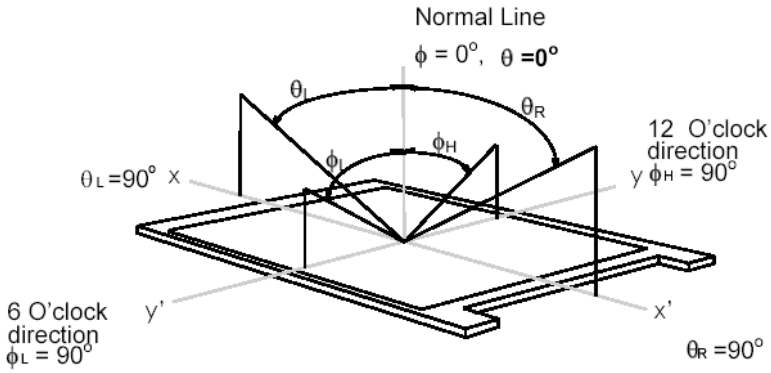
a. Measured position: Center of screen (P5) & perpendicular to the screen (θ=Φ=0°)

Note 1-4: Viewing angle measurement

Definition: The angle at which the contrast ratio is greater than 10 & 5 .

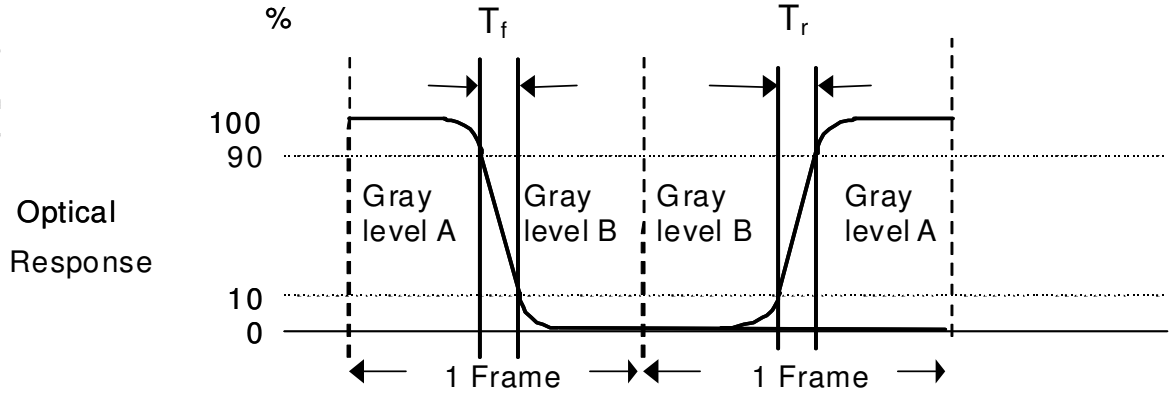
a. Horizontal view angle: Divide to left & right (θL & θR)

Vertical view angle: Divide to up & down (ΦH &ΦL)



Note 1-5: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Gray level A" to "Gray level B" (falling time, T_F), and from "Gray level B" to "Gray level A" (rising time, T_R), respectively. The response time is interval between the 10% and 90% of optical response.
The gray to gray response time is defined as the following table.



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Gray Level to Gray Level		Target gray level				
		L0	L63	L127	L191	L255
Start gray level	L0					
	L63					
	L127					
	L191					
	L255					

■ T_{GTG_typ} is the total average time at rising time and falling time of gray to gray.

Note 1-6: Crosstalk measurement

Definition:

CT = Max. (CTH,CTV);

Where

a. Maximum Horizontal Crosstalk :

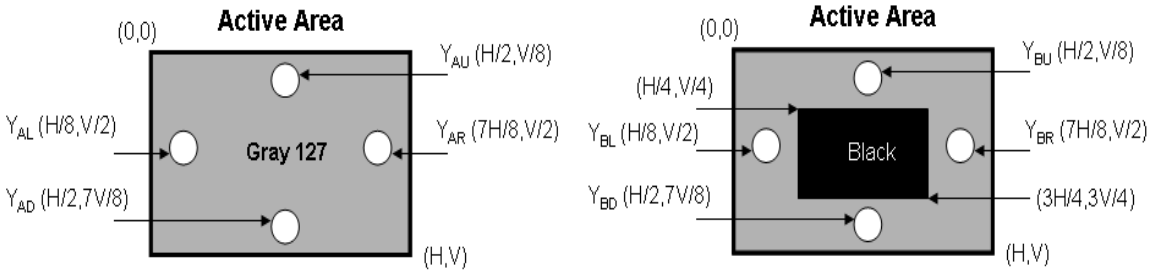
CTH = Max. (| YBL – YAL | / YAL × 100 %, | YBR – YAR | / YAR × 100 %);

Maximum Vertical Crosstalk:

CTV = Max. (| YBU – YAU | / YAU × 100 %, | YBD – YAD | / YAD × 100 %);

b. YAU, YAD, YAL, YAR = Luminance of measured location without Black pattern

YBU, YBD, YBL, YBR = Luminance of measured location with Black pattern



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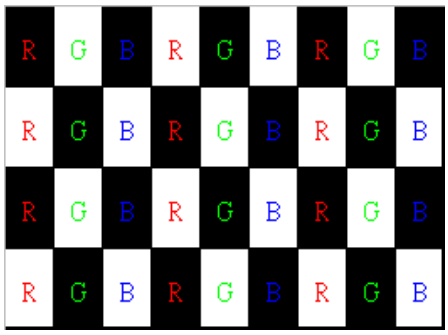
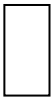
Note 1-7: Flicker measurement

a. Test pattern: It is listed as following.



Gray level = L0



Gray level = L127

R: Red, G: Green, B: Blue

b. Measured position: Center of screen (P5) & perpendicular to the screen (θ=Φ=0°)

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1.3. Mechanical Characteristics

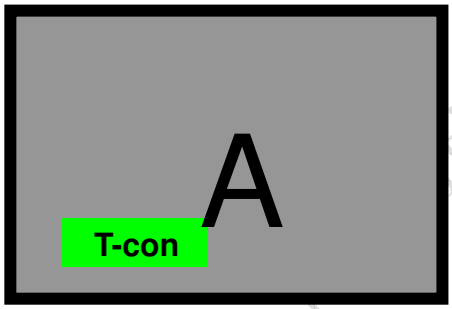
The contents provide general mechanical characteristics for the model PXXXXXXXX.X In addition the figures in the next page are detailed mechanical drawing of the LCD.

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	489.3	mm	
	Vertical	287.0	mm	
	Depth (typ)	12.3	mm	
Weight	1625		G	

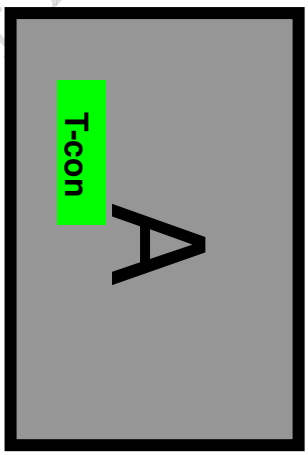
1.3.1. *Placement Suggestions*

1. Landscape Mode: The default placement is T-Con Side on the lower side and the image is shown upright via viewing from the front.
2. Portrait Mode: The default placement is that T-Con side has to be placed on the left side via viewing from the front.

Landscape (Front view)



Portrait (Front view)



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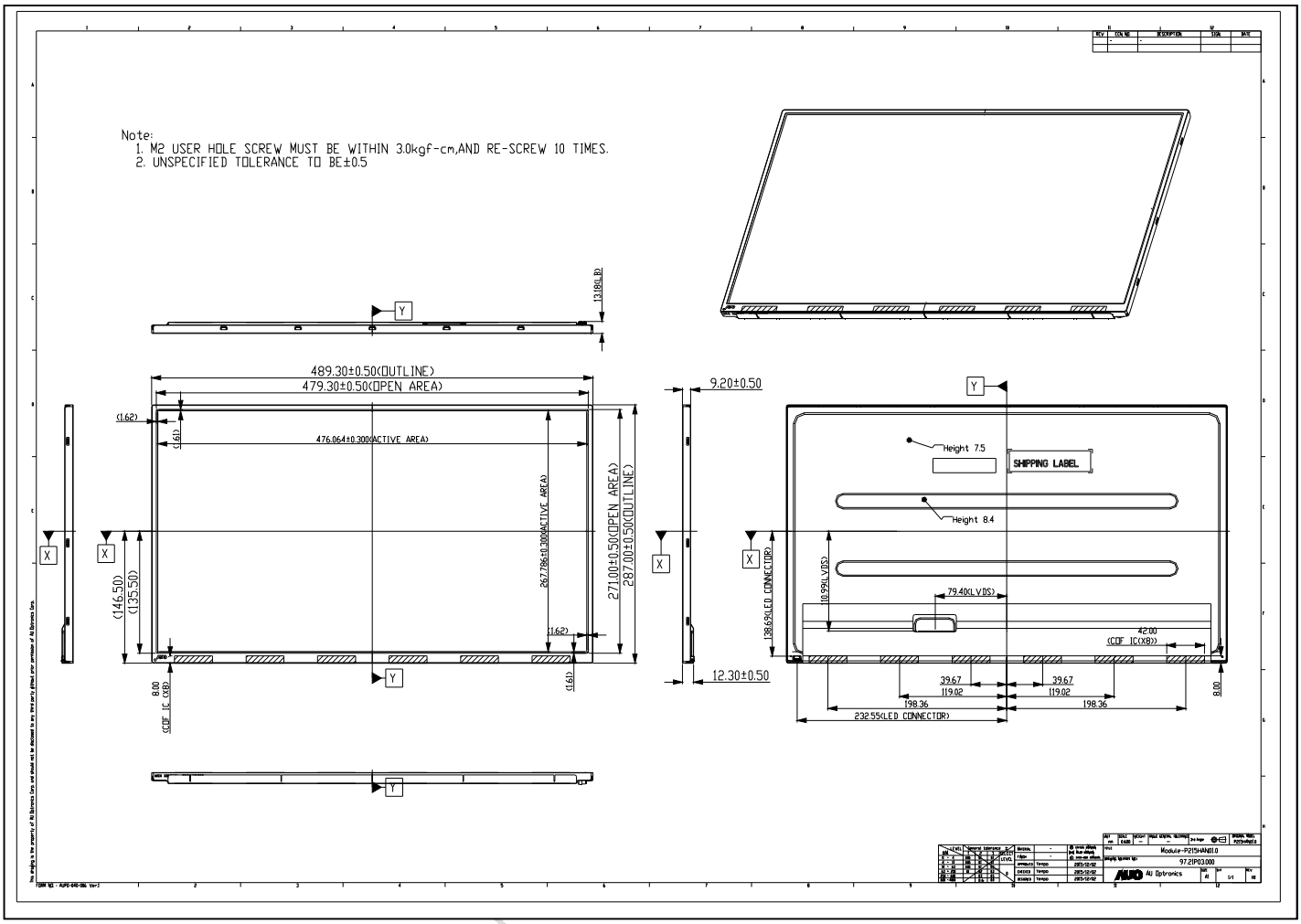
Mechanical Characteristics



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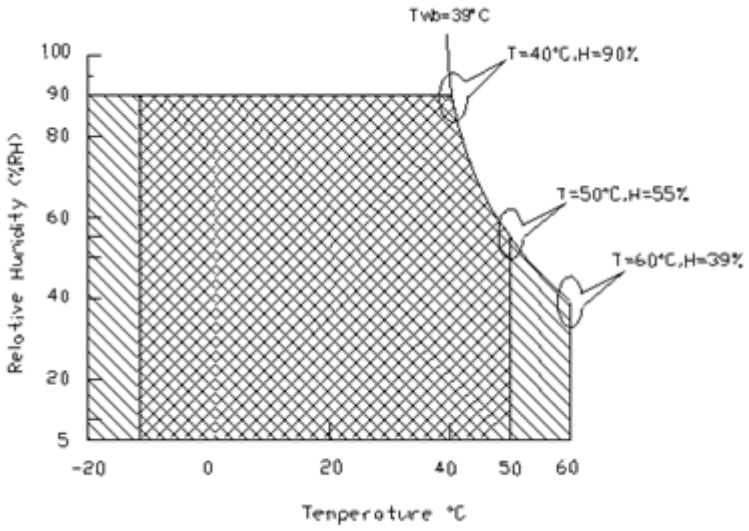
Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[oC]	Note 2-1
TGS	Glass surface temperature (operation)	0	+65	[oC]	Note 2-1 Function judged only
HOP	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[oC]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

1. 90% RH Max (Ta ≦39℃)
2. Max wet-bulb temperature at 39℃ or less. (Ta ≦39℃)
3. No condensation



Operating Range Storage Range +

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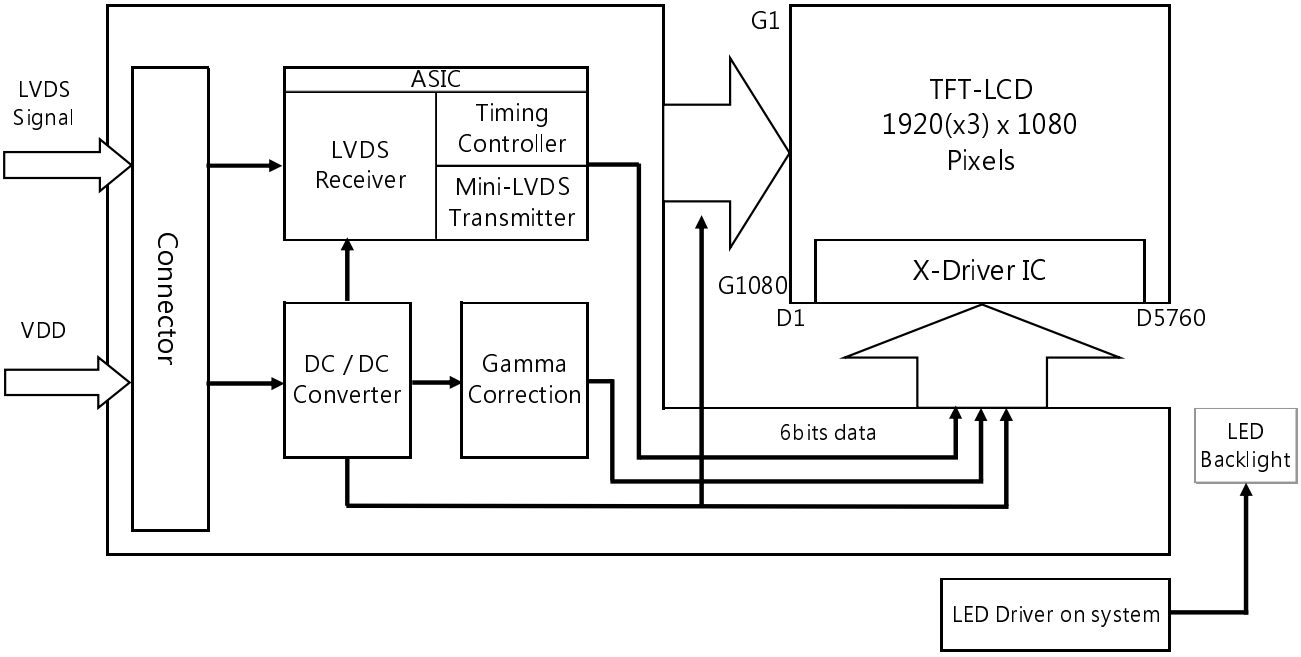
2. Electrical Specification

The P215HAN01.0 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other is to power Back Light Unit.

2.1. Block Diagram

The following shows the block diagram of the 21.5 inch Color TFT-LCD Module.



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2.2. Interface Connection

2.2.1. Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM
	Part Number	187034-3009	MSBKT2407P30HB
Mating Connector	Manufacturer	JAE	
	Part Number	FI-X30HL (Locked Type)	

2.2.2. Connector Pin Assignment

PIN #	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	GND	Ground	
25	NC	No connection (for AUO test only. Do not connect)	
26	NC	No connection (for AUO test only. Do not connect)	
27	NC	No connection (for AUO test only. Do not connect)	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	

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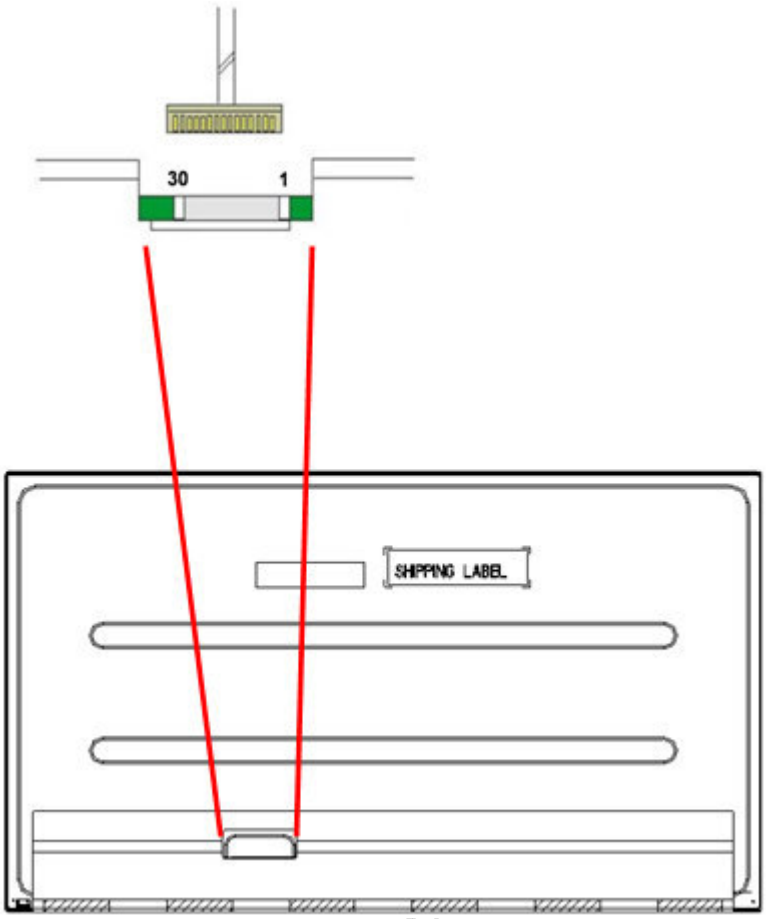

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2.3. Electrical Characteristics

2.3.1. Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

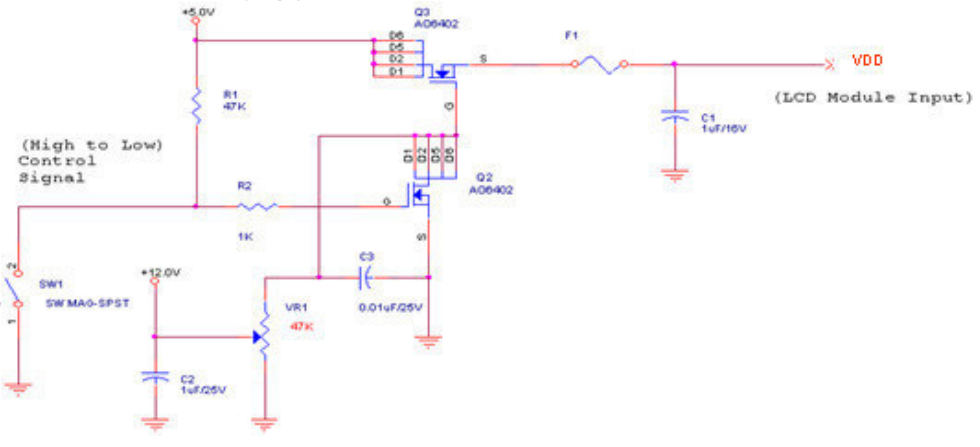
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25℃

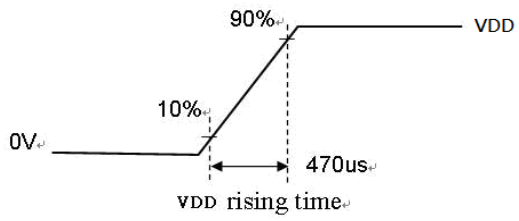
2.3.2. Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.46	0.55	[A]	VDD= 5.0V, All white Pattern, Fv=60Hz
			0.50	0.60	[A]	VDD= 5.0V, All white Pattern, Fv=75Hz
PDD	VDD PowerVDD Power Consumption	-	2.30	2.75	[Watt]	VDD= 5.0V, All white Pattern, Fv=60Hz
			2.50	3.00	[Watt]	VDD= 5.0V, All white Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 3-1
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, All white Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:

Test circuit:





The duration of VDD rising time: 470us.

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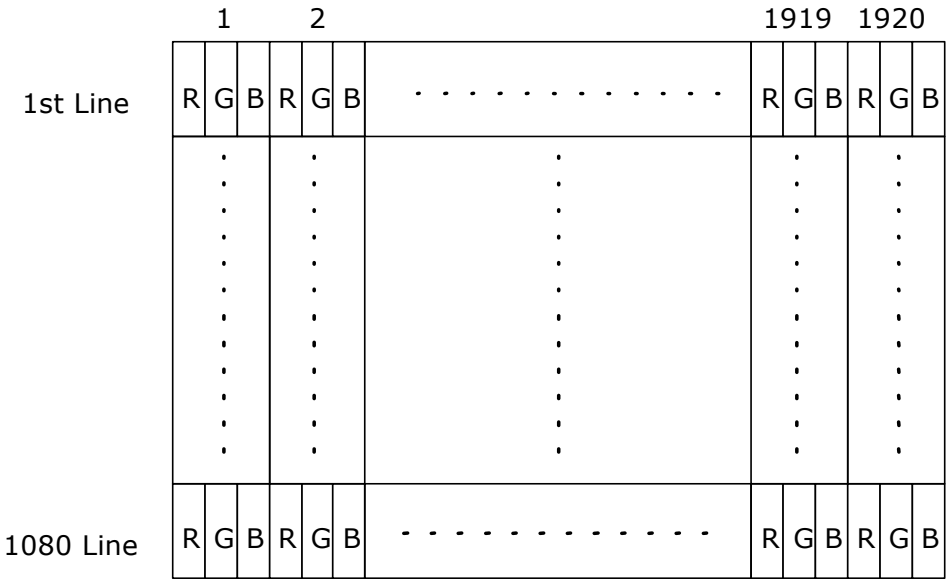
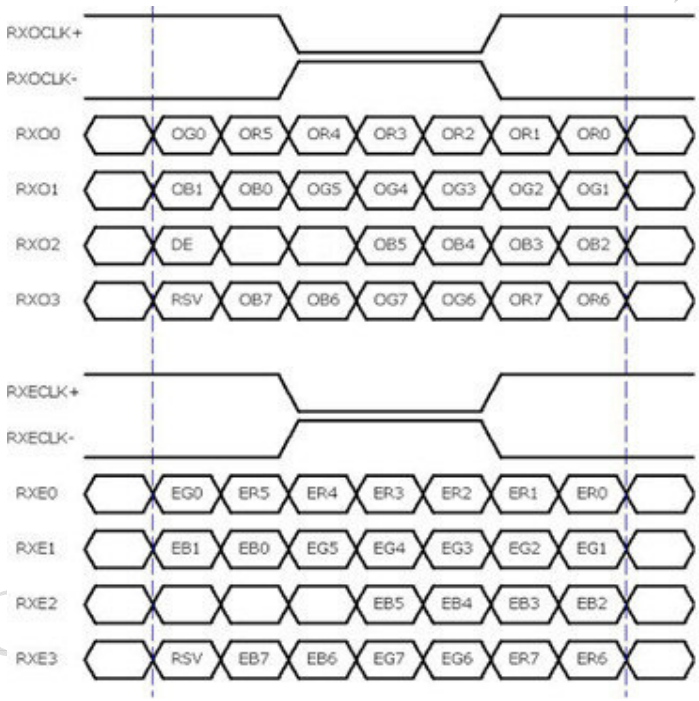
2.4. Signal Characteristics

2.4.1. LCD Pixel Format



2.4.2. LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 3-2:

a. O = “Odd Pixel Data” E = “Even Pixel Data”

b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

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2.4.3. Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB:R7, LSB:R0)								GREEN data (MSB:G7, LSB:G0)								BLUE data (MSB:B7, LSB:B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

2.4.4. LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
VTH	LVDS Differential Input High Threshold	-	-	+100	[mV]	VCM = 1.2V
VTL	LVDS Differential Input Low Threshold	-100	-	-	[mV]	VCM = 1.2V
|VID|	LVDS Differential Input Voltage	100	-	600	[mV]	
VCM	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	VTH-VTL = 200mV

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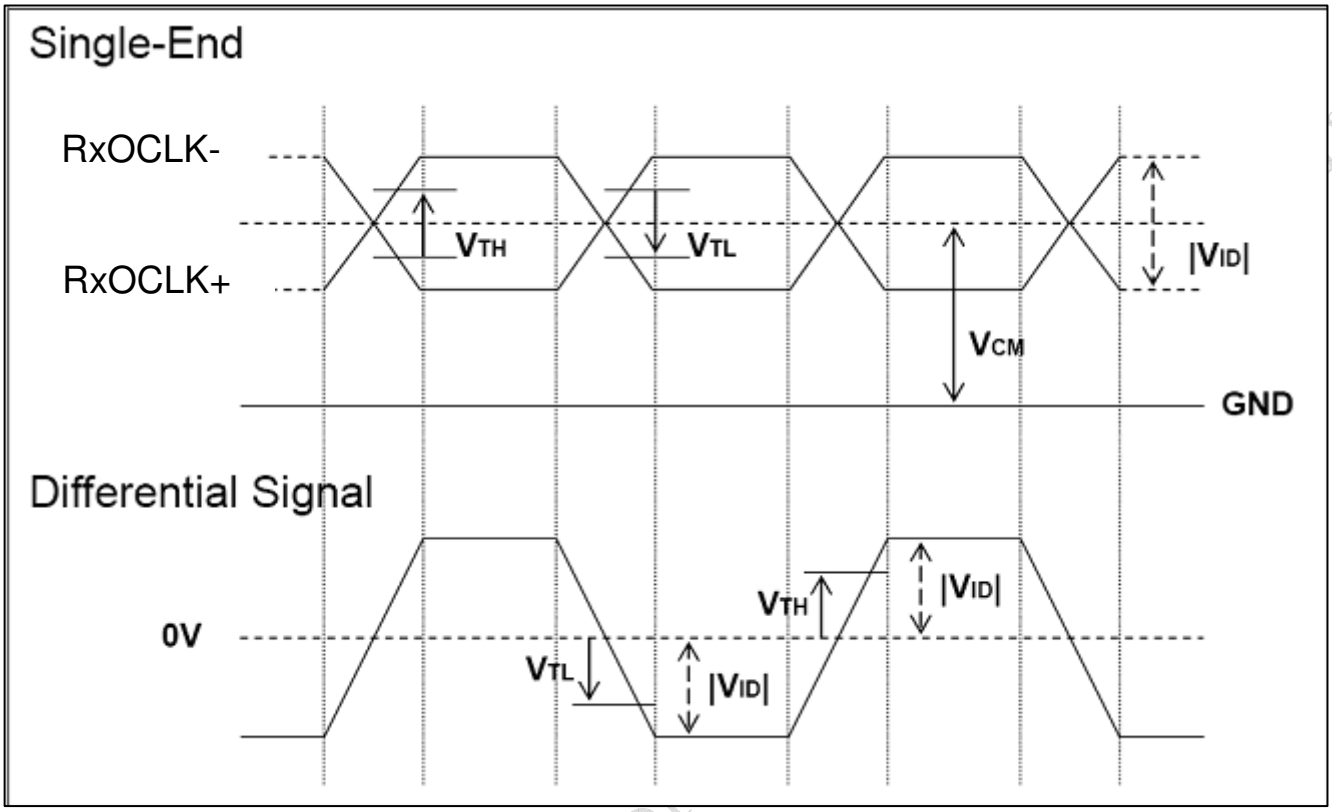
LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
FDEV	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
FMOD	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	

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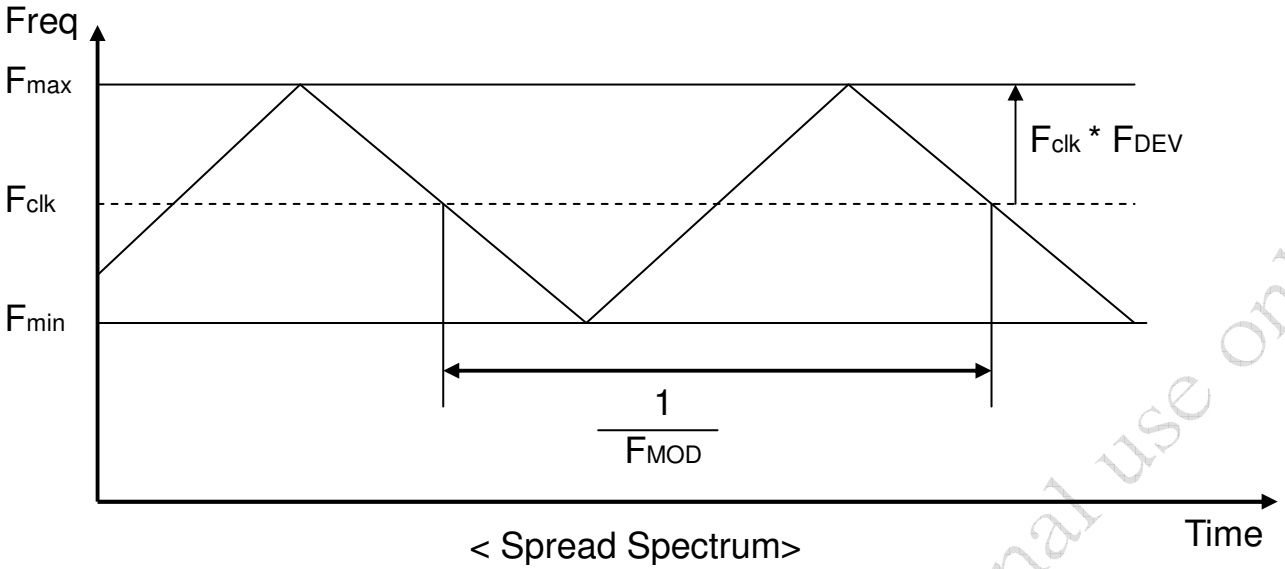

Fclk: LVDS Clock Frequency

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2.4.5. Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1094	1130	1836	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	756	Th	
Fv		Frequency	49	60	76	Hz	
Th	Horizontal Section	Period	1000	1050	1678	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	40	90	718	Tclk	
Fh		Frequency	53.7	67.8	90.0	KHz	***Note 3-3***
Tclk	LVDS Clock	Period	11.2	14.0	18.6	ns	1/Fclk
Fclk		Frequency	53.7	71.2	90.0	MHz	***Note 3-4***

Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

Fh (Min.) = Fclk (Min.) / Th (Min.);
Fh (Typ.) = Fclk (Typ.) / Th (Typ.);
Fh (Max.)= Fclk (Max.) / Th (Min.);

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

Fclk (Min.) = Fv (Min.) x Th (Min.) x Tv (Min.);

Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.);

Fclk (Max.) = Fv (Max.) x Th (Typ.) x Tv (Typ.);

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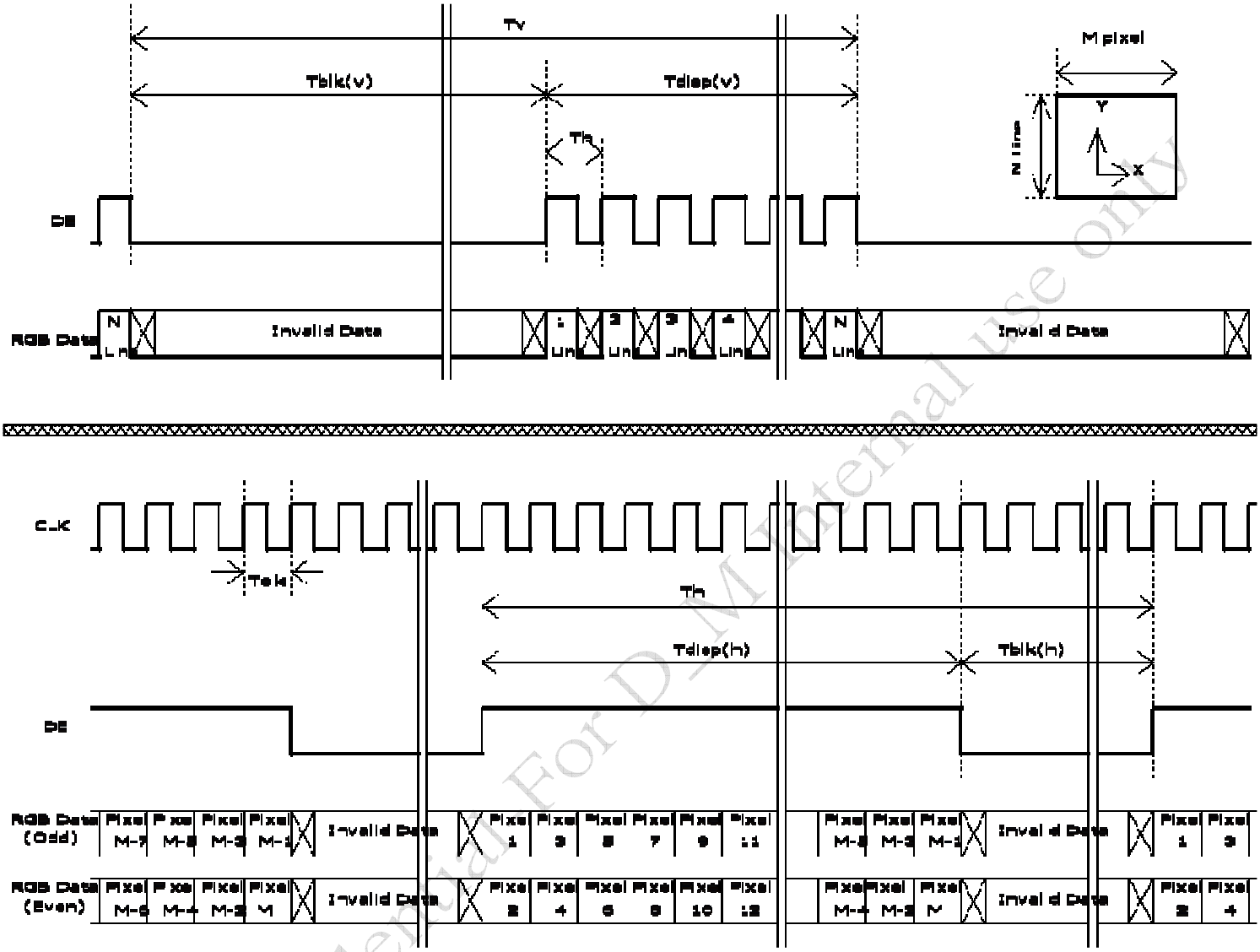
2.4.6. *Input Timing Diagram*



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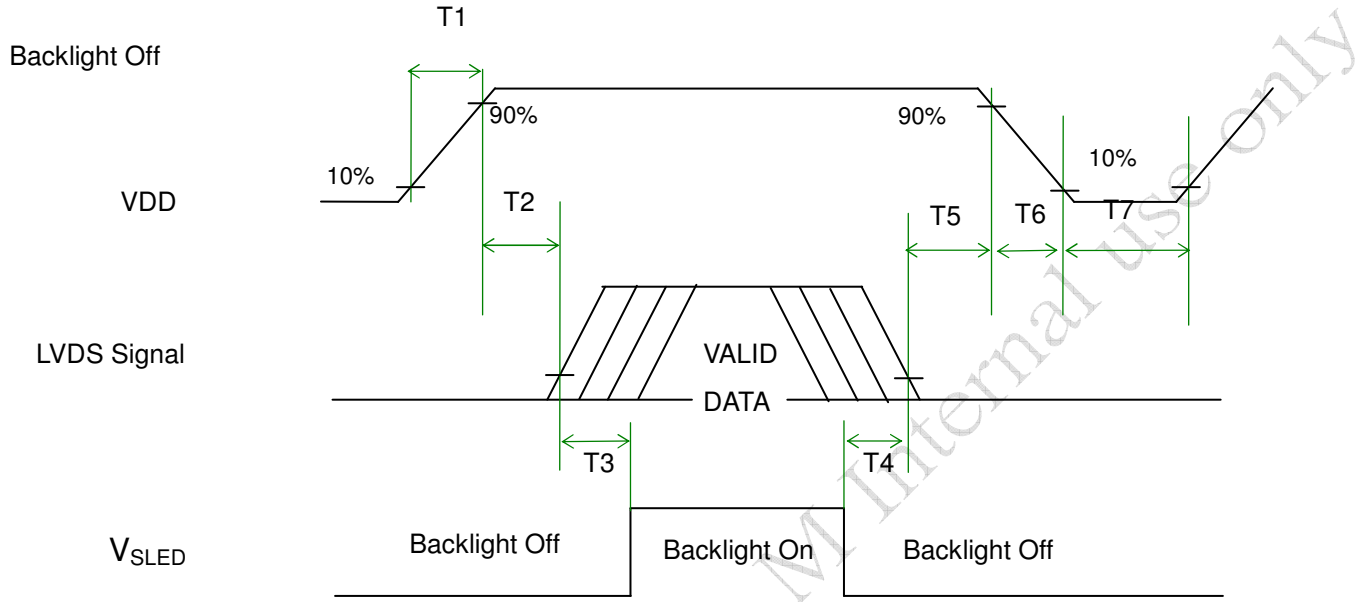
2.5. Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	Note 3-5 Note 3-6
T6	0	-	200	[ms]	Note 3-6 Note 3-7
T7	1000	-	-	[ms]	

Note 3-5 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

Note 3-6 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.

Note 3-7 : Voltage of VDD must decay smoothly after power-off. (customer system decide this value)

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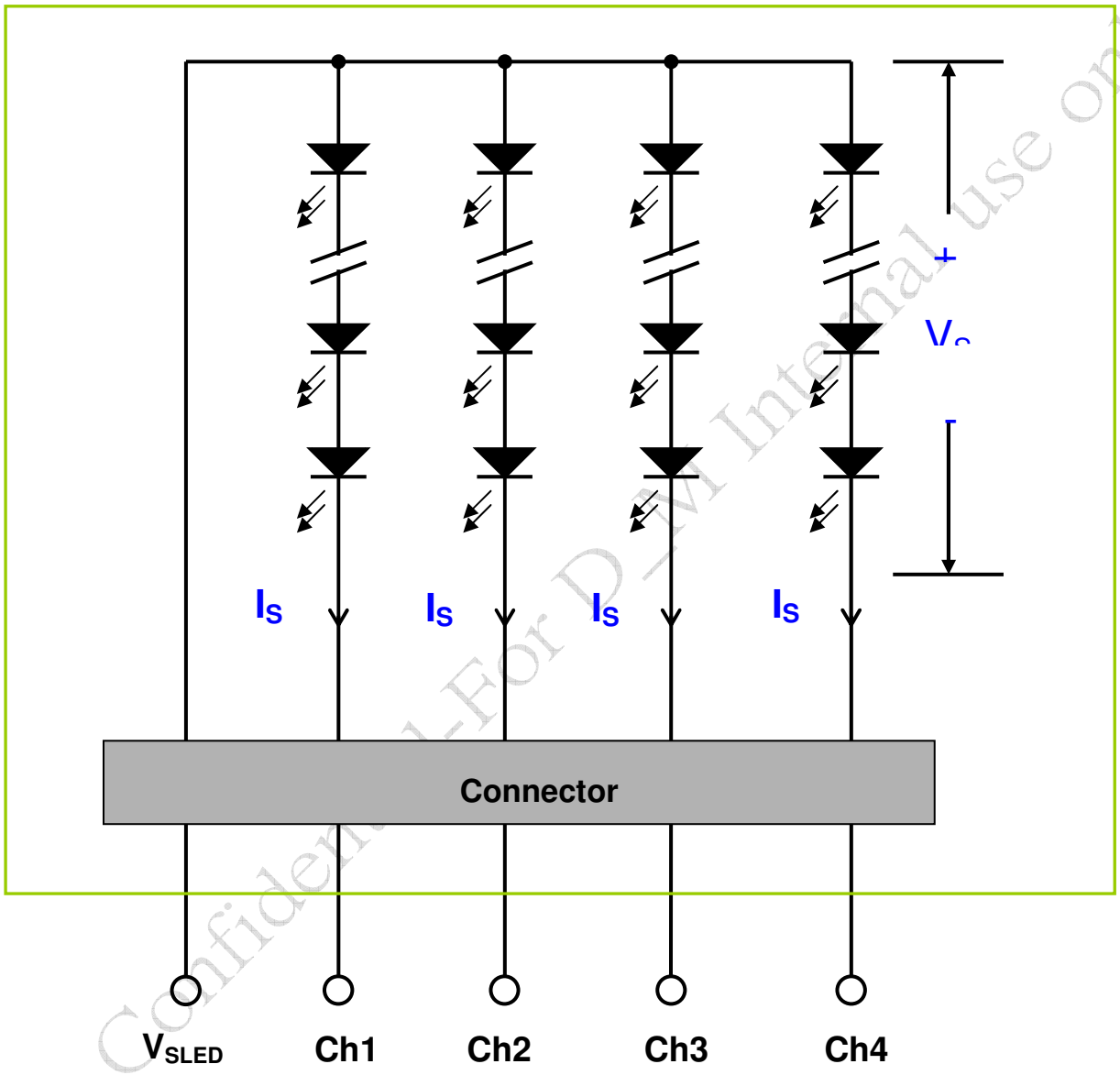
3. Backlight Unit

3.1. Block Diagram

The following shows the block diagram of the 21.5 inch Backlight Unit.



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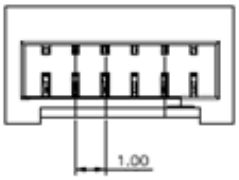
3.2. Interface Connection

3.2.1. Connector Type

Backlight Connector	Manufacturer	CviLux
	Part Number	CI1406M1VLD-NH
Mating Connector	Manufacturer	CviLux
	Part Number	CI1406SL000-NH(Lock)

Backlight Connector dimension:

$H \times V \times D = 7.7 \times 3.98 \times 4.85, Pitch = 1.0(unit = mm)$



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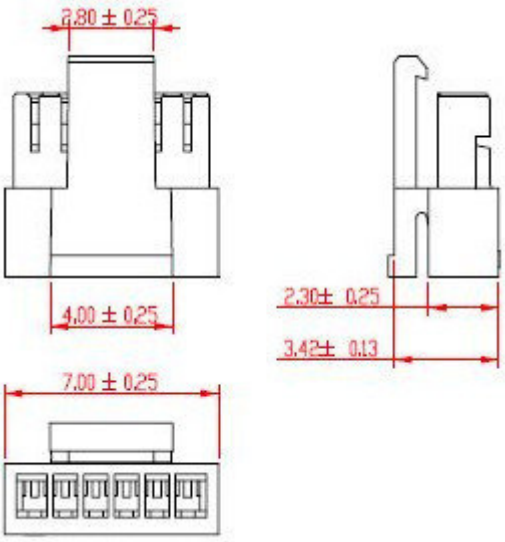
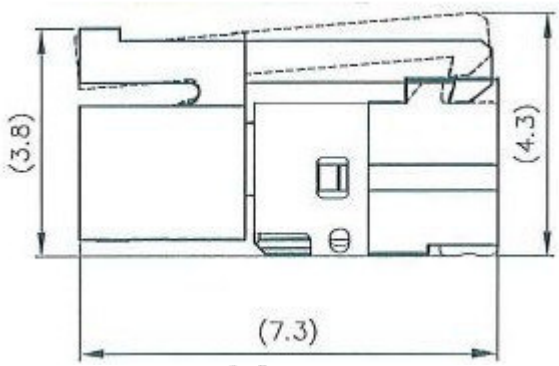
Mating Connector dimension:





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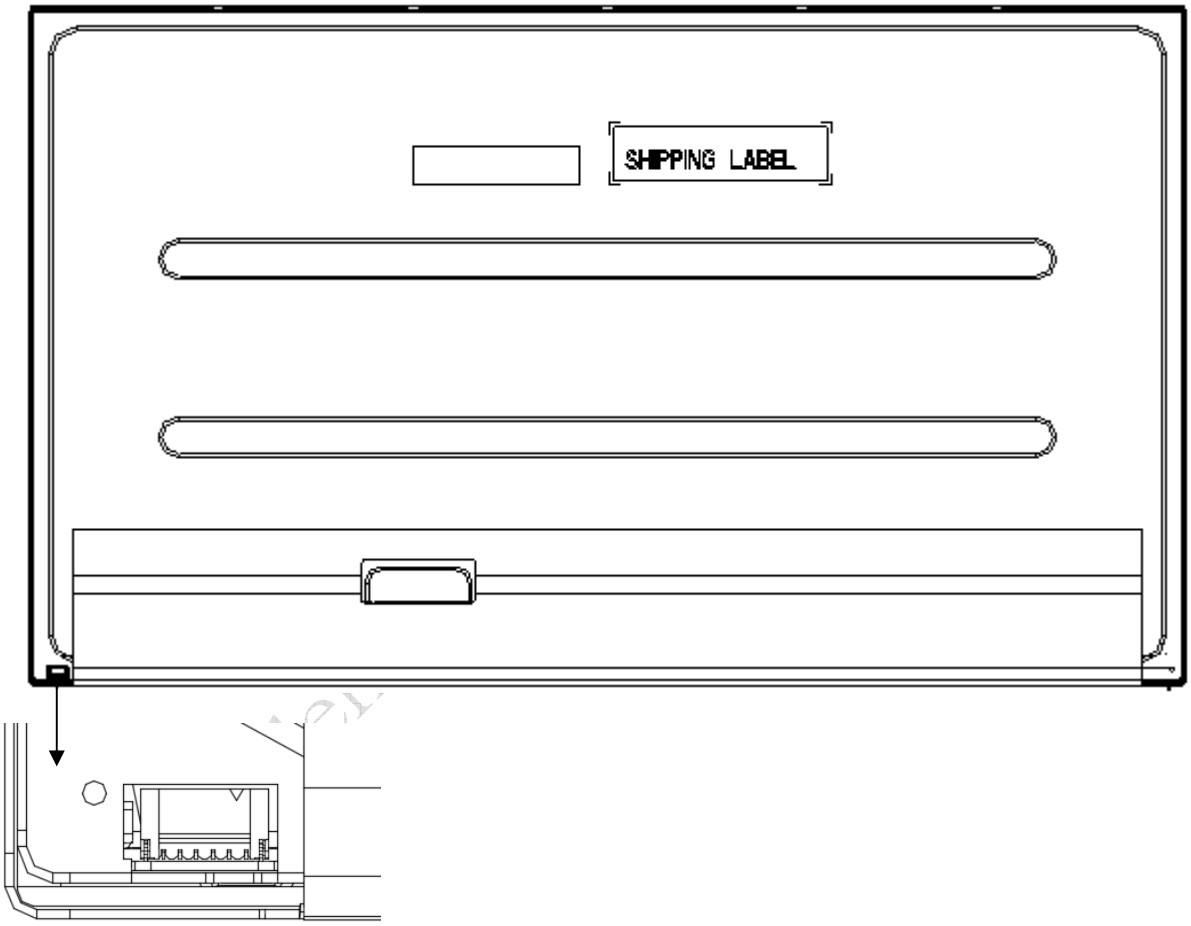
3.2.2. *Connector Pin Assignment*

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	V_{SLED}	LED Power Supply Voltage Input Terminal	
4	V_{SLED}	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	



3.3.

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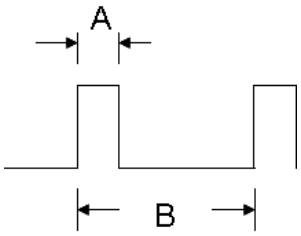
Electrical Characteristics

3.3.1. Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25℃)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	90	[mA]	100% duty ratio
			120	[mA]	Duty ratio≦ 10% Pulse time=10 ms



Duty ratio= (A / B) X 100% ; (A: Pulse time, B: Period)

3.3.2. Recommended Operating Condition

(Ta=25℃)

Symbol	Description	Min.	Typ.	Max.	Unit	Remark
Is	LED String Current	-	55	60	[mA]	100% duty ratio of LED chip, ***Note 4-6***
Vs	LED String Voltage	47.6	51	54.4	[Volt]	Is=55mA @ 100% duty ratio; *Note 4-1, Note 4-5,* , ***Note 4-7***
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	3.4	[Volt]	Is=55mA @ 100% duty ratio; *Note 4-2*
P_{BLU}	LED Light Bar Power Consumption	-	11.2	12.0	[Watt]	***Note 4-3***
LT_{LED}	LED Life Time	50,000	-	-	[Hour]	***Note 4-4***
OVP	Over Voltage Protection in system board	110% Vsmax	-	-	[Volt]	***Note 4-5***

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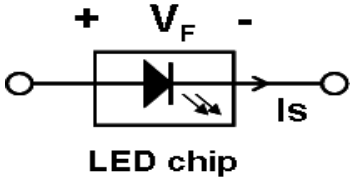
Note 4-1: Vs (Typ.) = V_F (Typ.) X LED No. (one string);

a. V_F: LED chip forward voltage, V_F (Min.)= 2.8V, V_F(Typ.)=3.0V, V_F(Max.)=3.2V

b. The same euqation to calculate Vs(Min.) & Vs (Max.) for respective V_F (Min.) & V_F(Max.);



Note 4-2: ΔVs (Max.) = ΔV_F X LED No. (one string);

a. ΔV_F: LED chip forward voltage deviation; (0.2 V , each Bin of LED V_F)

Note 4-3: P_{BLU} (Typ.) = Vs (Typ.) X Is (Typ.) X 4 ; (4 is total String No. of LED Light bar)

P_{BLU} (Max.) = Vs (Max.) X Is (Typ.) X 4 ;

Note 4-4: Definition of life time:

a. Brightness of LED becomes to 50% of its original value

b. Test condition: Is = 55mA and 25℃ (Room Temperature)

Note 4-5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

Note 4-6: AUO strongly recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

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4. Reliability Test Items

AUO reliability test items are listed as following table. (*Bare Panel only*)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50℃, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50℃, 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0℃, 300hours	
High Temperature Storage (HTS)	Ta= 60℃, 300hours	
Low Temperature Storage (LTS)	Ta= -20℃, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	

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5. International Standard

5.1. Safety

(1) UL 60950-1; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) IEC 60950-1; Standard for Safety of International Electrotechnical Commission

(3) EN 60950-1; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

5.2. EMC

(1) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. “American National standards Institute(ANSI), 1992

(2) C.I.S.P.R “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” International Special committee on Radio Interference.

(3) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” European Committee for Electrotechnical Standardization. (CENELEC), 1998

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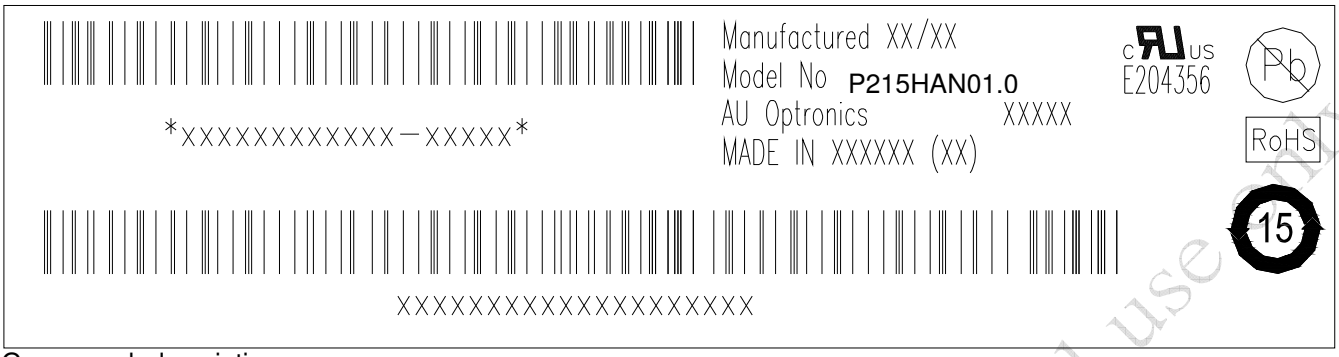
6. Packing

6.1. Definition of Label

A. Panel Label:



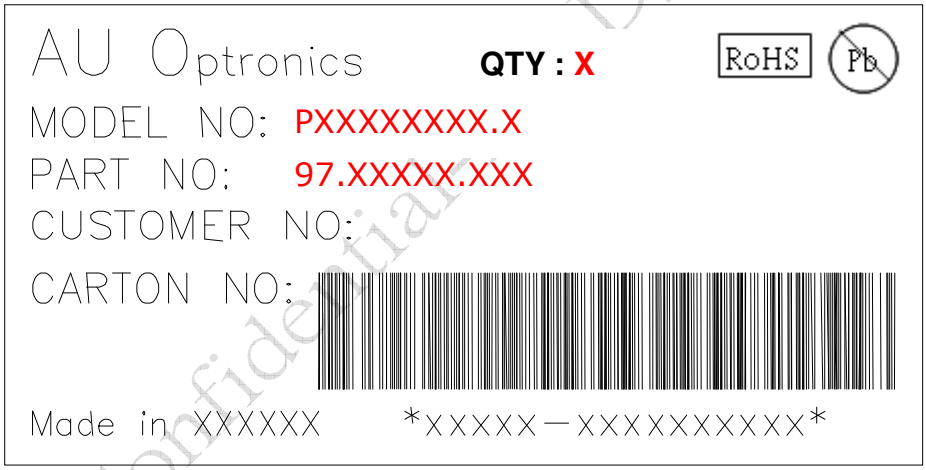
Green mark description

(1) For Pb Free Product, AUO will add Pb for identification.

(2) For RoHs compatible products, AUO will add RoHS for identification.

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:



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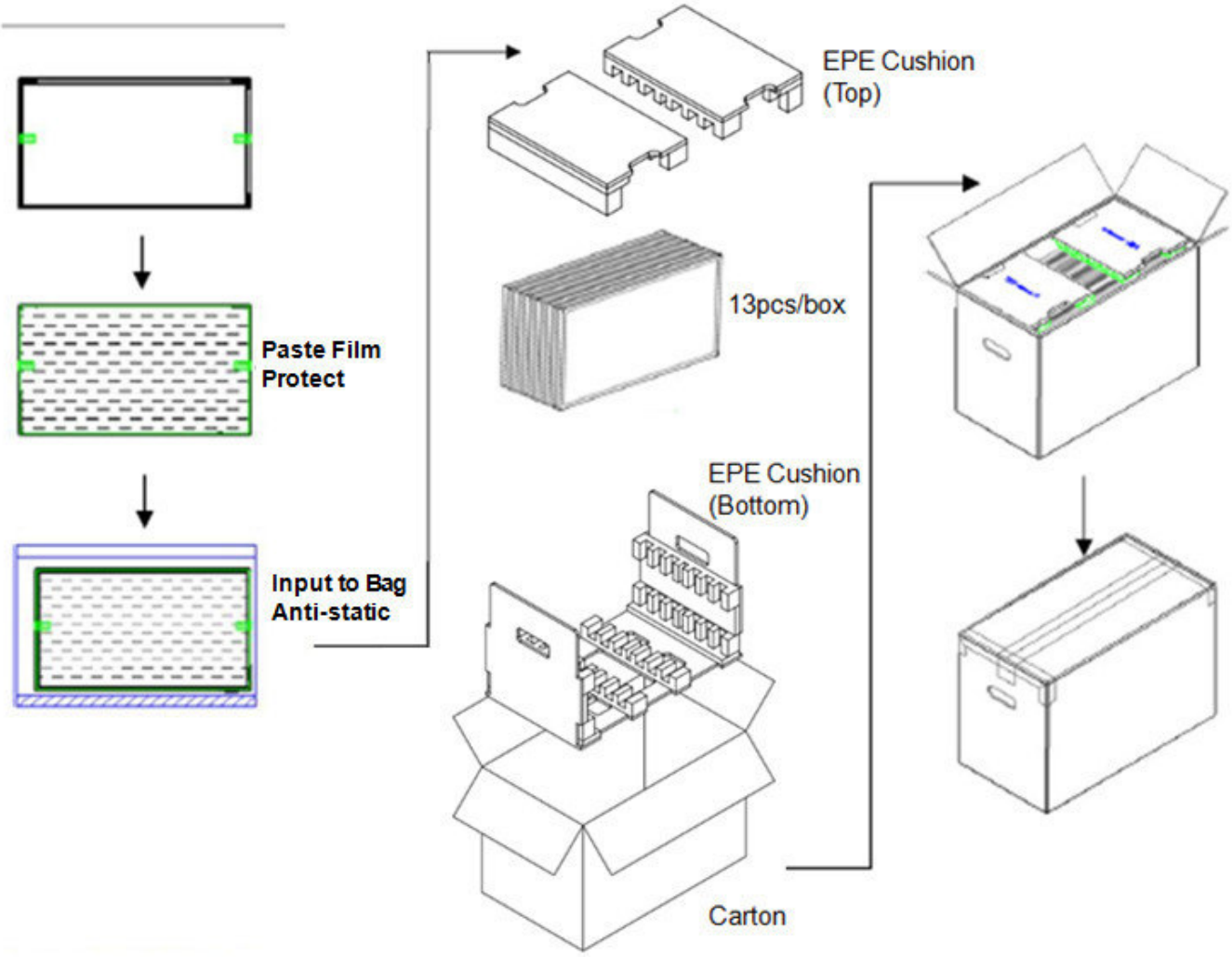
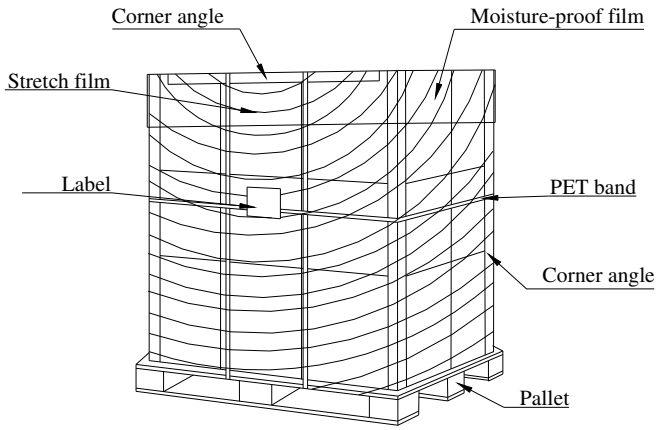
6.2. Packing Methods





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6.3. Pallet and Shipment Information

	Item	Specification			Remark
		Q'ty	Dimension	Weight(kg)	
1	Panel	1	489.3mm(H) × 287.0mm(V) ×12.3(D)mm	1.62	***Note 1***
2	Cushion	1	-	0.55	
3	Box	1	565(L)mm x 345(W)mm x 375(H)mm	1.40	without Panel & cushion ***Note 1***
4	Packing Box	13 pcs/Box	565(L)mm x 345(W)mm x 375(H)mm	23.01	with panel & cushion ***Note 1***
5	Pallet	1	1150(L)mm x 1070(W)mm x 132(H)mm	14.2	***Note 1***
6	Pallet after Packing	18 boxes/pallet	1150(L)mm x 1070(W)mm x 1257(H)mm	428.38	***Note 1***

Note 1: Estimated value which is subject to change based on real measured data.

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7. Precautions

Please pay attention to the followings when you use this TFT LCD module.

7.1. Mounting Precautions

(1) You must mount a module using holes arranged in four corners or four sides.

(2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(4) You should adopt radiation structure to satisfy the temperature specification.

(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.

(6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)

(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.

(9) Do not open the case because inside circuits do not have sufficient strength.

7.2. Operating Precautions

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)

(2) Response time depends on the temperature. (In lower temperature, it becomes longer..)

(3) Brightness depends on the temperature. (In lower temperature, it may become lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.

(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.

(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

7.3. Operating Condition for Public Information Display

The device listed in the product specification is designed and manufactured for PID (Public Information

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Display) application. To optimize module's lifetime and function, below operating usages are required.

(1) Normal operating condition
   A. Operating temperature: -10~50℃
   B. Operating humidity: 10~90%
   C. Display pattern: dynamic pattern (Real display).
      Note) Long-term static display would cause image sticking.

(2) Operation usage to protect against image sticking due to long-term static display.
   A. Suitable operating time: 20 hours or less a day.
   B. Liquid Crystal refresh time is required. Cycling display between 5 minutes' information (static) display and 10 seconds' moving image.
   C. Periodically change background and character (image) color.
   D. Avoid combination of background and character with large different luminance.

(3) Periodically adopt one of the following actions after long time display.
   A. Running the screen saver (motion picture or black pattern)
   B. Power off the system for a while

(4) LCD system is required to place in well-ventilated environment. Adapting active cooling system is highly recommended.

(5) Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions, such as high temperature/ humidity, display stationary patterns, or long operation time etc…, it is strongly recommended to contact AUO for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

7.4. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

7.5. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

7.6. Storage

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

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(3) Storage condition is guaranteed under packing conditions.

(4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

7.7. Handling Precautions for Protection Film

(1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.

(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.

(3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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