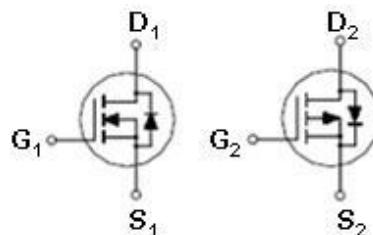
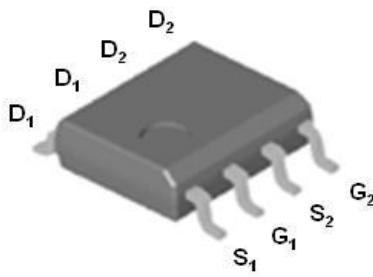


P2402OV

N&P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D	Channel
20V	24mΩ @ $V_{GS} = 4.5V$	10A	N
-20V	43mΩ @ $V_{GS} = -4.5V$	-5.2A	P



SOP-8

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	CH.	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	N	20	V
		P	-20	
Gate-Source Voltage	V_{GS}	N	± 12	
		P	± 12	
Continuous Drain Current	I_D	N	10	A
		P	-5.2	
		N	6.3	
		P	-3.2	
Pulsed Drain Current ¹	I_{DM}	N	40	
		P	-21	
Power Dissipation	P_D	N	2.5	W
		P	2.5	
		N	1.6	
		P	1.6	
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150		°C



P2402OV

N&P-Channel Enhancement Mode MOSFET

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	°C / W

¹Pulse width limited by maximum junction temperature.

²Duty cycle $\leq 1\%$

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ C$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	CH.	LIMITS			UNIT
				MIN	TYP	MAX	
STATIC							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	N	20			V
		$V_{GS} = 0V, I_D = -250\mu A$	P	-20			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	N	0.4	0.8	1.2	
		$V_{DS} = V_{GS}, I_D = -250\mu A$	P	-0.4	-0.8	-1.2	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 12V$	N			± 100	nA
		$V_{DS} = 0V, V_{GS} = \pm 12V$	P			± 100	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16V, V_{GS} = 0V$	N			1	μA
		$V_{DS} = -16V, V_{GS} = 0V$	P			-1	
		$V_{DS} = 16V, V_{GS} = 0V, T_J = 55^\circ C$	N			10	
		$V_{DS} = -16V, V_{GS} = 0V, T_J = 55^\circ C$	P			-10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 2.5V, I_D = 5.2A$	N		28	36	$m\Omega$
		$V_{GS} = -2.5V, I_D = -4A$	P		47	68	
		$V_{GS} = 4.5V, I_D = 8A$	N		18	24	
		$V_{GS} = -4.5V, I_D = -5A$	P		32	43	

P2402OV

N&P-Channel Enhancement Mode MOSFET

DYNAMIC							
Input Capacitance	C_{iss}	N-Channel $V_{GS} = 0V, V_{DS} = 10V, f = 1MHz$ P-Channel $V_{GS} = 0V, V_{DS} = -10V, f = 1MHz$	N		732		
Output Capacitance			P		1110		
Reverse Transfer Capacitance	C_{rss}		N		241		
			P		242		
Total Gate Charge ²	Q_g		N		169		
Gate-Source Charge ²			P		173		
Gate-Drain Charge ²	Q_{gd}	N-Channel $V_{DS} = 10V, V_{GS} = 4.5V, I_D = 5A$ P-Channel $V_{DS} = -10V, V_{GS} = -4.5V, I_D = -5A$	N		10	nC	
Turn-On Delay Time ²	$t_{d(on)}$	P		11			
Rise Time ²	t_r	N		0.8			
Turn-Off Delay Time ²	$t_{d(off)}$	P		2			
Fall Time ²	t_f	N		3.7			
		P		3.5			
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_C = 25^\circ C$)							
Continuous Current	I_S	N-Channel $V_{DS} = 10V$ $I_D \geq 1A, V_{GS} = 4.5V, R_{GEN} = 10\Omega$	N		1.9	A	
			P		-1.9		
Pulsed Current ³	I_{SM}		N		7.6		
			P		-9		
Forward Voltage ¹	V_{SD}	$I_F = 1A, V_{GS} = 0V$	N		1.3	V	
		$I_F = -1A, V_{GS} = 0V$	P		-1.3		

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

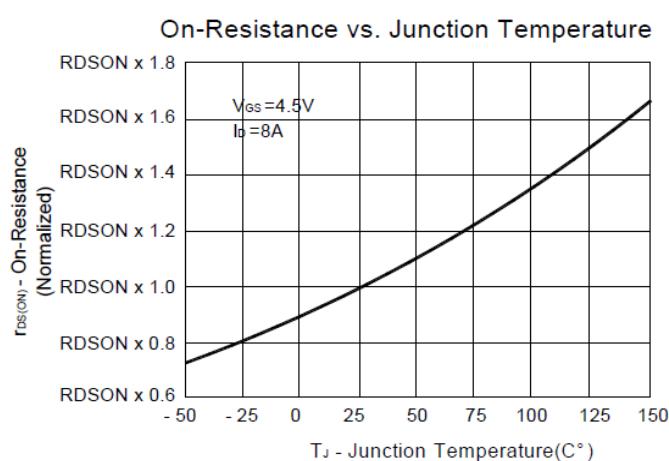
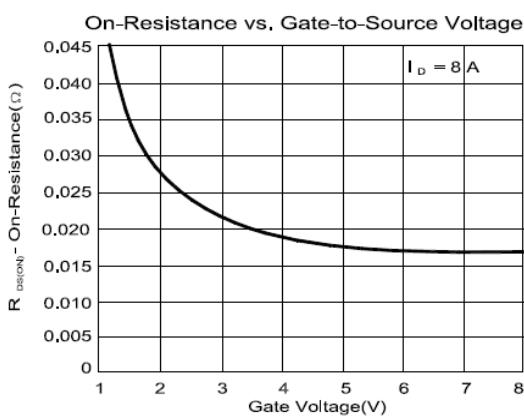
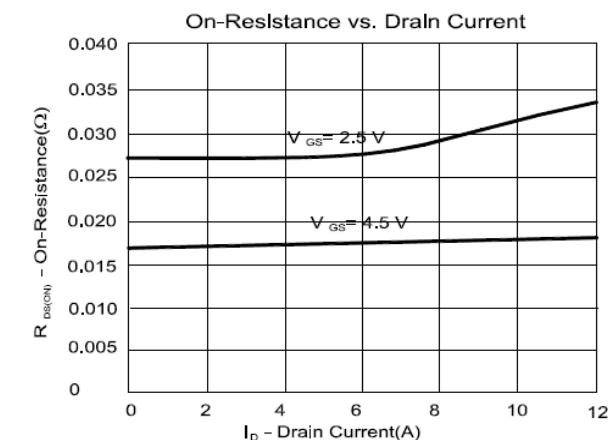
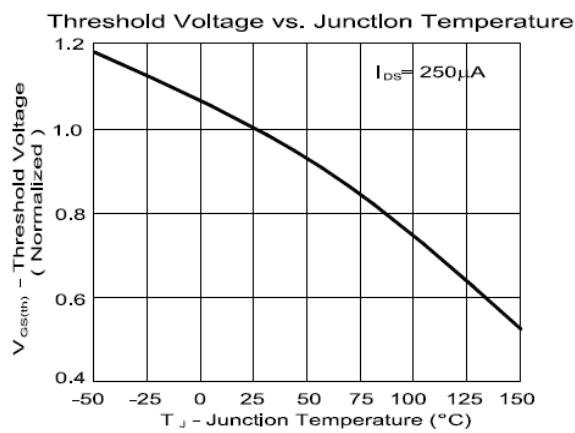
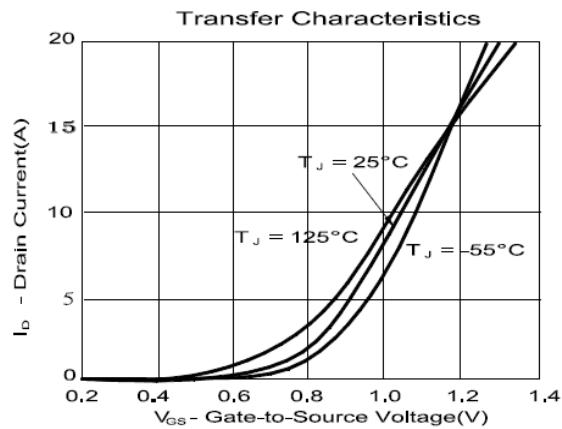
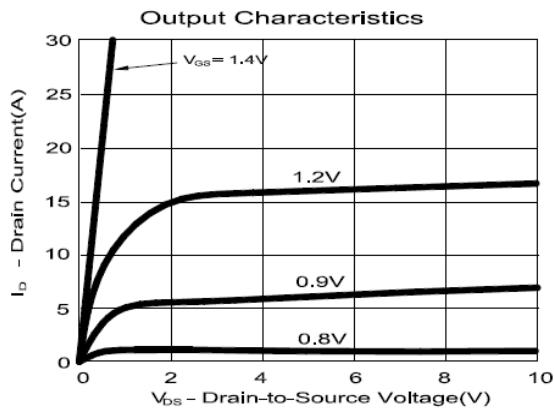
²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

P2402OV

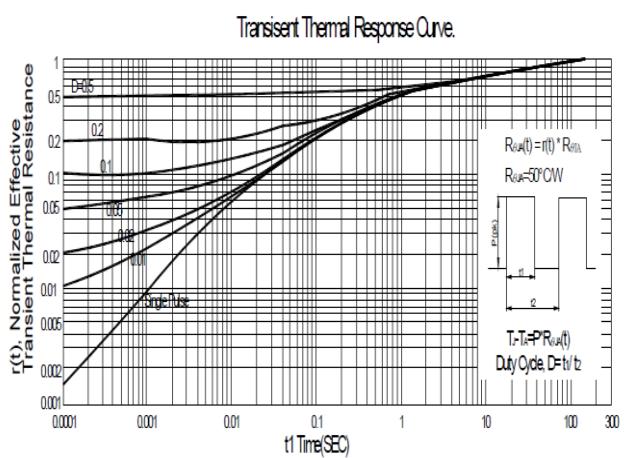
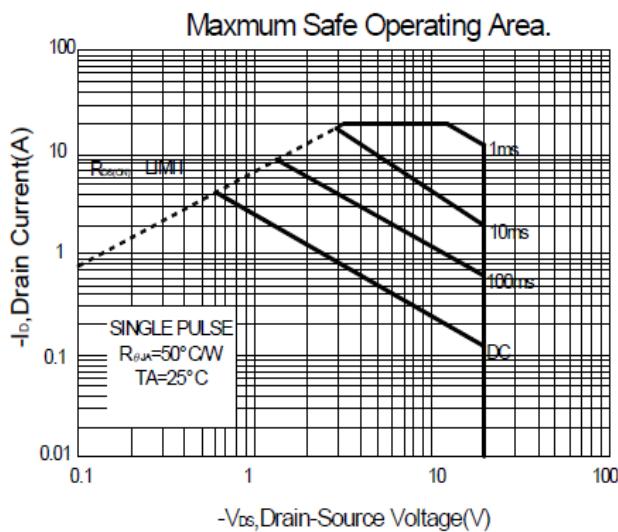
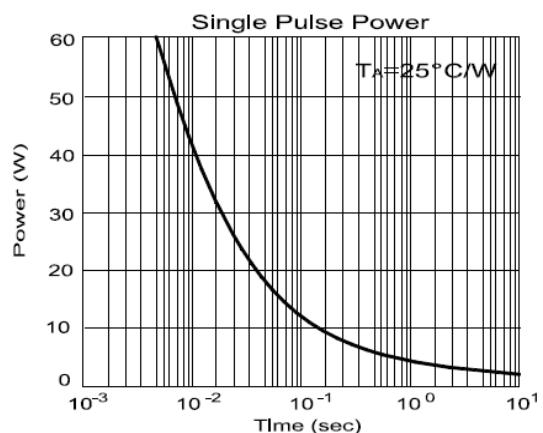
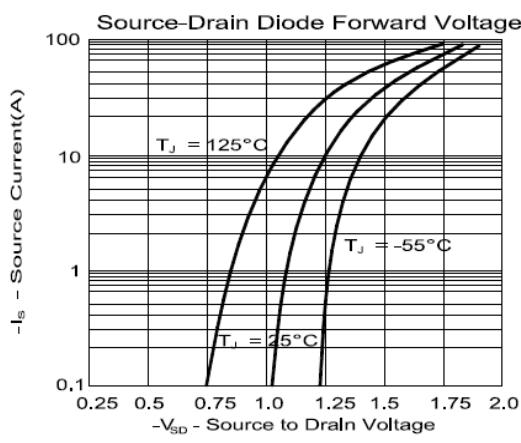
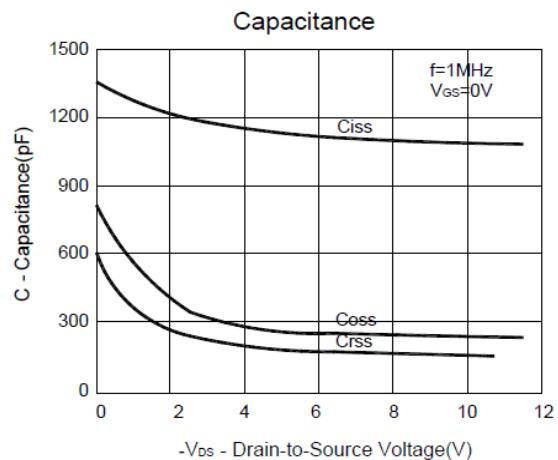
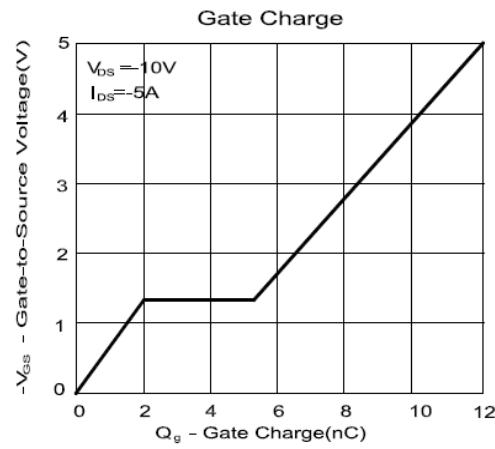
N&P-Channel Enhancement Mode MOSFET

N-CHANNEL



P2402OV

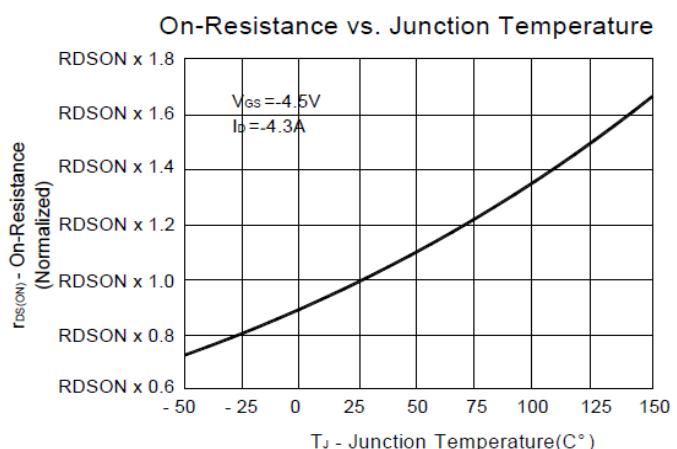
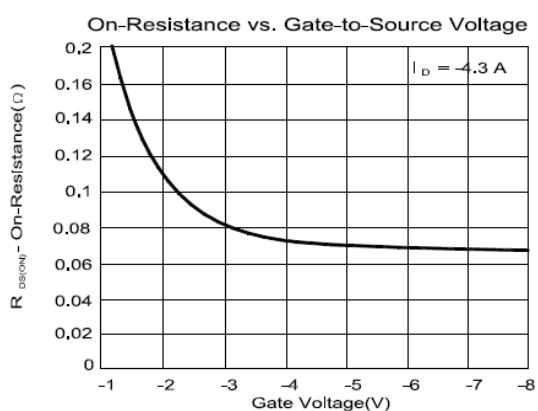
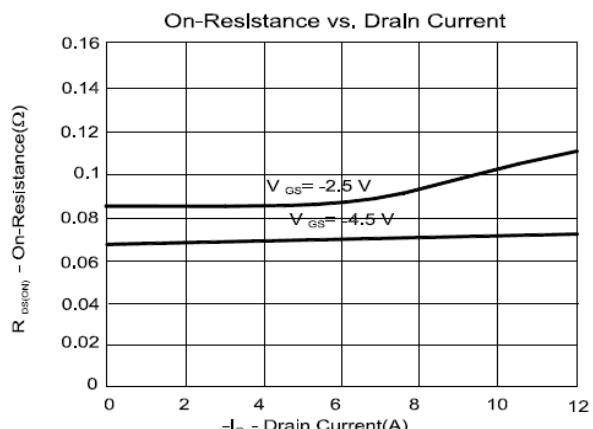
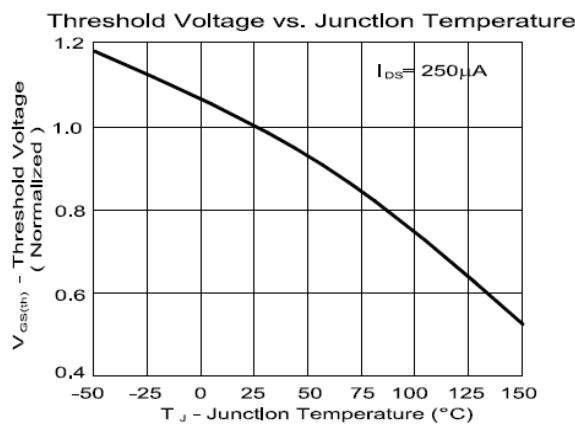
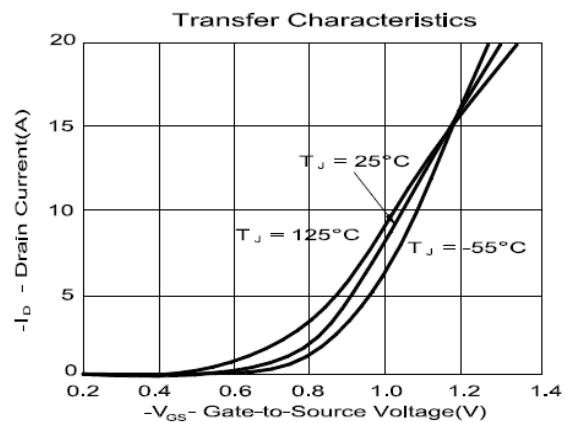
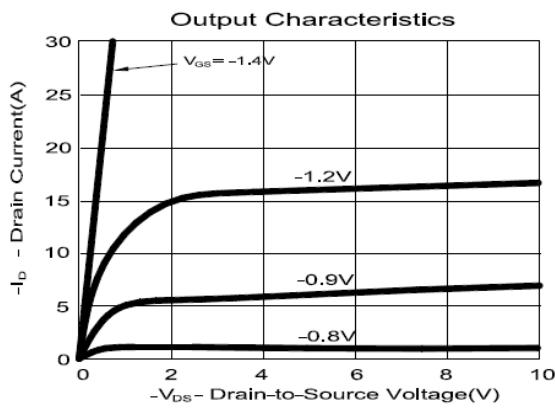
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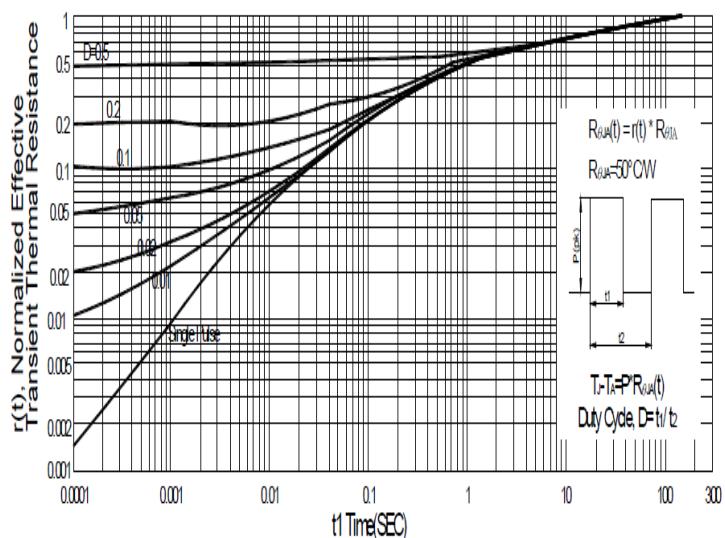
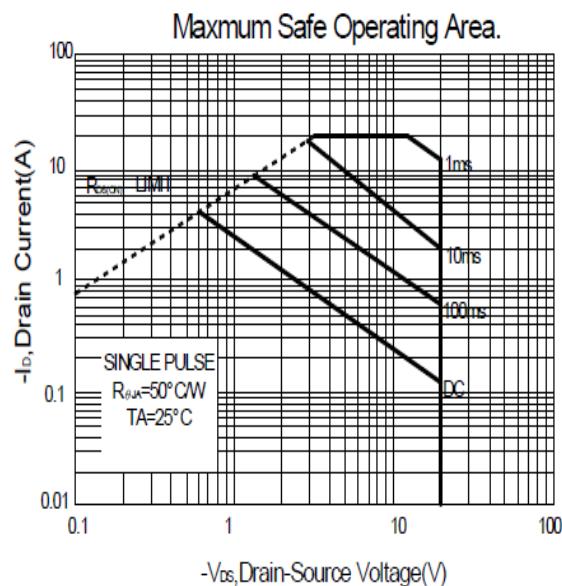
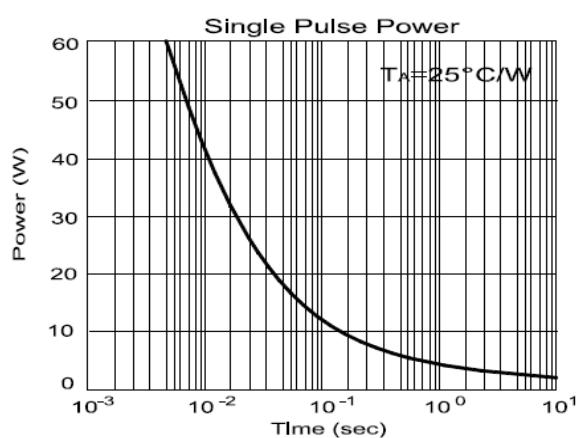
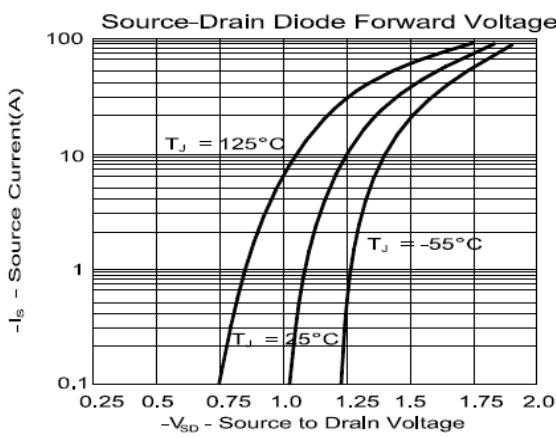
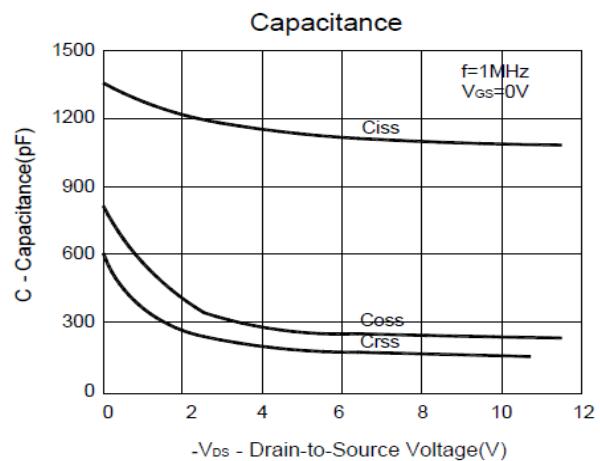
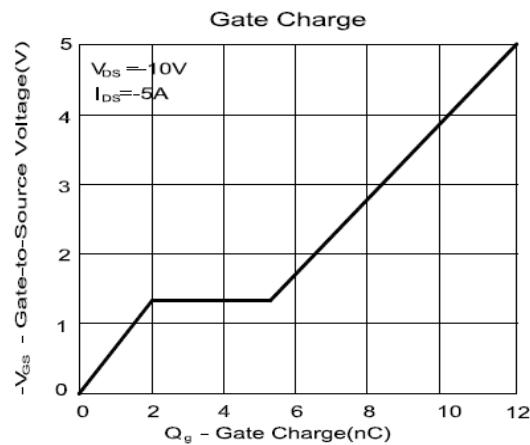
N&P-Channel Enhancement Mode MOSFET

P-CHANNEL



P2402OV

N&P-Channel Enhancement Mode MOSFET



P2402OV

N&P-Channel Enhancement Mode MOSFET

Package Dimension

SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

