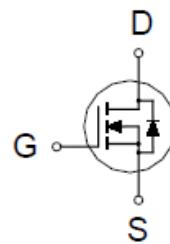
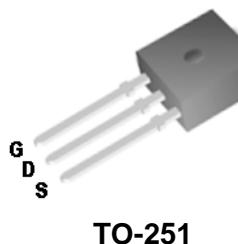


P2610AI

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	26mΩ @ $V_{GS} = 10V$	32A



ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_C = 25^\circ C$	I_D	32	A
$T_C = 100^\circ C$		20	
Pulsed Drain Current ¹	I_{DM}	100	
Avalanche Current	I_{AS}	53	
Avalanche Energy	E_{AS}	139	mJ
Power Dissipation $T_C = 25^\circ C$	P_D	42	W
$T_C = 100^\circ C$		17	
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$
Lead Temperature ('/16" from case for 10 sec.)	T_L	275	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3	$^\circ C / W$
Junction-to-Ambient	$R_{\theta JA}$		62.5	
Case-to-Heatsink	$R_{\theta CS}$	0.5		

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$, Unless Otherwise Noted)

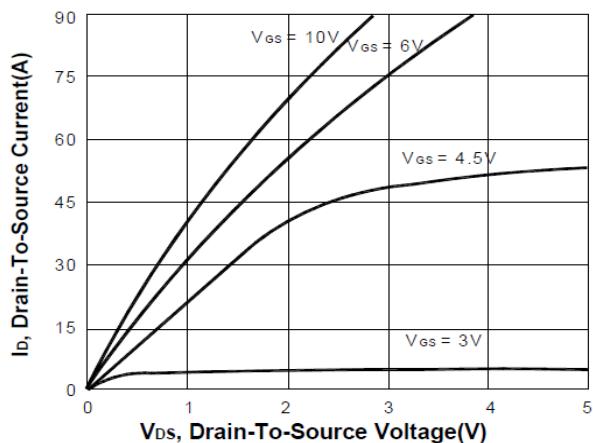
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.5	2.3	4	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 80\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 80\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
On-State Drain Current ¹	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 10\text{V}$	100			A
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 27.5\text{A}$		21	26	$\text{m}\Omega$
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 10\text{V}, I_D = 27.5\text{A}$		30		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		5060		pF
Output Capacitance	C_{oss}			294		
Reverse Transfer Capacitance	C_{rss}			190		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		2.3		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		86		nC
Gate-Source Charge ²	Q_{gs}			25		
Gate-Drain Charge ²	Q_{gd}			27		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 50\text{V}, I_D \approx 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GS}} = 25\Omega$		25		nS
Rise Time ²	t_r			250		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			110		
Fall Time ²	t_f			140		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_C = 25^\circ\text{C}$)						
Continuous Current	I_S				32	A
Forward Voltage ¹	V_{SD}	$I_F = I_S, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	t_{rr}			100		nS
Reverse Recovery Charge	Q_{rr}			380		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

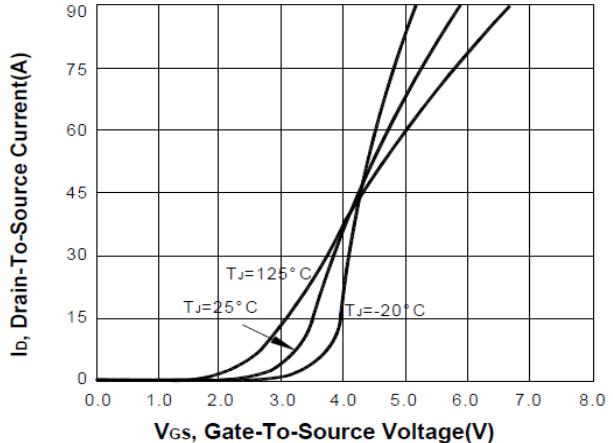
²Independent of operating temperature.

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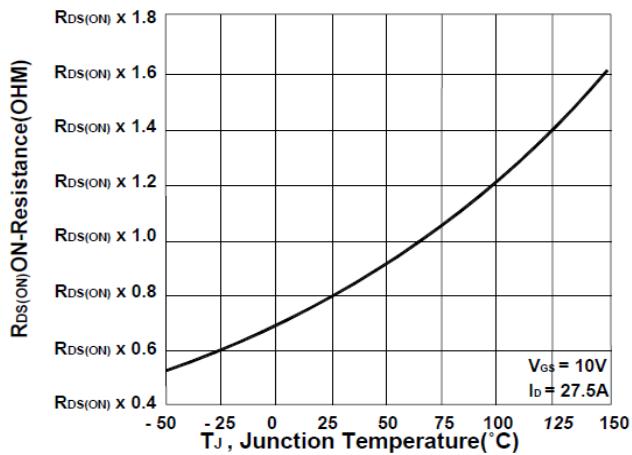
Output Characteristics



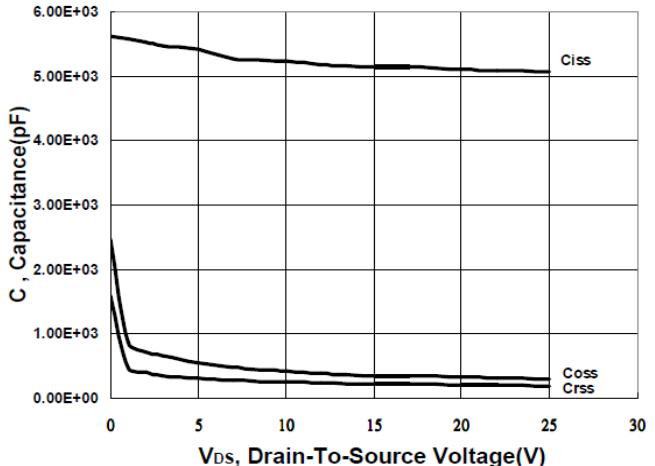
Transfer Characteristics



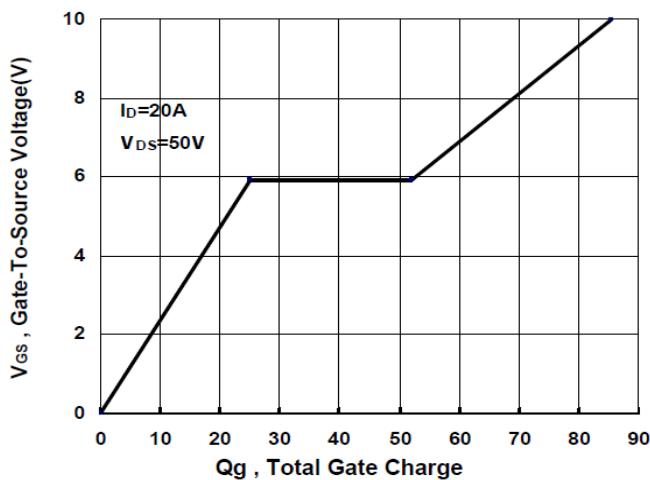
On-Resistance VS Temperature



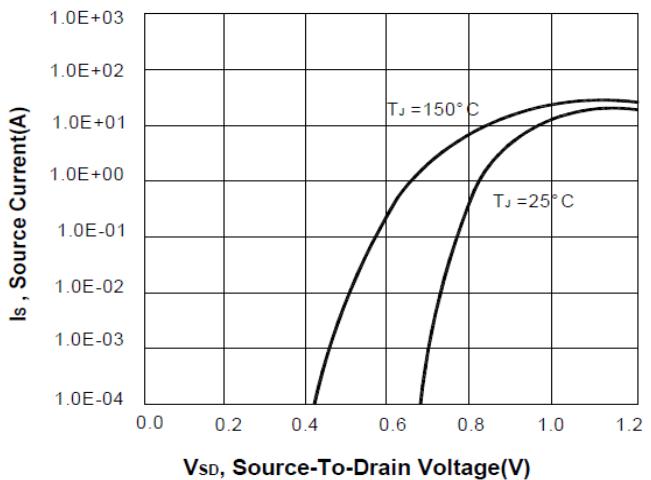
Capacitance Characteristic



Gate charge Characteristics

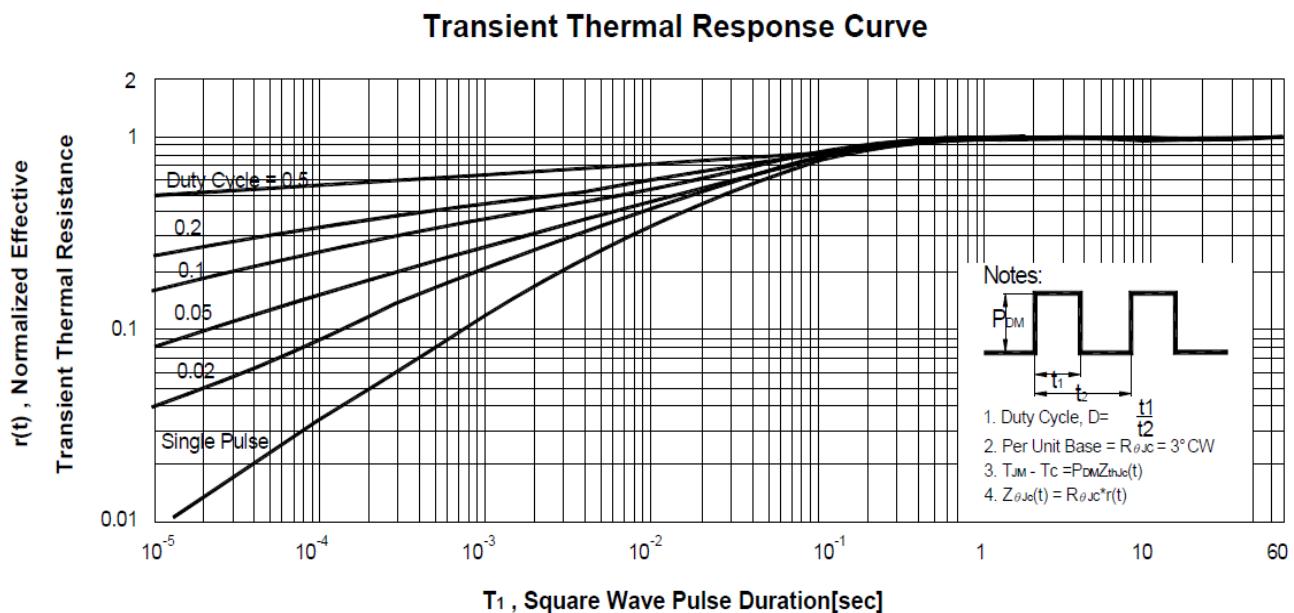
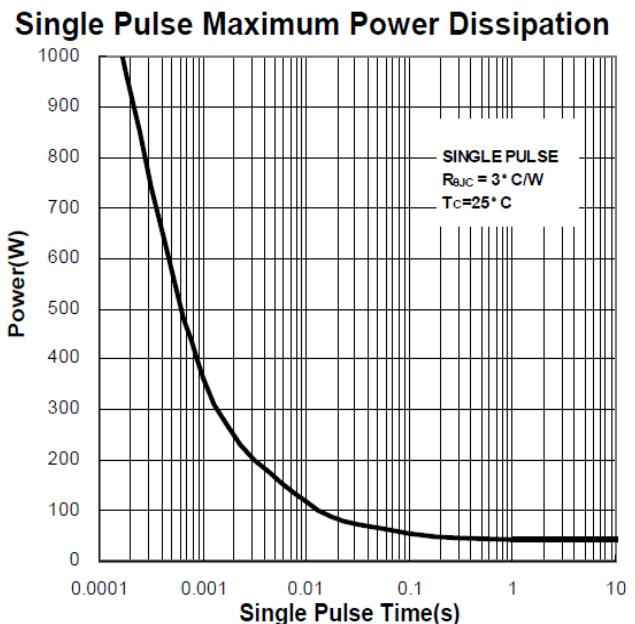
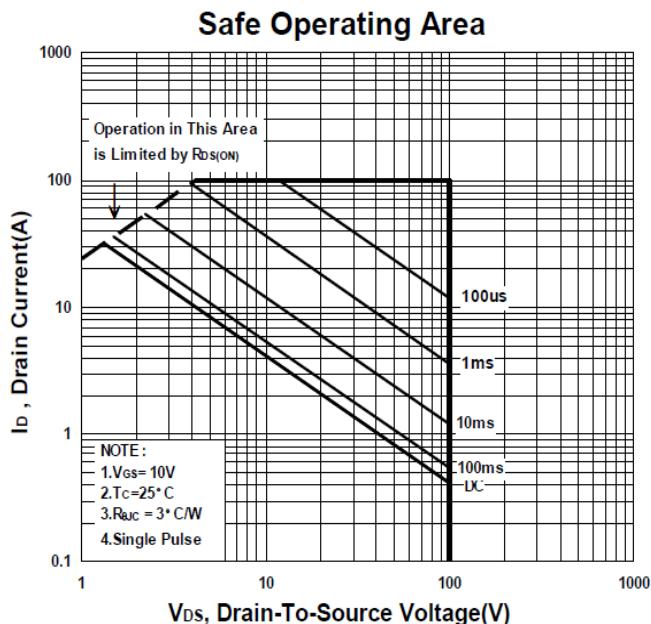


Source-Drain Diode Forward Voltage



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Package Dimension

TO-251 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	14	15	17.14	H	0.89		1.7
B	2.1	2.3	2.5	I	6.3		6.8
C	0.4	0.5	0.6	J	4.8		5.5
D	0.35	0.5	0.65	K	0.5	0.84	1.14
E	0.9	1.1	1.5	L	0.4	0.76	0.912
F	7		9.65	M		2.3	
G	5.3		6.22	N	1.4	2.16	2.23

