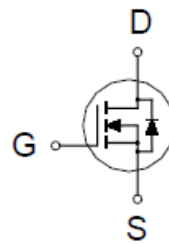
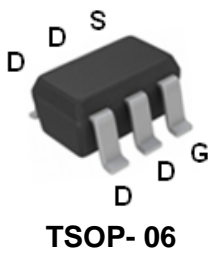


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PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	27m Ω @ $V_{GS} = 10V$	7A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ¹	$T_A = 25\text{ }^\circ\text{C}$	I_D	7	A
	$T_A = 70\text{ }^\circ\text{C}$		4.5	
Pulsed Drain Current ²		I_{DM}	35	
Avalanche Current		I_{AS}	17	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	15	mJ
Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	P_D	2	W
	$T_A = 70\text{ }^\circ\text{C}$		0.8	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		30	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Limited by package

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.5	3.0	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	μA
		V _{DS} = 20V, V _{GS} = 0V, T _J = 125 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 10V	35			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 5A		29	40	mΩ
		V _{GS} = 10V, I _D = 7A		19	27	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 7A		12		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		494		pF
Output Capacitance	C _{oss}			177		
Reverse Transfer Capacitance	C _{rss}			108		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.9		Ω
Total Gate Charge ²	Q _{g(VGS=10V)}	V _{DS} = 15V, I _D = 7A		11.5		nC
	Q _{g(VGS=4.5V)}			5.2		
Gate-Source Charge ²	Q _{gs}			1.1		
Gate-Drain Charge ²	Q _{gd}			4.1		
Turn-On Delay Time ²	t _{d(on)}		V _{DS} = 15V I _D ≅ 7A, V _{GS} = 10V, R _{GEN} = 6Ω		6.5	
Rise Time ²	t _r			5		
Turn-Off Delay Time ²	t _{d(off)}			23		
Fall Time ²	t _f			5		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				2	A
Forward Voltage ¹	V _{SD}	I _F = 7A, V _{GS} = 0V			1	V
Reverse Recovery Time	t _{rr}	I _F = 7A, di _F /dt = 100A / μS		31		nS
Reverse Recovery Charge	Q _{rr}	V _{GS} = 0V		12		μC

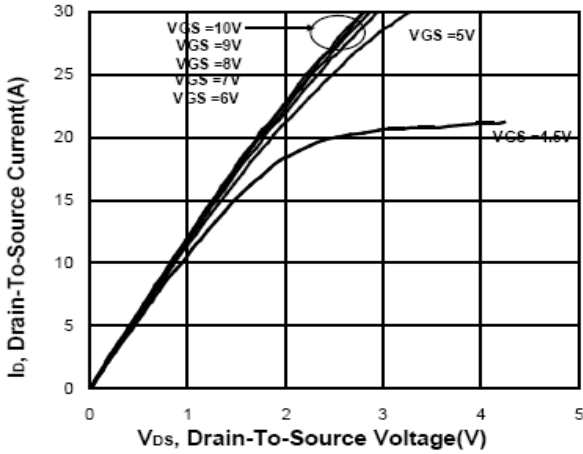
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

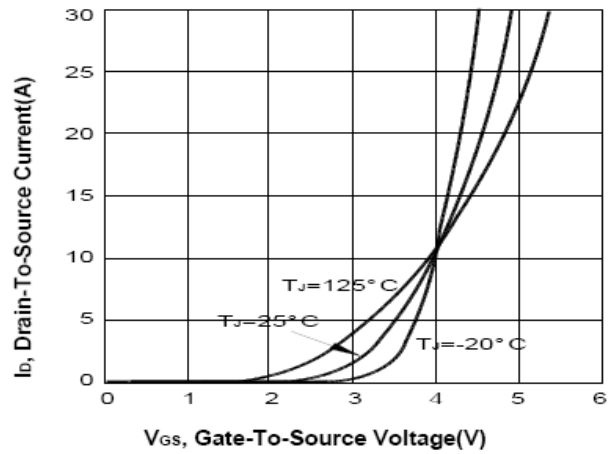
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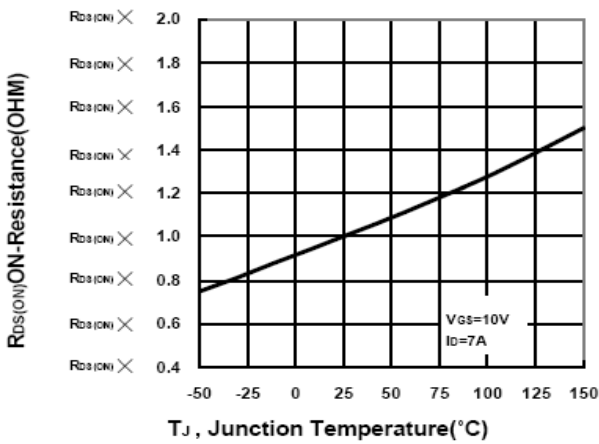
Output Characteristics



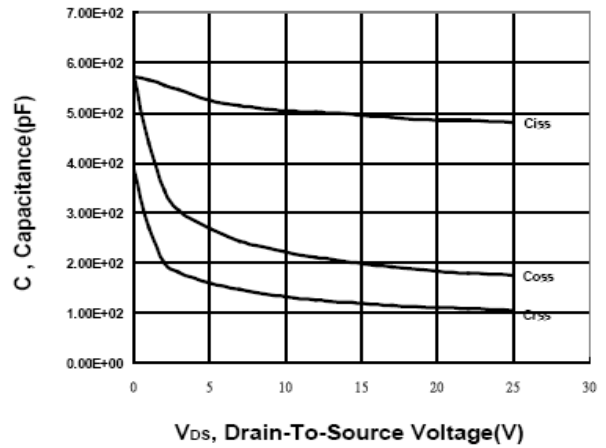
Transfer Characteristics



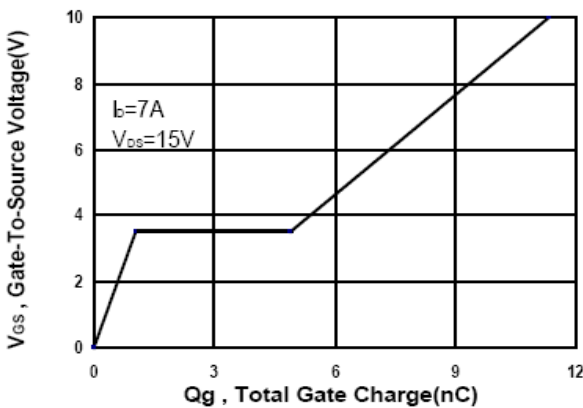
On-Resistance VS Temperature



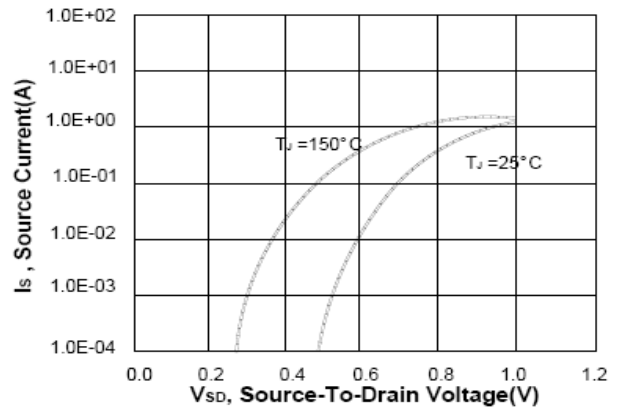
Capacitance Characteristic



Gate charge Characteristics



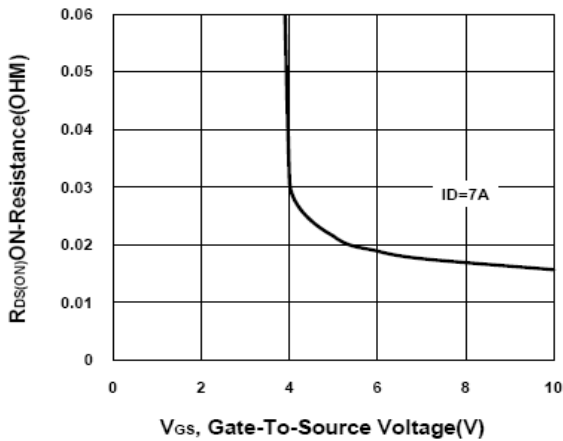
Source-Drain Diode Forward Voltage



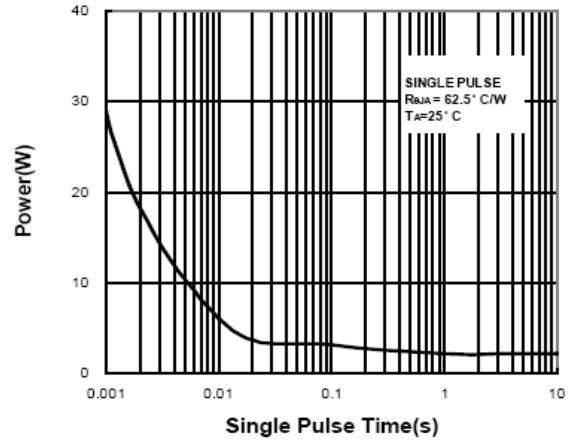
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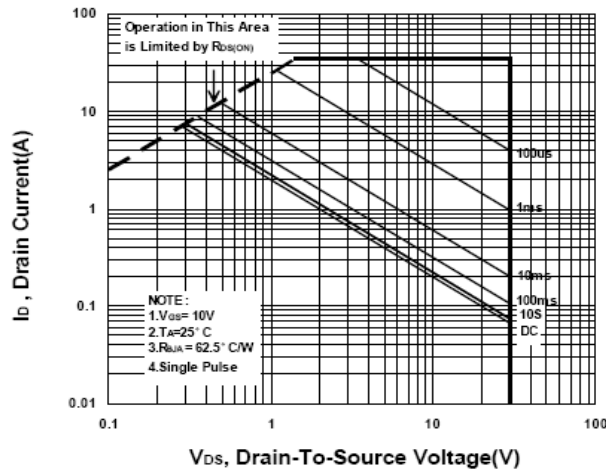
On-Resistance VS Gate-To-Source



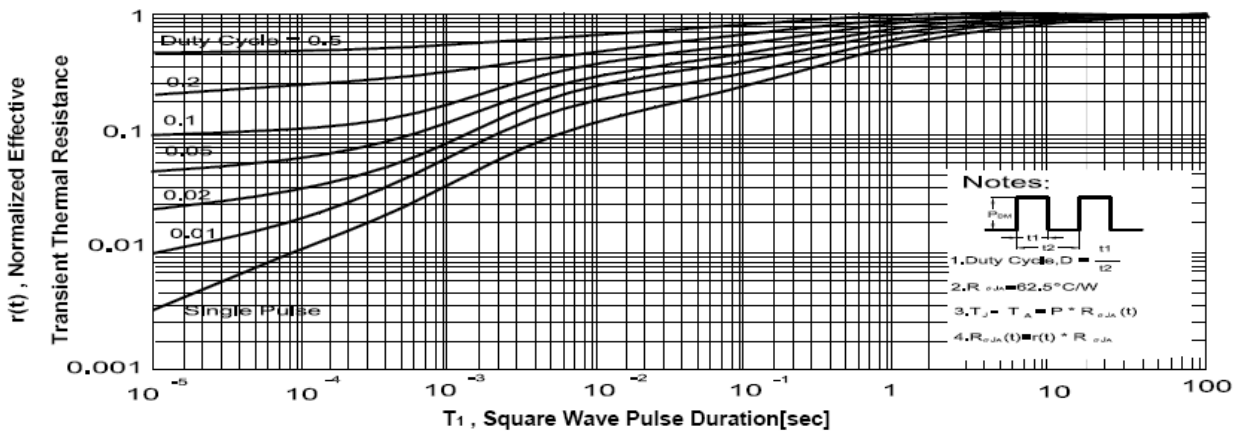
Single Pulse Maximum Power Dissipation



Safe Operating Area



Transient Thermal Response Curve



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Package Dimension

TSOP- 6 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.6		3	I	0.33		0.57
C	1.5		1.7	J			
D	2.8		3.02	K			
E	0.7		0.85	L			
F	0		0.1	M			
G	0.35		0.5	N			

