

## P2B60AMA

### N-Channel Enhancement Mode MOSFET

#### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
600V	200Ω @ $V_{GS} = 10V$	40mA



#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	40	mA
		31	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	120	
Power Dissipation	$P_D$	0.7	W
		0.4	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

#### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		178	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

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#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	600			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	3.4	4	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 30\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 600\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 480\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 16\text{mA}$		110	200	$\Omega$
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 10\text{V}, I_D = 16\text{mA}$		0.024		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		16.6		pF
Output Capacitance	$C_{\text{oss}}$			8.5		
Reverse Transfer Capacitance	$C_{\text{rss}}$			3.4		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 480\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 16\text{mA}$		4.3		nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			0.5		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			3.2		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 300\text{V}$ $I_D \geq 16\text{mA}, V_{\text{GEN}} = 10\text{V}, R_G = 6\Omega$		19		nS
Rise Time <sup>2</sup>	$t_r$			15		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			21		
Fall Time <sup>2</sup>	$t_f$			170		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$				38	mA
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 16\text{mA}, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 16\text{mA}, dI_F/dt = 100\text{A}/\mu\text{s}$		160		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			14		nC

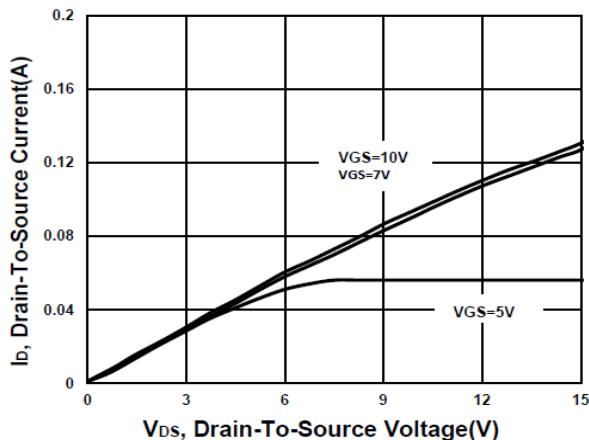
<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

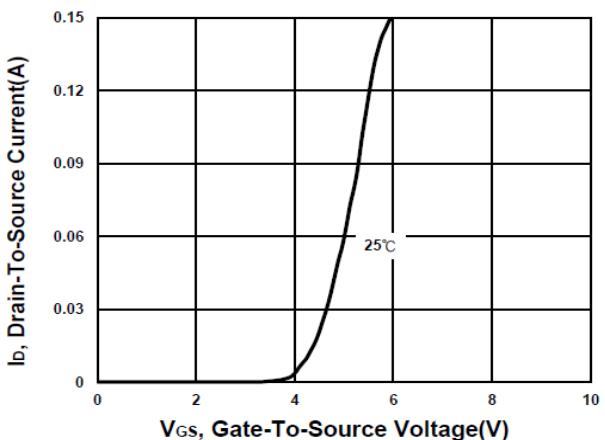
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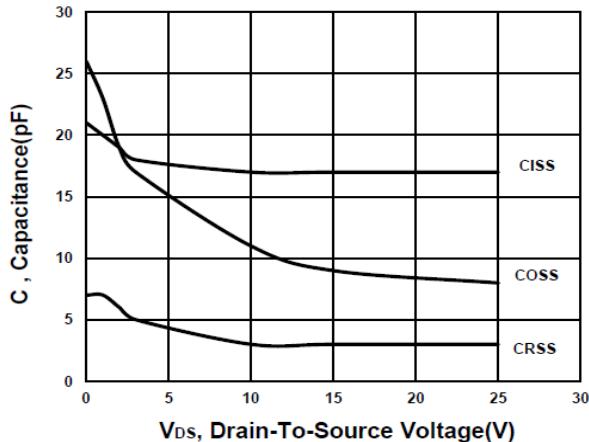
**Output Characteristics**



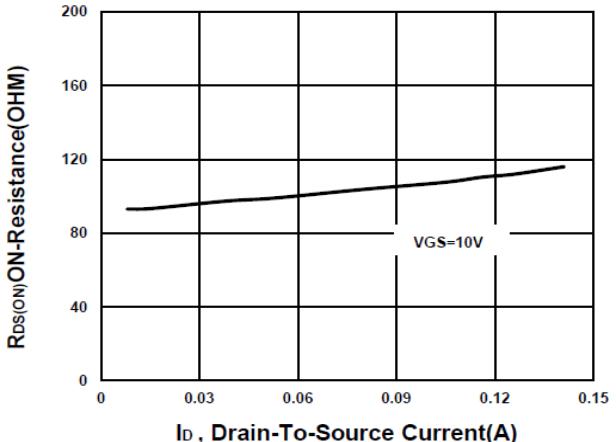
**Transfer Characteristics**



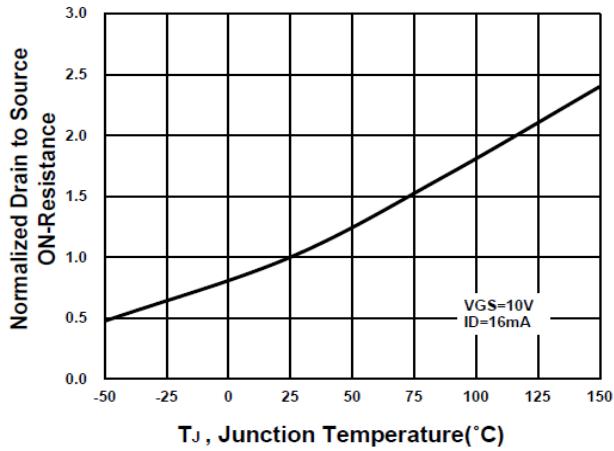
**Capacitance Characteristic**



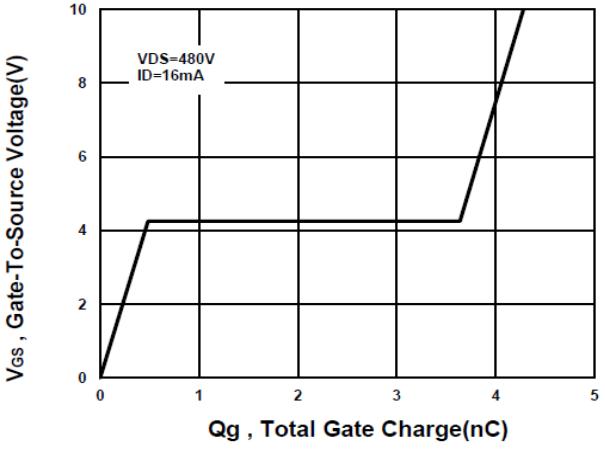
**On-Resistance VS Drain Current**



**On-Resistance VS Temperature**



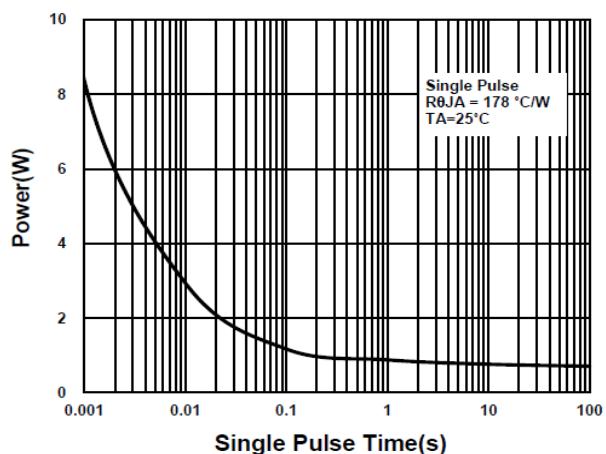
**Gate charge Characteristics**



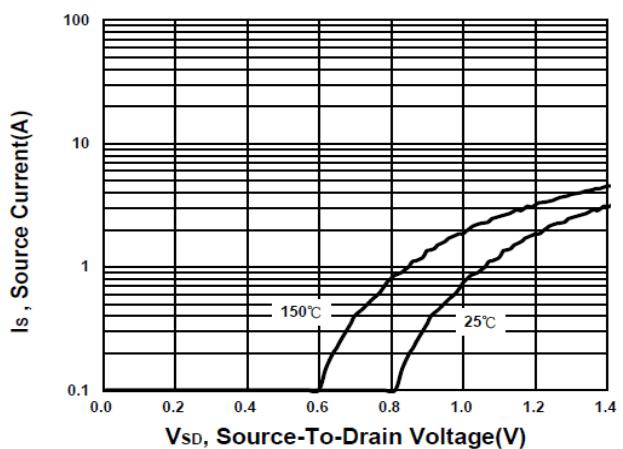
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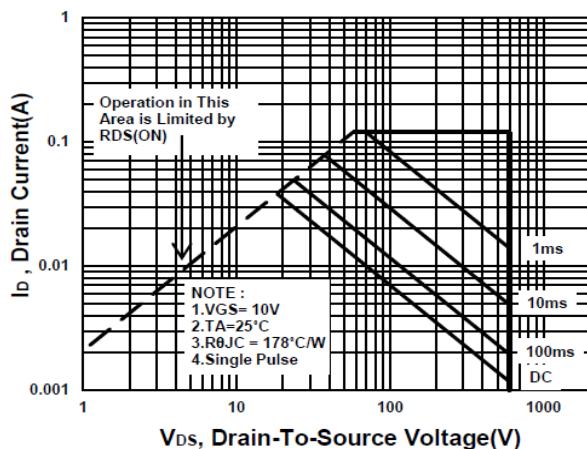
**Single Pulse Maximum Power Dissipation**



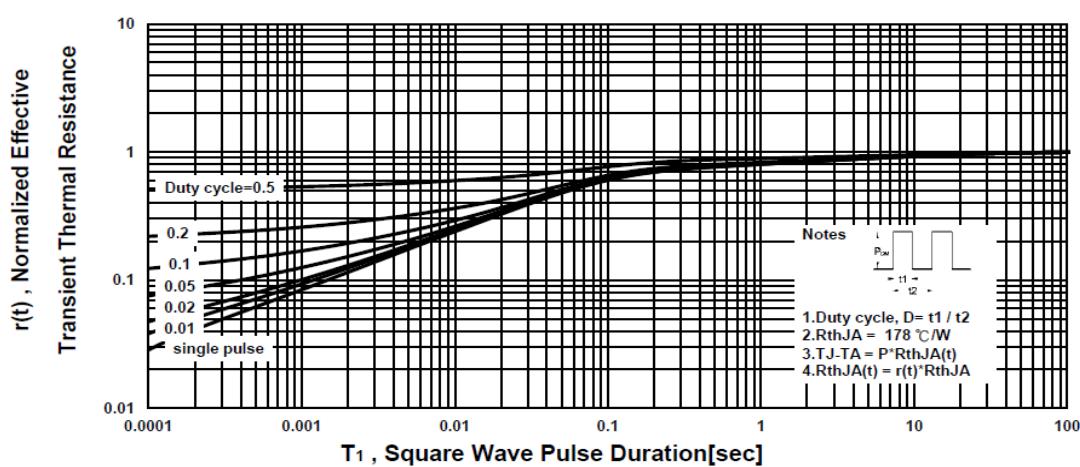
**Source-Drain Diode Forward Voltage**



**Safe Operating Area**



**Transient Thermal Response Curve**



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### Package Dimension

#### SOT-23 (S) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.25		2.85	I	0.15		0.6
C	1.2		1.4				
D	2.8		3.04				
E	0.89		1.2				
F	0		0.1				
G	0.3		0.5				

