

P2N4031

P2N4033

CASE 29-03, STYLE 1
TO-92 (TO-226AE)

ONE WATT
AMPLIFIER TRANSISTORS

PNP SILICON

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector Current - Continuous	I _C	1.0	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.0 8.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	2.5 20	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{θJC}	50	°C/W
Thermal Resistance, Junction to Ambient	R _{θJC}	125	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage (I _C = 10 mA)	V _{(BR)CEO}	80	—	V
Collector-Base Breakdown Voltage (I _C = 10 μA)	V _{(BR)CBO}	80	—	V
Emitter-Base Breakdown Voltage (I _E = 10 μA)	V _{(BR)EBO}	5.0	—	V
Collector Cutoff Current (V _{CB} = 60 V) (V _{CB} = 60 V, T _A = 150°C)	I _{CBO}	— —	5.0 50	nA μA
Emitter Cutoff Current (V _{EB} = 5.0 V)	I _{EBO}	—	10	nA

ON CHARACTERISTICS

DC Current Gain (I _C = 100 mA, V _{CE} = 5.0 V, -55°C)	P2N4031 P2N4033	h _{FE}	15 40	— —	—
(I _C = 100 μA, V _{CE} = 5.0 V)	P2N4031 P2N4033		30 75	— —	
(I _C = 100 mA, V _{CE} = 5.0 V)	P2N4031 P2N4033		40 100	120 300	
(I _C = 500 mA, V _{CE} = 5.0 V)	P2N4031 P2N4033		25 70	— —	
(I _C = 1.0 A, V _{CE} = 5.0 V)	P2N4031 P2N4033		10 25	— —	
Collector-Emitter Saturation Voltage (I _C = 150 mA, I _B = 15 mA) (I _C = 500 mA, I _B = 50 mA)		V _{CE(sat)}	— —	0.15 0.50	V
Base-Emitter Saturation Voltage (I _C = 150 mA, I _B = 15 mA) (I _C = 500 mA, I _B = 50 mA)		V _{BE(sat)}	— —	0.9 1.1	V

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ELECTRICAL CHARACTERISTICS (continued) ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
SMALL-SIGNAL CHARACTERISTICS				
Output Capacitance ($V_{CE} = 10 \text{ V}$, $f = 1.0 \text{ MHz}$)	C_{obo}	—	25	pF
Input Capacitance ($V_{EB} = 0.5 \text{ V}$, $f = 1.0 \text{ MHz}$)	C_{ibo}	—	150	pF
Current Gain — Bandwidth Product ($I_C = 50 \text{ mA}$, $V_{CC} = 10 \text{ V}$, $f = 100 \text{ MHz}$)	f_T	150		MHz
SWITCHING CHARACTERISTICS				
Turn-On Time (see Figure 1) ($I_C = 500 \text{ mA}$, $I_{B1} = 50 \text{ mA}$)	t_{on}	—	100	ns
Turn-Off Time (see Figure 1) ($I_C = 500 \text{ mA}$, $I_{B1} = I_{B2} = 50 \text{ mA}$)	t_{off}	—	400	ns

(1) Pulse Width = 300 μs , Duty Cycle 1.0%.

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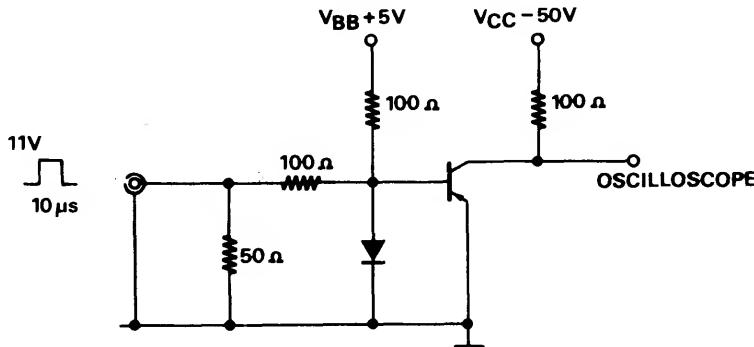


FIGURE 1: SWITCHING TIMES TEST CIRCUIT