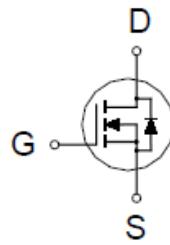
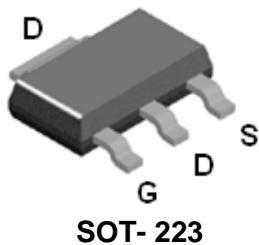


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N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
25V	72m Ω @ $V_{GS} = 10V$	6A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_A = 25^\circ C$	I_D	6	A
$T_A = 70^\circ C$	I_D	3.3	
Pulsed Drain Current ²	I_{DM}	21	
Avalanche Current	I_{AS}	12	
Avalanche Energy	E_{AS}	7.5	mJ
Power Dissipation $T_A = 25^\circ C$	P_D	3	W
$T_A = 70^\circ C$	P_D	1.1	
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$	12	42	°C / W
Junction-to-Ambient	$R_{\theta JA}$			

¹Pulse width limited by maximum junction temperature.

²Limited by package.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	25			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.8	1.5	2.5	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
On-State Drain Current ¹	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 5\text{V}, V_{\text{GS}} = 10\text{V}$	21			A
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 3\text{A}$		53	96	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 6\text{A}$		35	72	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 6\text{A}$		9		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$		220		pF
Output Capacitance	C_{oss}			114		
Reverse Transfer Capacitance	C_{rss}			72		
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 0.5V_{(\text{BR})\text{DSS}}, I_D = 6\text{A}, V_{\text{GS}} = 10\text{V}$		5.7		nC
Gate-Source Charge ²	Q_{gs}			0.8		
Gate-Drain Charge ²	Q_{gd}			1.7		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 15\text{V}, I_D \approx 6\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GS}} = 6\Omega$		10		nS
Rise Time ²	t_r			22		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			22		
Fall Time ²	t_f			8		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				2	A
Forward Voltage ¹	V_{SD}	$I_F = 6\text{A}, V_{\text{GS}} = 0\text{V}$			1.5	V
Reverse Recovery Time	t_{rr}	$I_F = 6\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		30		nS
Reverse Recovery Charge	Q_{rr}		$V_{\text{GS}} = 0\text{V}$		12	μC

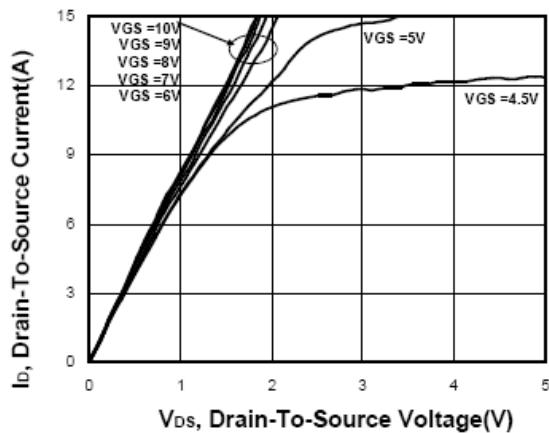
¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

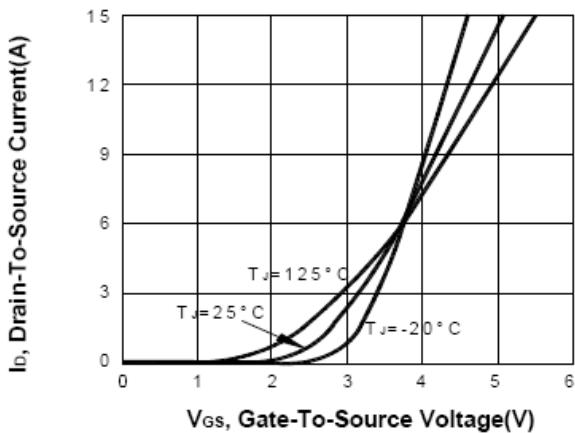
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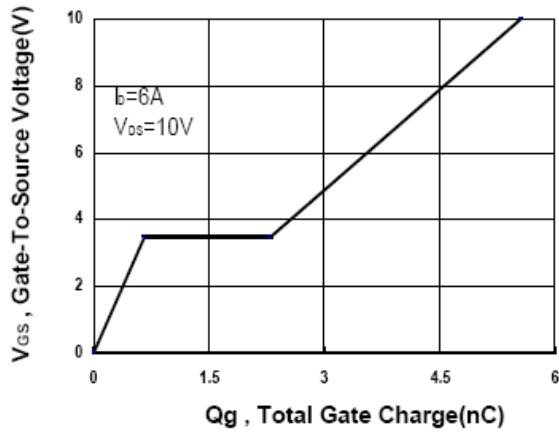
Output Characteristics



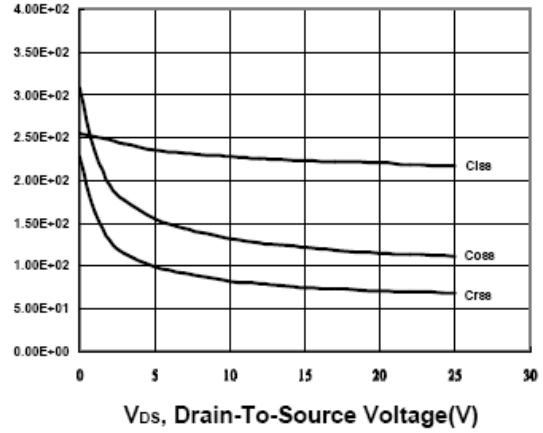
Transfer Characteristics



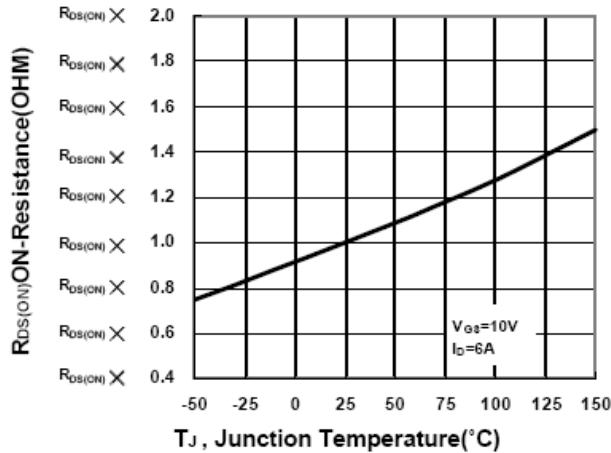
Gate charge Characteristics



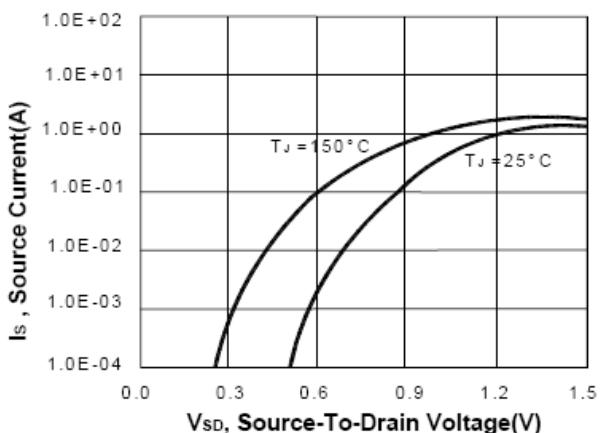
Capacitance Characteristic



On-Resistance VS Temperature



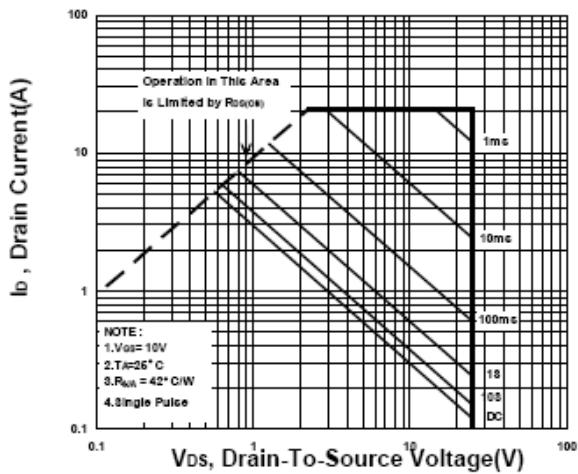
Source-Drain Diode Forward Voltage



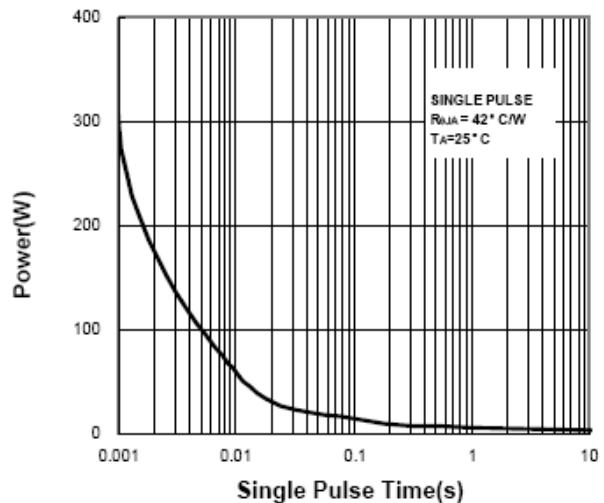
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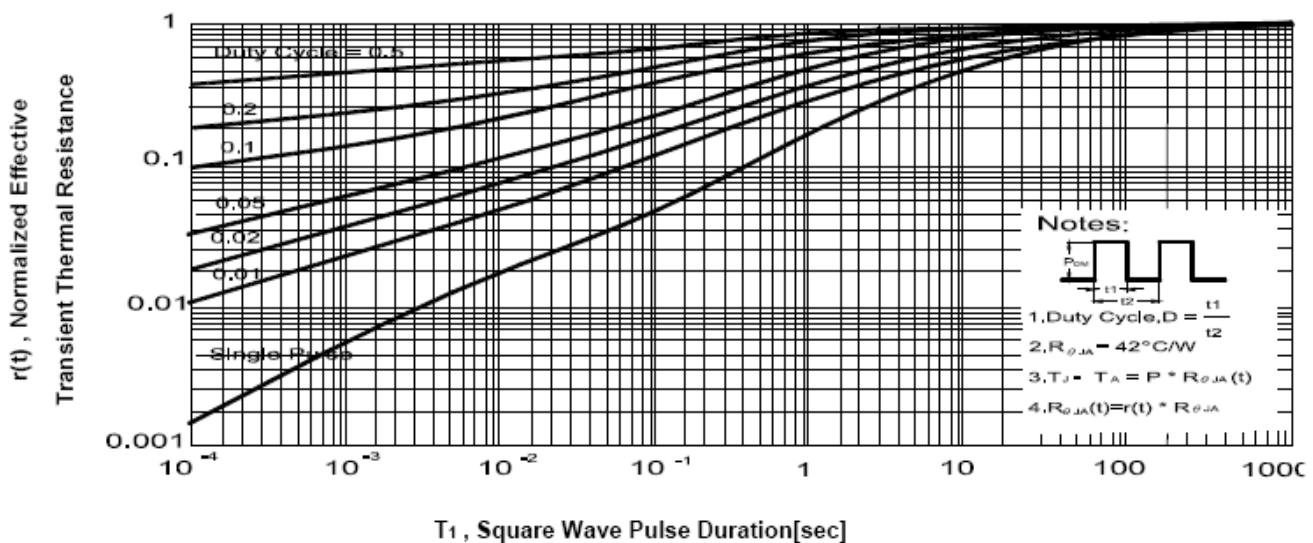
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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SOT-223 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.60	0.76	0.84	H	3.30	3.50	3.70
B	6.70	7.00	7.30	I	0.50	1.00	1.20
C	2.85	3.00	3.10	J	0.23	0.3	0.4
D	2.25	2.30	2.35	K	0°		10°
E	4.35	4.60	4.85	L	0	0.1	0.2
F	1.40	1.60	1.80	M			
G	6.30	6.50	6.80	N			

