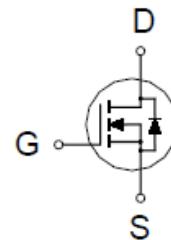
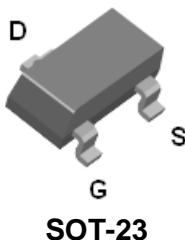


# P3202CMG

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
20V	32mΩ @ $V_{GS} = 4.5V$	5A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current $T_A = 25^\circ C$	$I_D$	5	A
		4	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	30	
Power Dissipation $T_A = 25^\circ C$	$P_D$	1.25	W
		0.8	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$
Lead Temperature (1/16" from case for 10 sec.)	$T_L$	275	

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$	75	100	$^\circ C / W$

<sup>1</sup>Pulse width limited by maximum junction temperature.

## P3202CMG

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#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

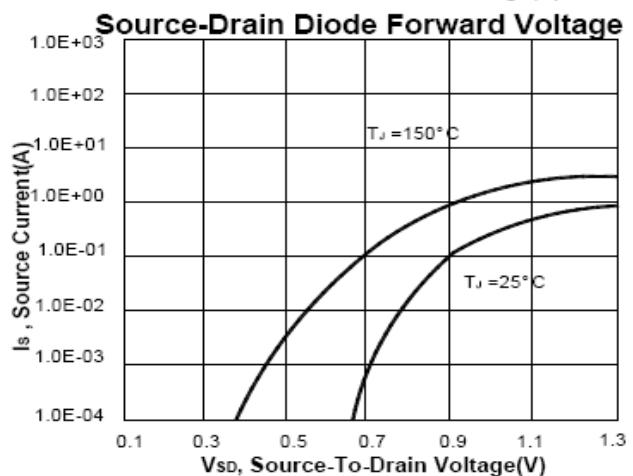
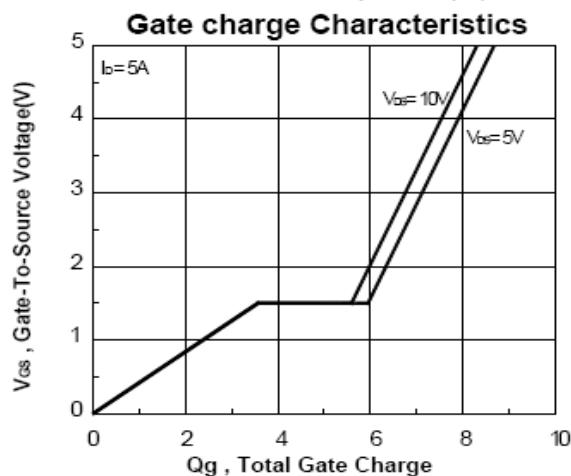
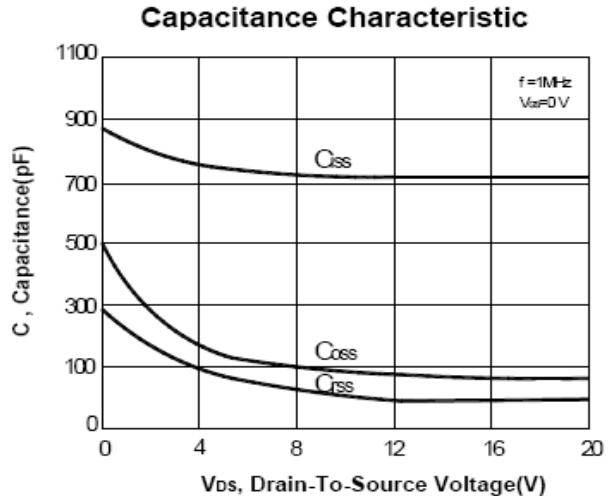
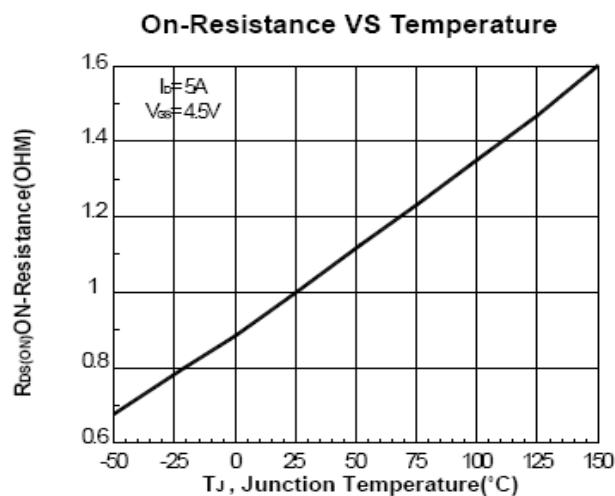
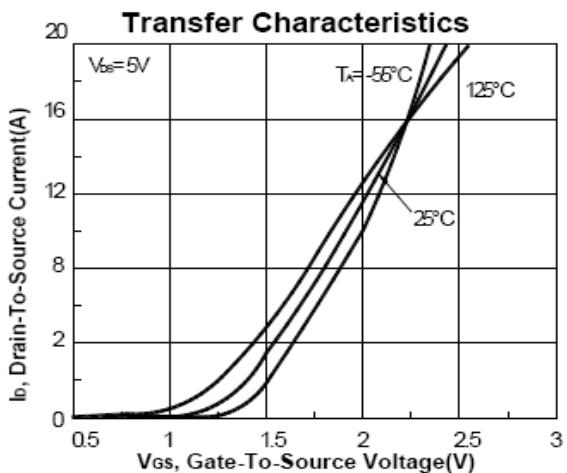
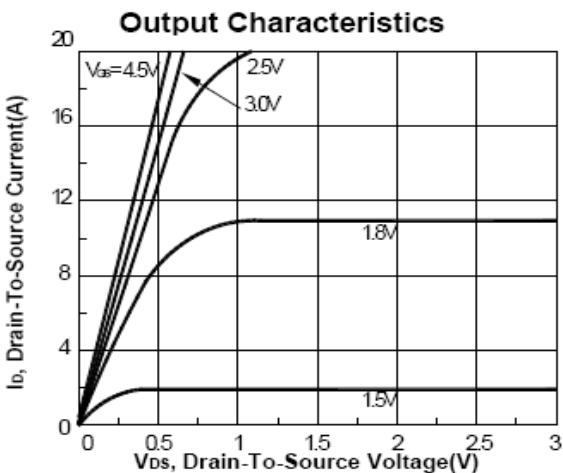
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.45	0.75	1.2	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 12\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 16\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 16\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 70^\circ\text{C}$			10	
On-State Drain Current <sup>1</sup>	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 10\text{V}$	5			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 1.8\text{V}, I_D = 2\text{A}$		57	80	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 4\text{A}$		38	50	
		$V_{\text{GS}} = 4.5\text{V}, I_D = 5\text{A}$		27	32	
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5\text{V}, I_D = 5\text{A}$		12		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 10\text{V}, f = 1\text{MHz}$		740		pF
Output Capacitance	$C_{\text{oss}}$			90		
Reverse Transfer Capacitance	$C_{\text{rss}}$			66		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 0.5V_{(\text{BR})\text{DSS}}, V_{\text{GS}} = 4.5\text{V}, I_D = 5\text{A}$		8	12	nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			3.6		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			2		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}$ $I_D \approx 1\text{A}, V_{\text{GEN}} = 4.5\text{V}, R_G = 0.2\Omega$		8	14	nS
Rise Time <sup>2</sup>	$t_r$			6	12	
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			19	45	
Fall Time <sup>2</sup>	$t_f$			7	23	
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( <math>T_J = 25^\circ\text{C}</math> )</b>						
Continuous Current	$I_S$				1	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = I_S, V_{\text{GS}} = 0\text{V}$			1.3	V

<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

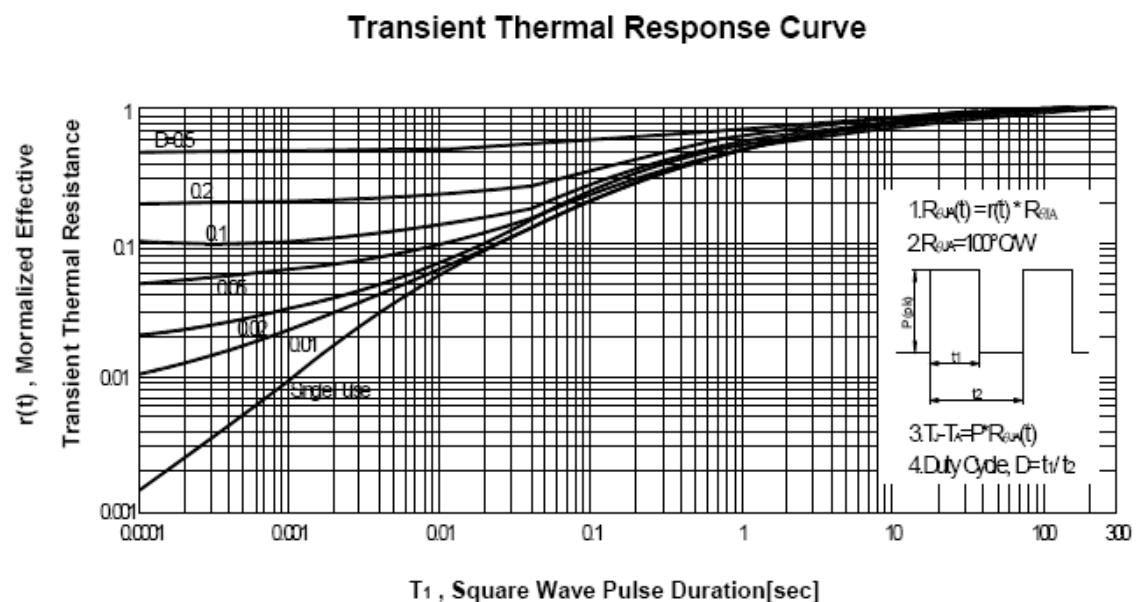
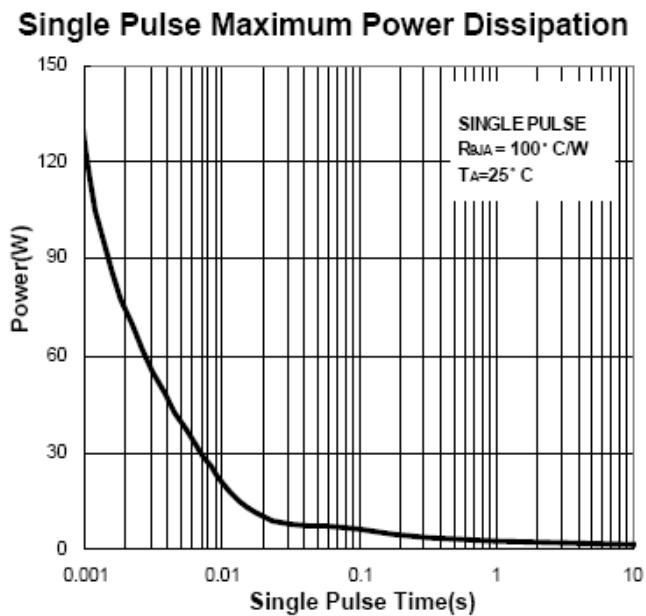
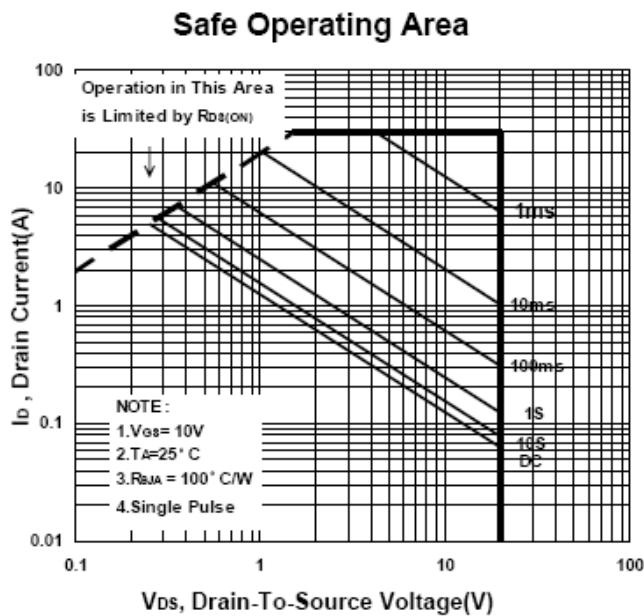
## P3202CMG

### N-Channel Enhancement Mode MOSFET



## P3202CMG

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# P3202CMG

## N-Channel Enhancement Mode MOSFET

### Package Dimension

#### SOT-23-3 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A		1.05		H	0.1		0.2
B	2.4		3	I	0.3		0.6
C	1.4		1.73				
D	2.7		3.1				
E	1		1.31				
F	0		0.15				
G	0.3		0.5				

