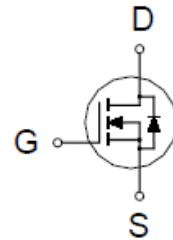
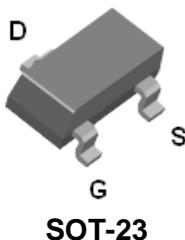


# P3203CMG

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	32mΩ @ $V_{GS} = 4.5V$	6A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current <sup>2</sup>	$I_D$	6	A
		5	
Pulsed Drain Current <sup>1,2</sup>	$I_{DM}$	30	
Power Dissipation	$P_D$	1.25	W
		0.8	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$		100	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Limited only by maximum temperature allowed.

# P3203CMG

## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

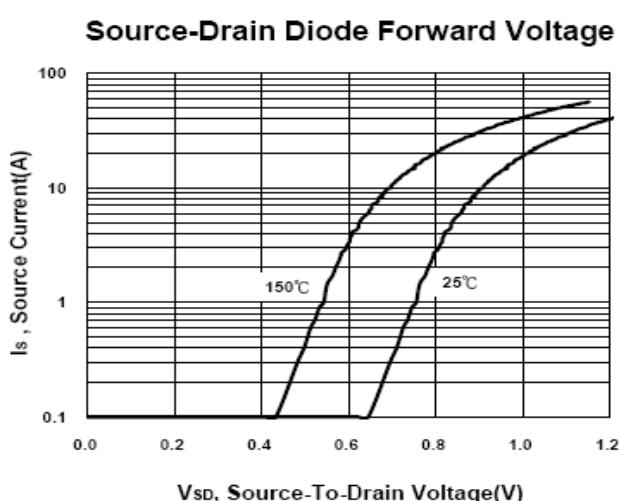
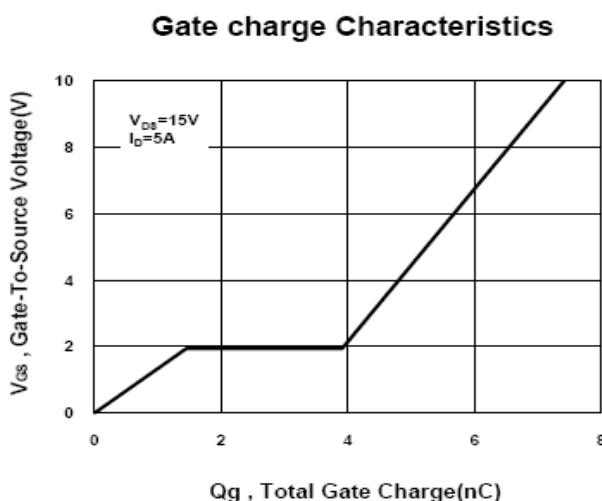
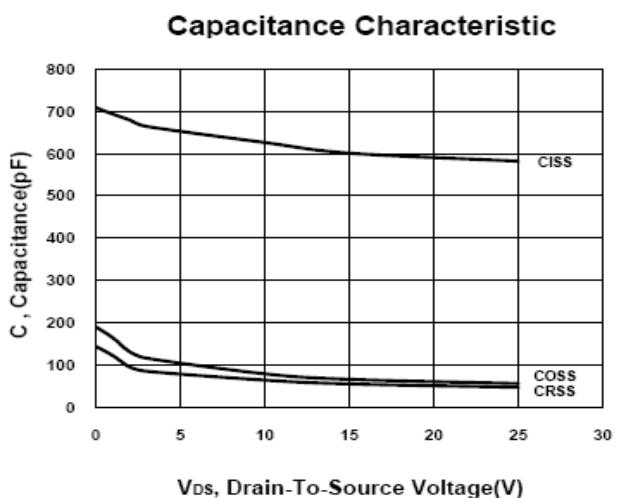
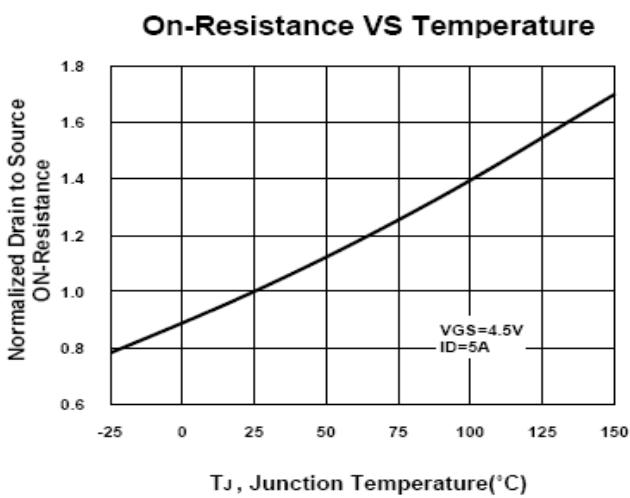
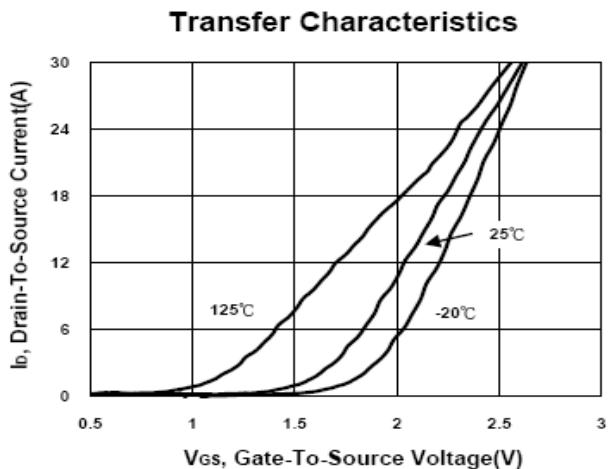
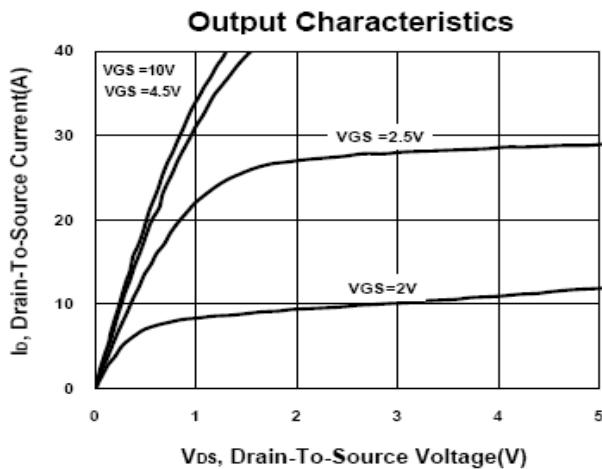
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			MIN	TYP	MAX		
<b>STATIC</b>							
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0V, I_D = 250\mu\text{A}$	30			V	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.45	0.7	1.2		
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0V, V_{\text{GS}} = \pm 12V$			$\pm 100$	nA	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 24V, V_{\text{GS}} = 0V$			1	$\mu\text{A}$	
		$V_{\text{DS}} = 20V, V_{\text{GS}} = 0V, T_J = 55^\circ\text{C}$			10		
On-State Drain Current <sup>1</sup>	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 5V, V_{\text{GS}} = 4.5V$	30			A	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 2.5V, I_D = 4A$		32	52	$\text{m}\Omega$	
		$V_{\text{GS}} = 4.5V, I_D = 5A$		24	32		
		$V_{\text{GS}} = 10V, I_D = 6A$		22	28		
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5V, I_D = 5A$		33		S	
<b>DYNAMIC</b>							
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0V, V_{\text{DS}} = 15V, f = 1\text{MHz}$		620		pF	
Output Capacitance	$C_{\text{oss}}$			69			
Reverse Transfer Capacitance	$C_{\text{rss}}$			62			
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 15V, V_{\text{GS}} = 4.5V, I_D = 5A$		8		nC	
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			1.5			
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			3			
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 15V$ $I_D \approx 5A, V_{\text{GS}} = 4.5V, R_{\text{GS}} = 6\Omega$		4.5		nS	
Rise Time <sup>2</sup>	$t_r$			4			
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			37			
Fall Time <sup>2</sup>	$t_f$			6			
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( <math>T_J = 25^\circ\text{C}</math> )</b>							
Continuous Current	$I_S$				6	A	
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 1.3A, V_{\text{GS}} = 0V$			1.3	V	
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 6A, dI_F/dt = 100 \text{ A}/\mu\text{s}$		10.5		nS	
Reverse Recovery Charge	$Q_{\text{rr}}$			2.1		$\mu\text{C}$	

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

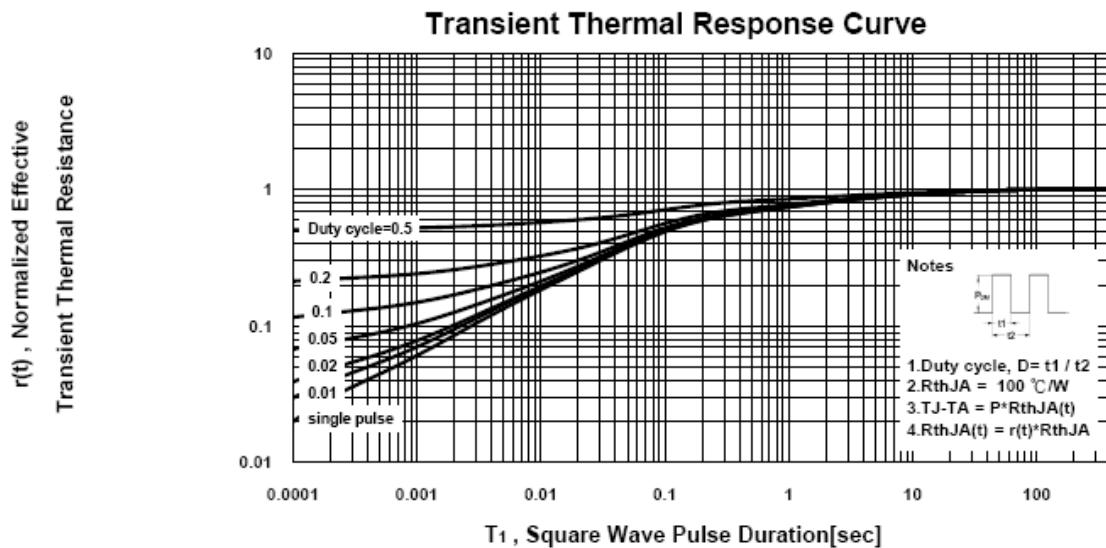
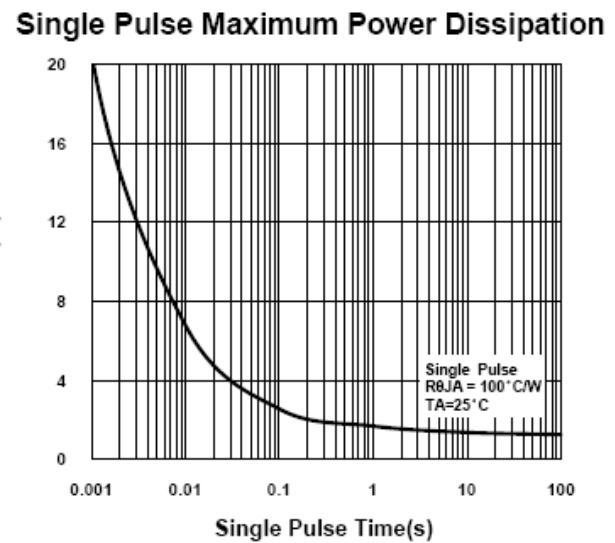
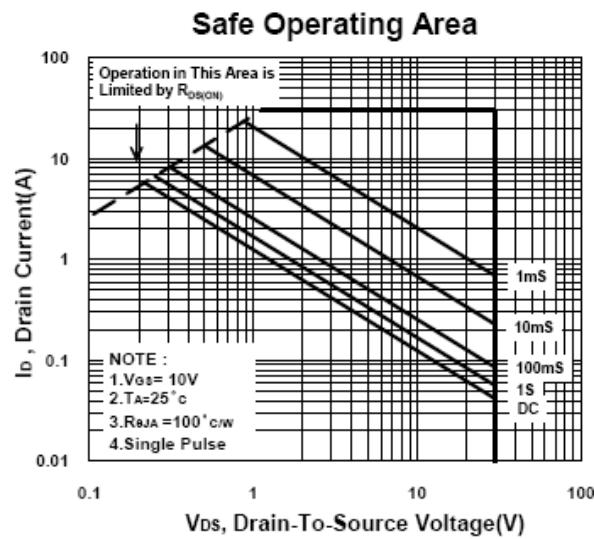
## P3203CMG

### N-Channel Enhancement Mode MOSFET



## P3203CMG

### N-Channel Enhancement Mode MOSFET



# P3203CMG

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### SOT-23 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A		1.05		H	0.1		0.2
B	2.4		3	I	0.3		0.6
C	1.4		1.73				
D	2.7		3.1				
E	1		1.31				
F	0		0.15				
G	0.3		0.5				

