

# P34F6EL

Power MOSFETs  
60V, 34A, N-channel

## Feature

- N-channel
- Isolated Package
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Pb free terminal
- RoHS:Yes

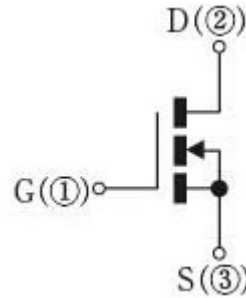
## OUTLINE

Package (House Name): FTO-220AG

Package (JEITA Code): SC-91



## Equivalent circuit



## Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

| Item                           | Symbol           | Conditions  | Ratings    | Unit |
|--------------------------------|------------------|---|------------|------|
| Storage temperature            | Tstg             |   | -55 to 150 | °C   |
| Channel temperature            | Tch              |   | -55 to 150 | °C   |
| Drain-source voltage           | V <sub>DSS</sub> |   | 60         | V    |
| Gate-source voltage            | V <sub>GSS</sub> |   | ±20        | V    |
| Continuous drain current(DC)   | I <sub>D</sub>   |   | 34         | A    |
| Continuous drain current(Peak) | I <sub>DP</sub>  | Pulse width 10μs, duty=1/100                          | 136        | A    |
| Total power dissipation        | P <sub>T</sub>   |   | 35         | W    |
| Single avalanche current       | I <sub>AS</sub>  | Starting T <sub>ch</sub> =25°C T <sub>ch</sub> ≤150°C | 27         | A    |
| Single avalanche energy        | E <sub>AS</sub>  | Starting T <sub>ch</sub> =25°C T <sub>ch</sub> ≤150°C | 36         | mJ   |
| Dielectric strenght            | V <sub>dis</sub> | Terminals to case, AC1min                             | 2          | kV   |
| Mounting torque                | TOR              | (Recommended torque : 0.3N·m)                         | 0.5        | N·m  |

\* : See the original Specifications

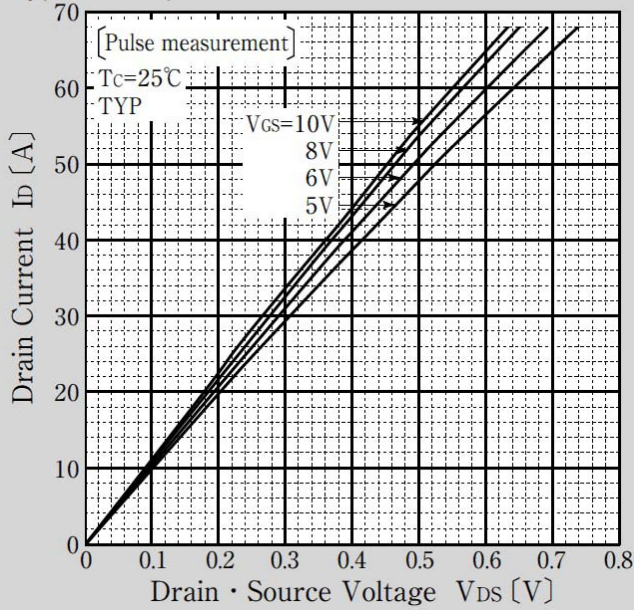
**Electrical Characteristics** (unless otherwise specified : Tc=25°C)

| Item                                    | Symbol        | Conditions  | Ratings |       |       | Unit |
|---|---------------|---|---------|-------|-------|------|
|   |               |   | MIN     | TYP   | MAX   |      |
| Drain-Source breakdown voltage          | $V_{(BR)DSS}$ | ID=1mA, VGS=0V  | 60      |       |       | V    |
| Zero gate voltage drain current         | $I_{DSS}$     | VDS=60V, VGS=0V   |         |       | 1     | μA   |
| Gate-source leakage current             | $I_{GSS}$     | VGS=±20V, VDS=0V  |         |       | ±0.1  | μA   |
| Forward transconductance                | $g_{fs}$      | ID=17A, VDS=10V   | 12      |       |       | S    |
| Static drain-source on-state resistance | $R_{DS(ON)}$  | ID=17A, VGS=10V   |         | 0.009 | 0.011 | Ω    |
| Static drain-source on-state resistance | $R_{DS(ON)}$  | ID=17A, VGS=4.5V  |         | 0.011 | 0.015 | Ω    |
| Gate threshold voltage                  | $V_{th}$      | ID=1mA, VDS=10V   | 1.5     | 2     | 2.5   | V    |
| Source-drain diode forward voltage      | $V_{SD}$      | IS=34A, VGS=0V  |         |       | 1.5   | V    |
| Thermal resistance                      | $R_{th(j-c)}$ | Junction to case, with heatsink                         |         |       | 3.55  | °C/W |
| Total gate charge                       | $Q_g$         | VDD=48V, VGS=10V, ID=34A                                |         | 41    |       | nC   |
| Gate to source charge                   | $Q_{gs}$      | VDD=48V, VGS=10V, ID=34A                                |         | 7     |       | nC   |
| Gate to drain charge                    | $Q_{gd}$      | VDD=48V, VGS=10V, ID=34A                                |         | 12    |       | nC   |
| Input capacitance                       | $C_{iss}$     | VDS=25V, VGS=0V, f=1MHz                                 |         | 1960  |       | pF   |
| Reverse transfer capacitance            | $C_{rss}$     | VDS=25V, VGS=0V, f=1MHz                                 |         | 130   |       | pF   |
| Output capacitance                      | $C_{oss}$     | VDS=25V, VGS=0V, f=1MHz                                 |         | 240   |       | pF   |
| Turn-on delay time                      | $t_{d(on)}$   | ID=17A, RL=1.76Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V |         | 8     |       | ns   |
| Rise time                               | $t_r$         | ID=17A, RL=1.76Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V |         | 21    |       | ns   |
| Turn-off delay time                     | $t_{d(off)}$  | ID=17A, RL=1.76Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V |         | 23    |       | ns   |
| Fall time                               | $t_f$         | ID=17A, RL=1.76Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V |         | 4     |       | ns   |
| Diode reverse recovery time             | $t_{rr}$      | IF=34A, VGS=0V, di/dt=100A/μs                           |         | 43    |       | ns   |
| Diode reverse recovery charge           | $Q_{rr}$      | IF=34A, VGS=0V, di/dt=100A/μs                           |         | 57    |       | nC   |

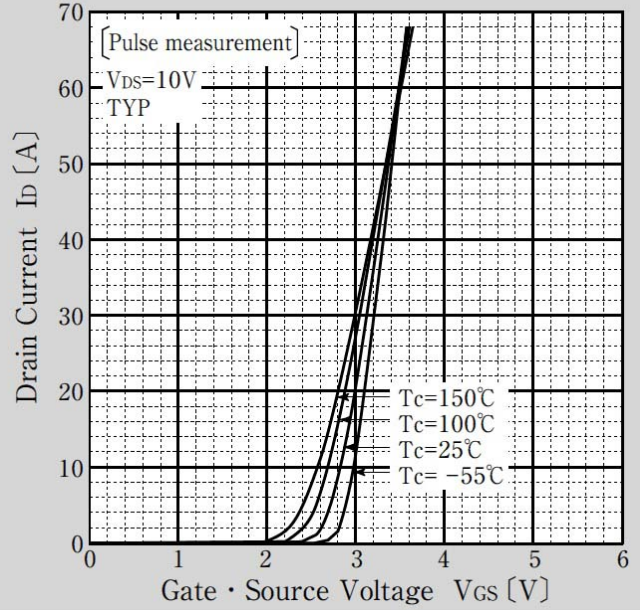
\* : See the original Specifications

# CHARACTERISTIC DIAGRAMS

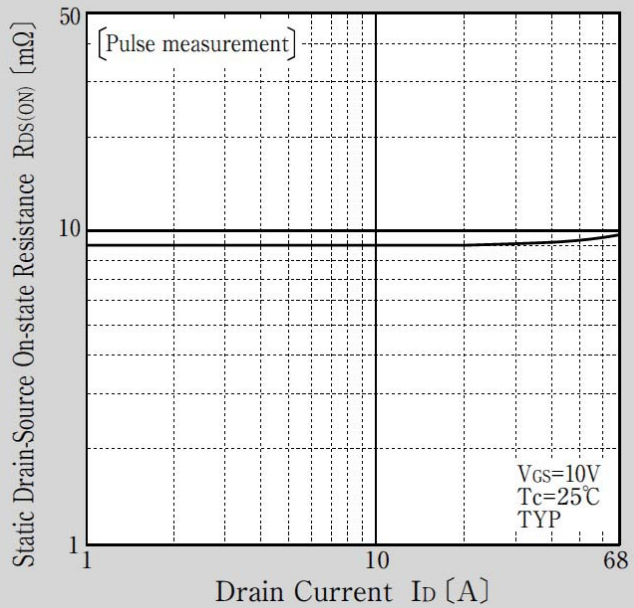
### Typical Output Characteristics



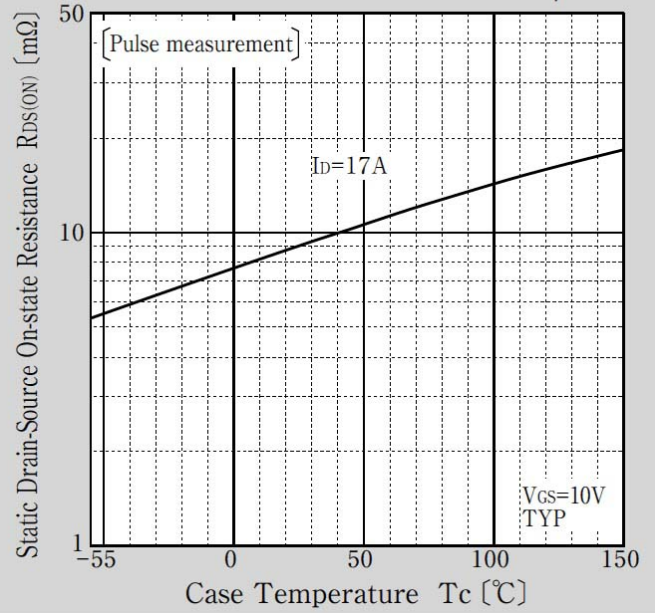
### Transfer Characteristics



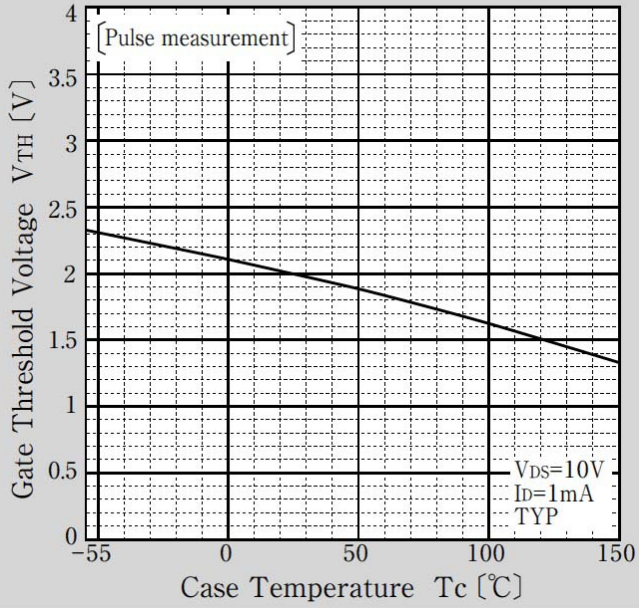
### Static Drain-Source On-state Resistance vs Drain Current



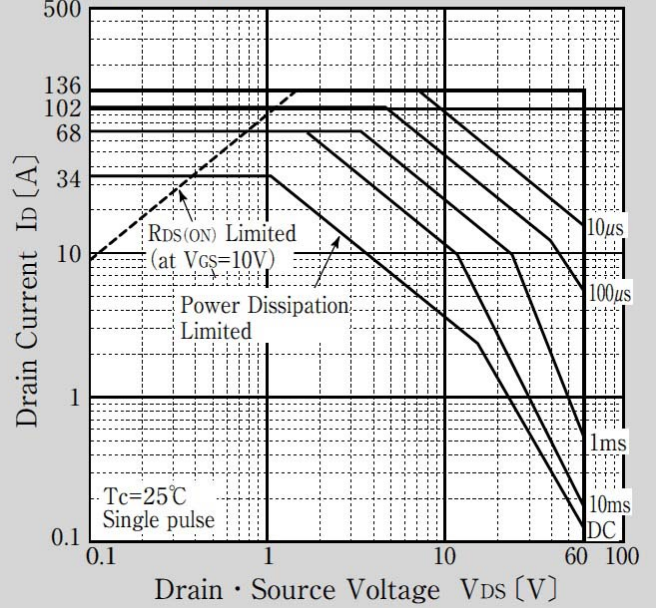
### Static Drain-Source On-state Resistance vs Case Temperature



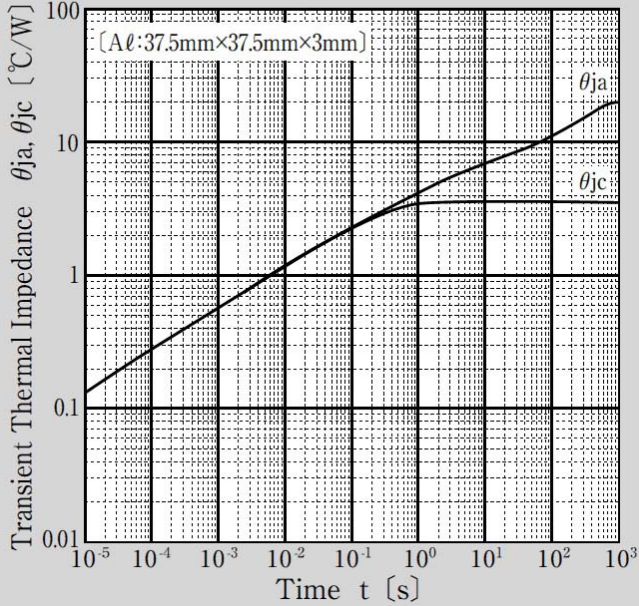
Gate Threshold Voltage vs Case Temperature



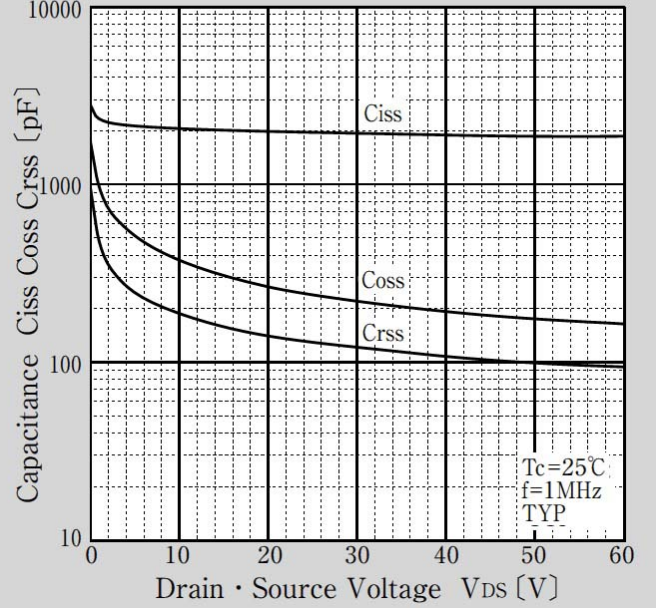
Safe Operating Area



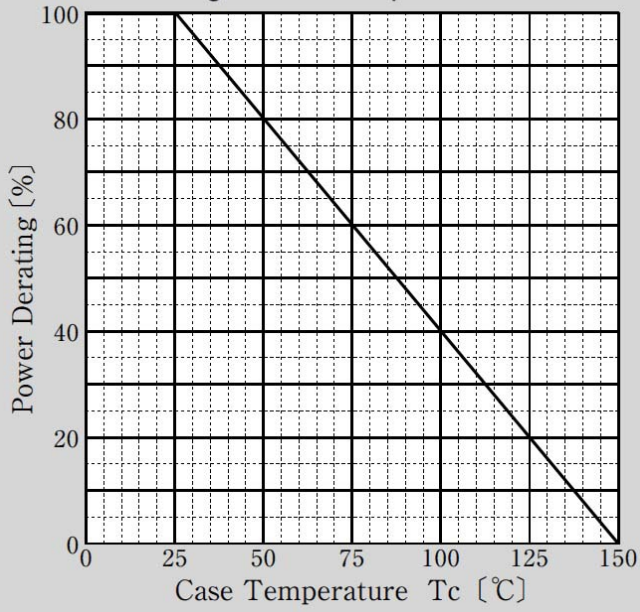
Transient Thermal Impedance



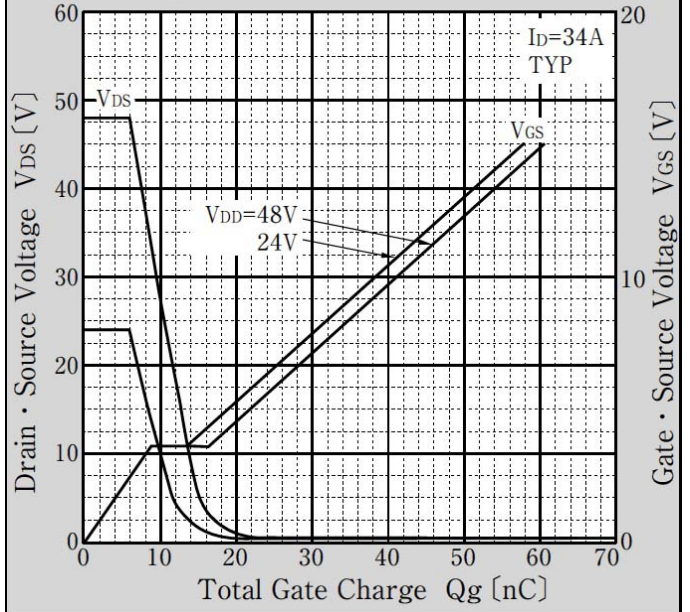
Capacitance Characteristics



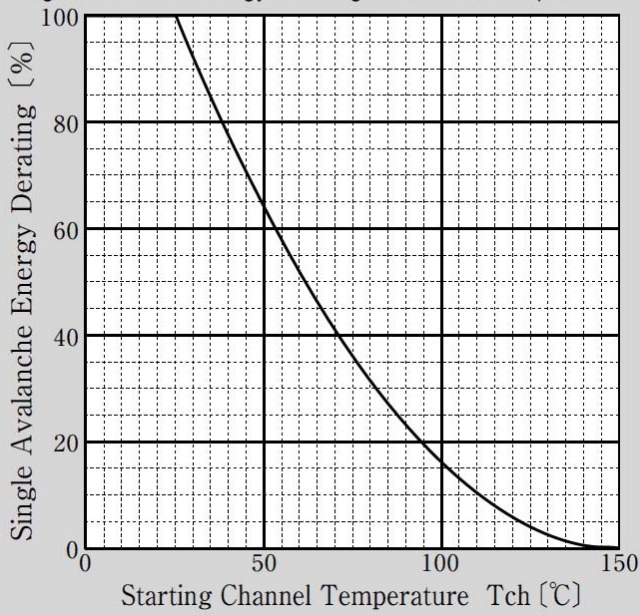
Power Derating - Case Temperature



Gate Charge Characteristics

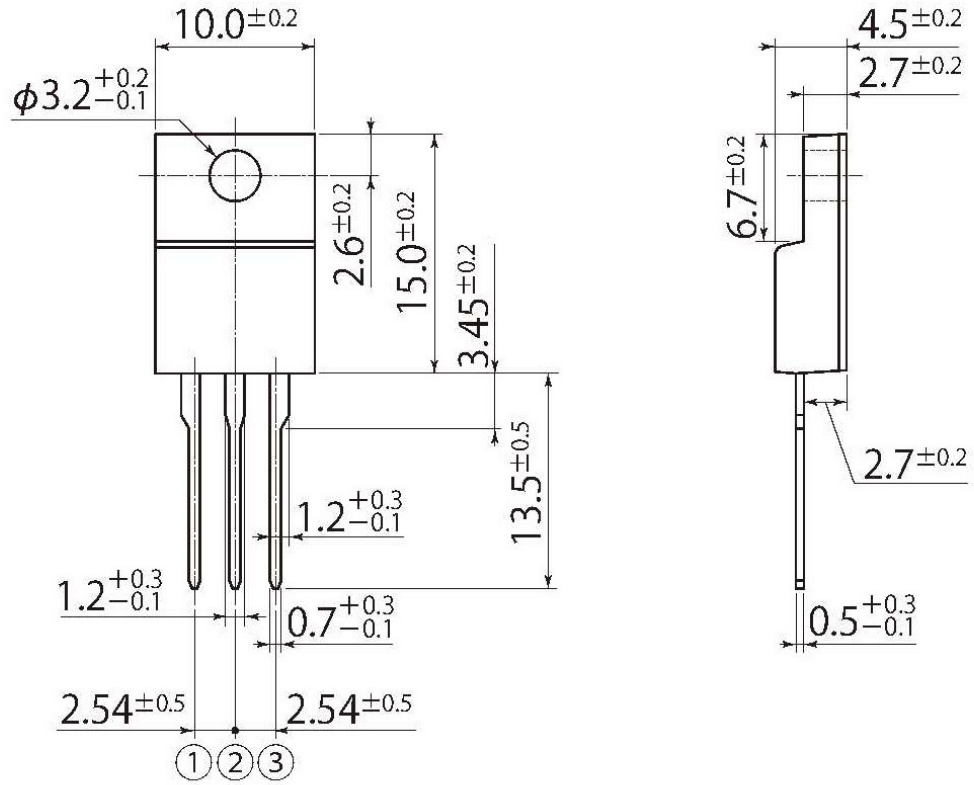


Single Avalanche Energy Derating vs Channel Temperature



J8

|            |                 |
|------------|-----------------|
| JEDEC Code | -               |
| JEITA Code | SC-91           |
| House Name | FTO-220AG(3pin) |



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