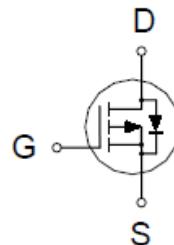
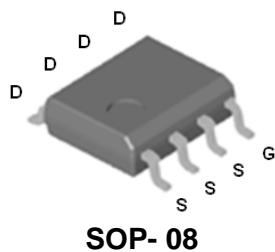


# P3503EVG

## P-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
-30V	35mΩ @ $V_{GS} = -10V$	-8A



### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current $T_C = 25^\circ C$	$I_D$	-8	A
		-7	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	-30	
Power Dissipation $T_C = 25^\circ C$	$P_D$	2.5	W
		1.3	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		25	°C / W
Junction-to-Ambient	$R_{\theta JA}$		50	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Duty cycle ≤ 1%

## P3503EVG

### P-Channel Enhancement Mode MOSFET

#### ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = -250\mu\text{A}$	-30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.8	-1.5	-2.5	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -24V, V_{GS} = 0V$			-1	$\mu\text{A}$
		$V_{DS} = -20V, V_{GS} = 0V, T_J = 125^\circ\text{C}$			-10	
On-State Drain Current <sup>1</sup>	$I_{D(\text{ON})}$	$V_{DS} = -5V, V_{GS} = -10V$	-30			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(\text{ON})}$	$V_{GS} = -4.5V, I_D = -6\text{A}$		44	60	$\text{m}\Omega$
		$V_{GS} = -10V, I_D = -8\text{A}$		28	35	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = -10V, I_D = -6\text{A}$		7		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = -10V, f = 1\text{MHz}$		970		pF
Output Capacitance	$C_{oss}$			370		
Reverse Transfer Capacitance	$C_{rss}$			180		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 0.5V_{(\text{BR})\text{DSS}}, I_D = -8\text{A}, V_{GS} = -10V$		28		nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			6		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			12		
Turn-On Delay Time <sup>2</sup>	$t_{d(\text{on})}$	$V_{DS} = -15V, R_L = 1\Omega, I_D \approx -1\text{A}, V_{GS} = -10V, R_{GS} = 6\Omega$		20		nS
Rise Time <sup>2</sup>	$t_r$			17		
Turn-Off Delay Time <sup>2</sup>	$t_{d(\text{off})}$			180		
Fall Time <sup>2</sup>	$t_f$			75		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_C = 25^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$				-3	A
Pulsed Current <sup>3</sup>	$I_{SM}$				-6	
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = -1\text{A}, V_{GS} = 0V$			-1	V
Reverse Recovery Charge	$Q_{rr}$			7.9		nC

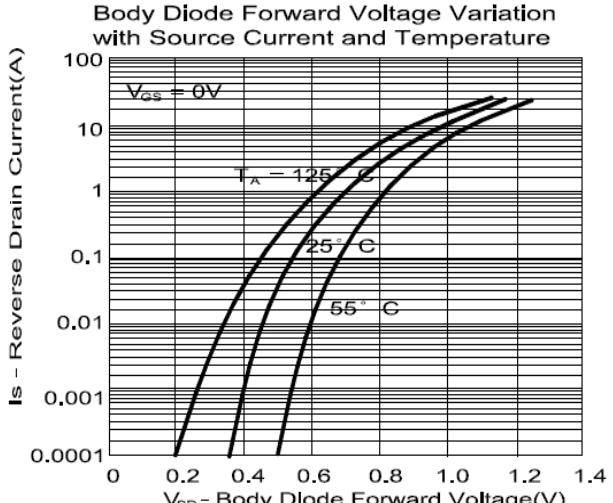
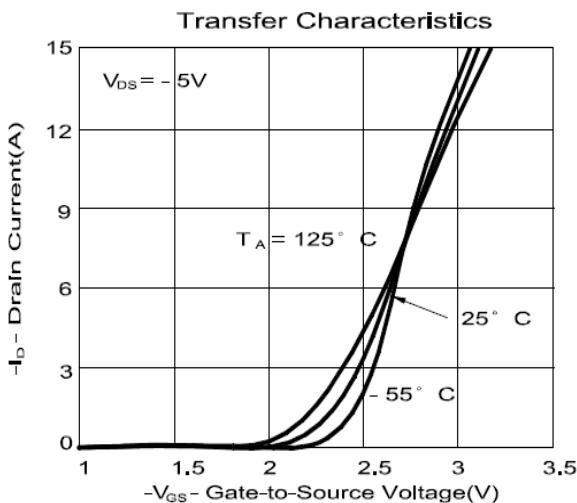
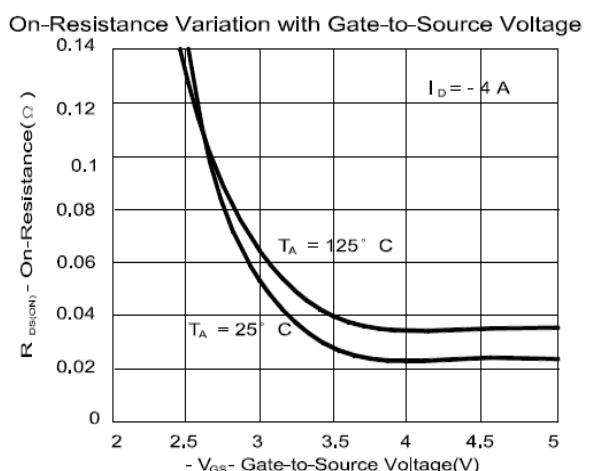
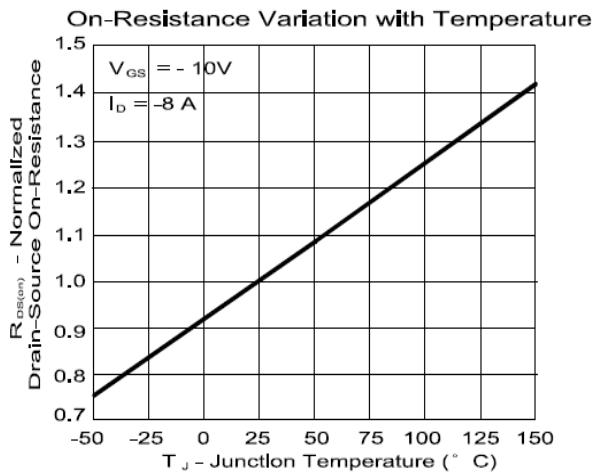
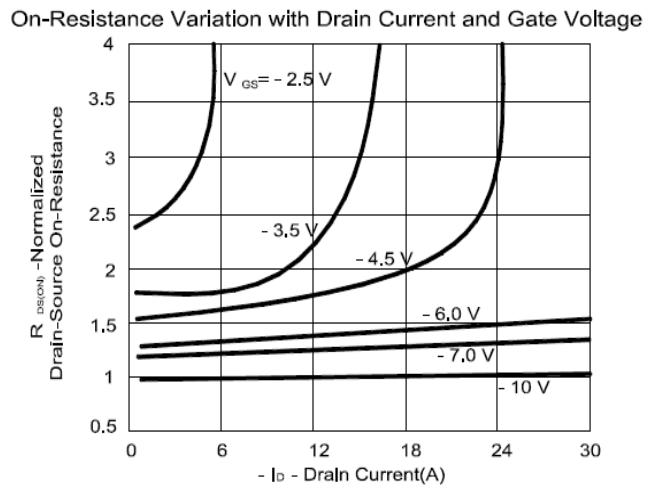
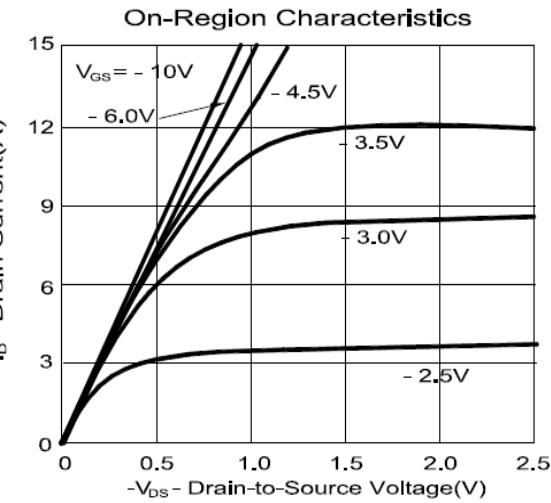
<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

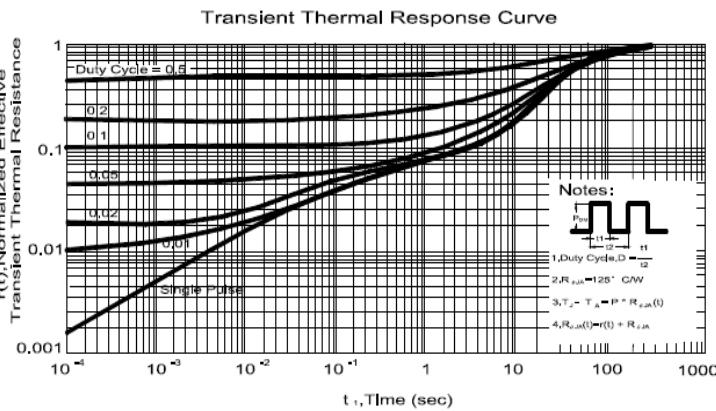
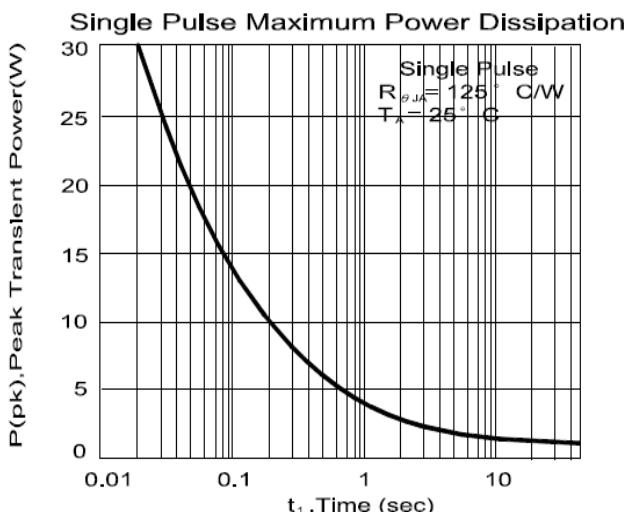
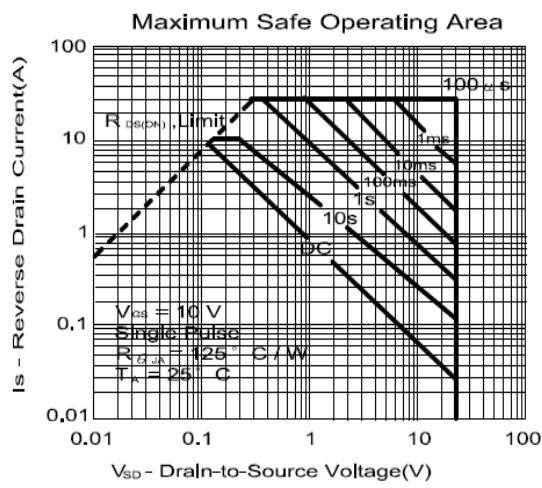
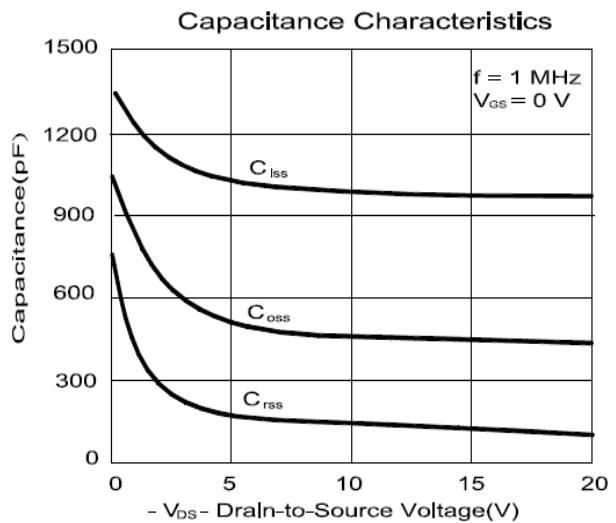
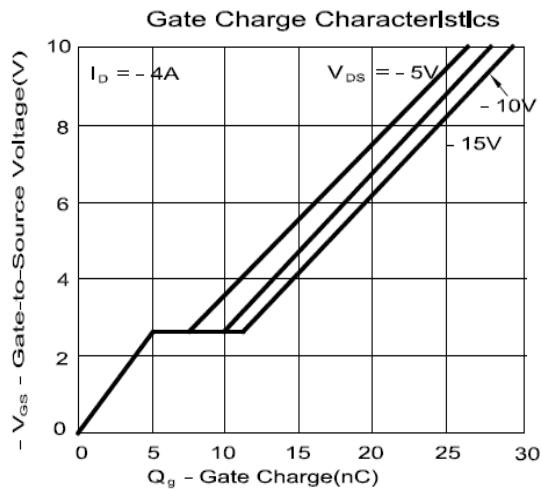
## P3503EVG

### P-Channel Enhancement Mode MOSFET



## P3503EVG

### P-Channel Enhancement Mode MOSFET



# P3503EVG

## P-Channel Enhancement Mode MOSFET

### Package Dimension

### SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

