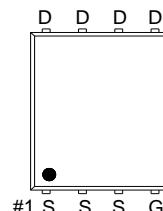
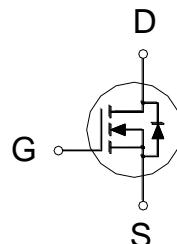


**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****P3606BK  
PDFN 5x6P  
Halogen-Free & Lead-Free****PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
60V	40mΩ	17A

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	17	A
	$T_C = 100^\circ\text{C}$		11	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	40	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	6	
	$T_A = 70^\circ\text{C}$		5	
Avalanche Current		$I_{AS}$	17.8	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	15.8	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	27	
	$T_C = 100^\circ\text{C}$		11	
Power Dissipation <sup>3</sup>	$T_A = 25^\circ\text{C}$	$P_D$	3	
	$T_A = 70^\circ\text{C}$		2	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$t \leq 10\text{s}$	$R_{\theta JA}$	$^{\circ}\text{C} / \text{W}$	40	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient <sup>2</sup>	Steady-State	$R_{\theta JA}$		70	
Junction-to-Case	Steady-State	$R_{\theta JC}$		4.6	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .<sup>3</sup>The Power dissipation is based on  $R_{\theta JA}$   $t \leq 10\text{s}$  value.

**NIKO-SEM**
**N-Channel Enhancement Mode  
Field Effect Transistor**
**P3606BK**  
**PDFN 5x6P**  
**Halogen-Free & Lead-Free**
**ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.3	1.8	2.3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V			1	μA
		V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55 °C			10	
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6A		35	50	mΩ
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A		32	40	
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 6A		30		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz		585		pF
Output Capacitance	C <sub>oss</sub>			77		
Reverse Transfer Capacitance	C <sub>rss</sub>			47		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz		1.5		Ω
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 10V		13.4		nC
		V <sub>GS</sub> = 4.5V		7.7		
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A		1.7		nC
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>			4.8		
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>			18		
Rise Time <sup>2</sup>	t <sub>r</sub>			12		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>	V <sub>DS</sub> = 15V , I <sub>D</sub> ≈ 6A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 6Ω		33		nS
Fall Time <sup>2</sup>	t <sub>f</sub>			11		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current	I <sub>S</sub>				19	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 6A, V <sub>GS</sub> = 0V			1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 6A, dI <sub>F</sub> /dt = 100A / μS		14		nS
Reverse Recovery Charge	Q <sub>rr</sub>			5		nC

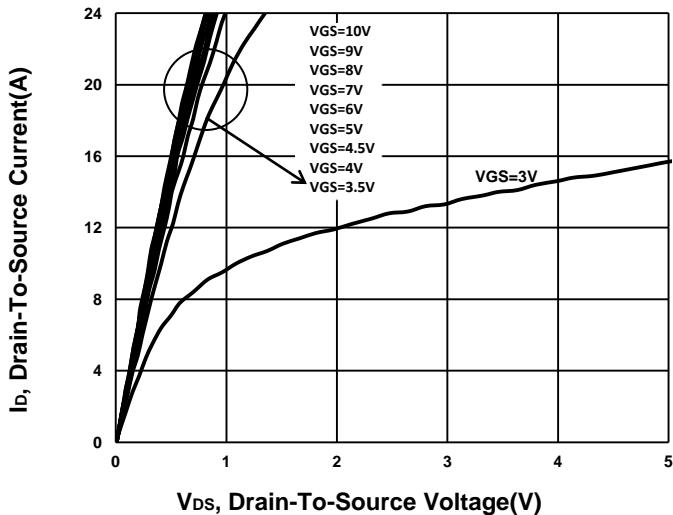
<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.<sup>2</sup>Independent of operating temperature.

**NIKO-SEM**

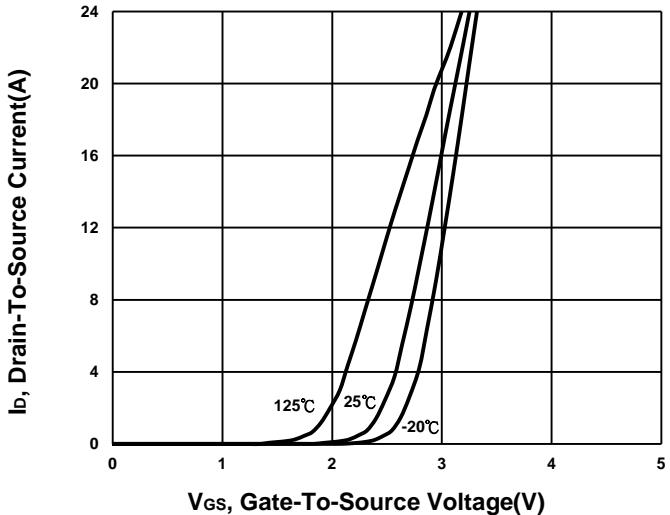
**N-Channel Enhancement Mode  
Field Effect Transistor**

**P3606BK**  
**PDFN 5x6P**  
**Halogen-Free & Lead-Free**

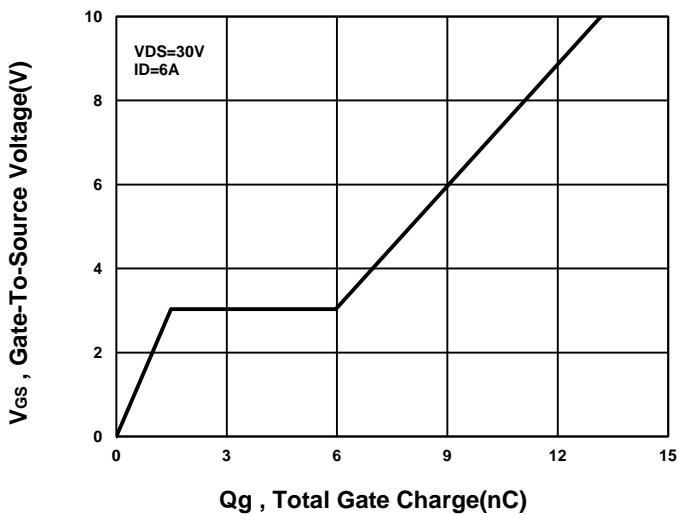
### Output Characteristics



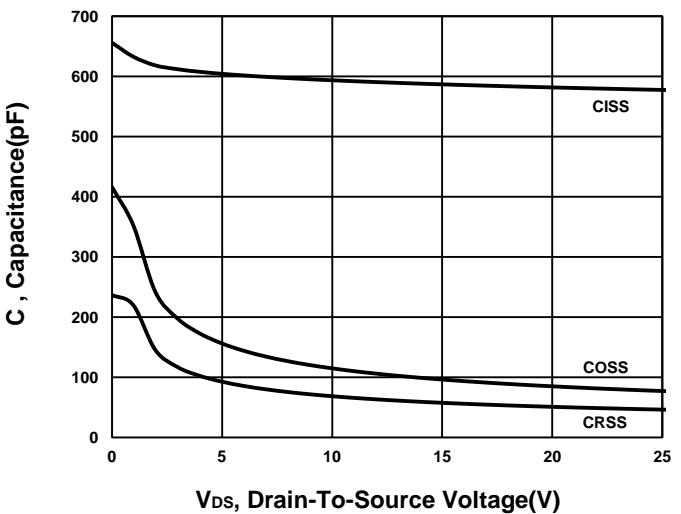
### Transfer Characteristics



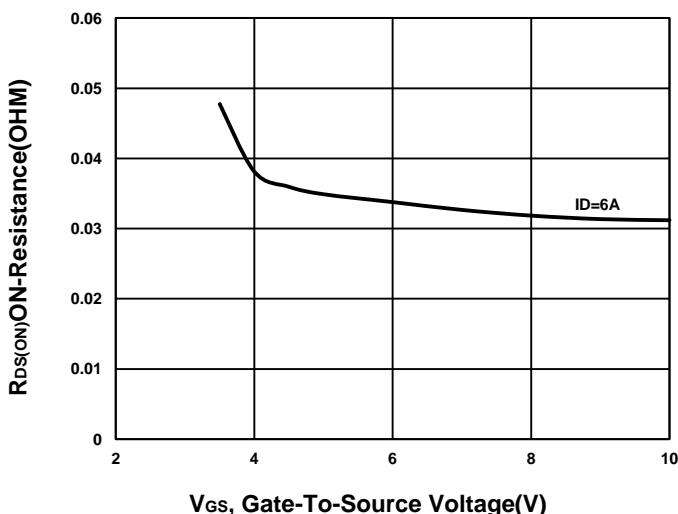
### Gate charge Characteristics



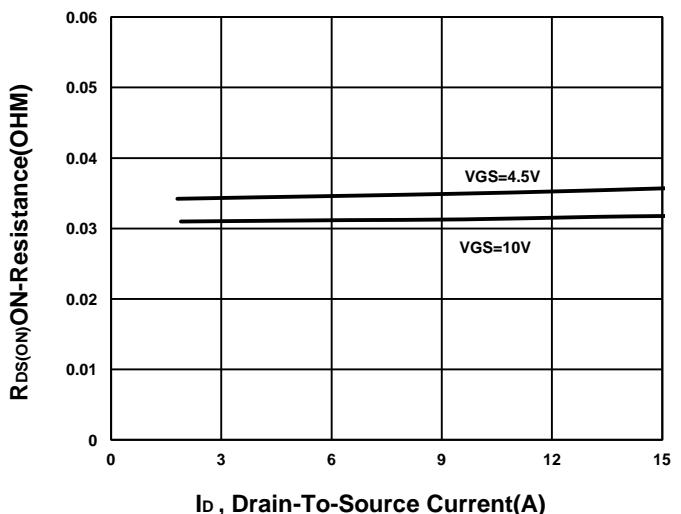
### Capacitance Characteristic

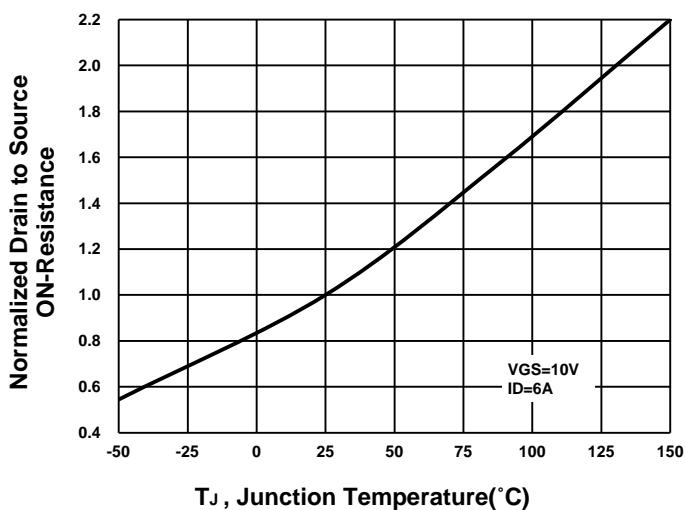
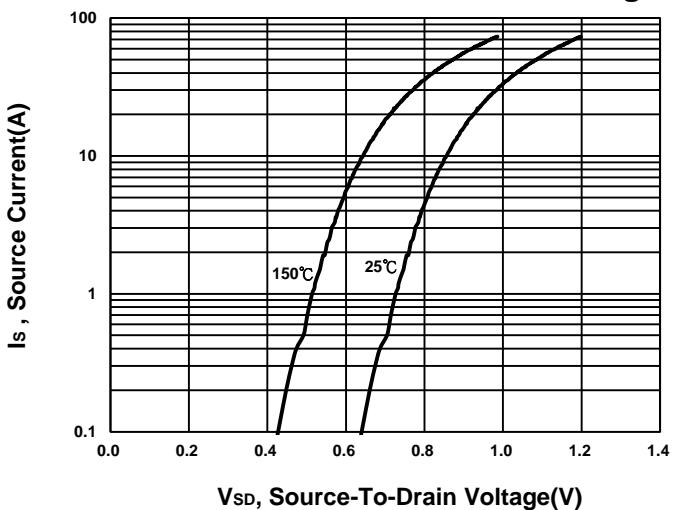
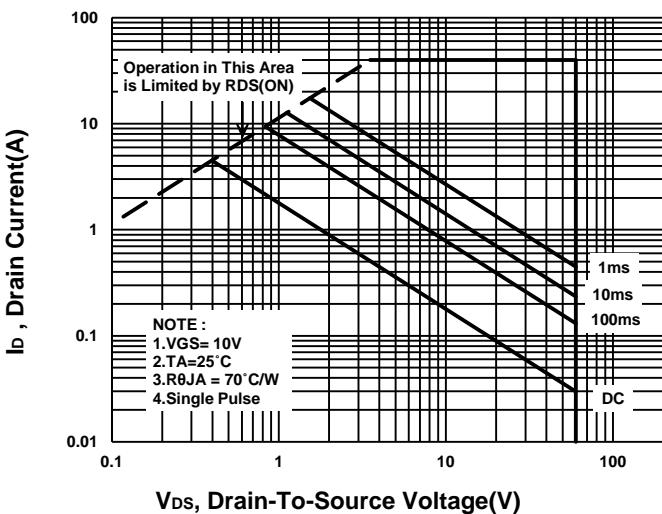
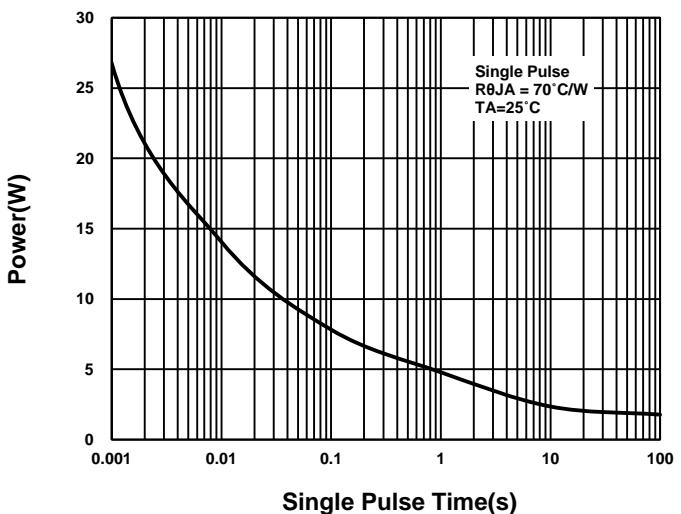


### On-Resistance VS Gate-To-Source



### On-Resistance VS Drain Current



**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****P3606BK  
PDFN 5x6P  
Halogen-Free & Lead-Free****On-Resistance VS Temperature****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**