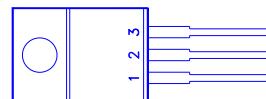
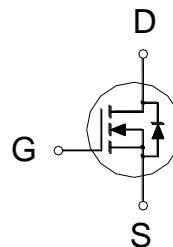


NIKO-SEM
**N-Channel Enhancement Mode
Field Effect Transistor**
P3710BT
TO-220
Halogen-Free & Lead-Free
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	37mΩ	31A


1. GATE
2. DRAIN
3. SOURCE
ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_C = 25^\circ\text{C}$	I_D	31	A
$T_C = 100^\circ\text{C}$	I_D	20	
Pulsed Drain Current ¹	I_{DM}	75	
Avalanche Current	I_{AS}	16	
Avalanche Energy ²	E_{AS}	128	mJ
Power Dissipation $T_C = 25^\circ\text{C}$	P_D	78	W
$T_C = 100^\circ\text{C}$	P_D	21	
Junction & Storage Temperature Range	T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.6	°C / W

¹Pulse width limited by maximum junction temperature.²Starting $T_j=25^\circ\text{C}$, $L=1\text{mH}$, $V_{DD}=50\text{V}$ **ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	1	1.8	3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80\text{V}$, $V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 80\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}$, $I_D = 10\text{A}$		35	48	$\text{m}\Omega$
	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}$, $I_D = 10\text{A}$		27	37	$\text{m}\Omega$
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 10\text{A}$		30		S

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DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		970		pF
Output Capacitance	C_{oss}			129		
Reverse Transfer Capacitance	C_{rss}			59		
Total Gate Charge ²	Q_g	$V_{DS} = 50V, I_D = 10A$ $V_{GS} = 10V$		25		nC
Gate-Source Charge ²	Q_{gs}			4.3		
Gate-Drain Charge ²	Q_{gd}			8.3		
Turn-On Delay Time ²	$t_{d(on)}$			30		
Rise Time ²	t_r	$V_{DD} = 50V$ $I_D \geq 10A, V_{GS} = 10V, R_{GS} = 6\Omega$		21		nS
Turn-Off Delay Time ²	$t_{d(off)}$			45		
Fall Time ²	t_f			21		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current	I_S			31	A	
Forward Voltage ¹	V_{SD}	$I_F = 10A, V_{GS} = 0V$		1.2	V	
Reverse Recovery Time	t_{rr}	$I_F = 10A, dI_F/dt = 100A / \mu S$		36		nS
Reverse Recovery Charge	Q_{rr}			52		nC

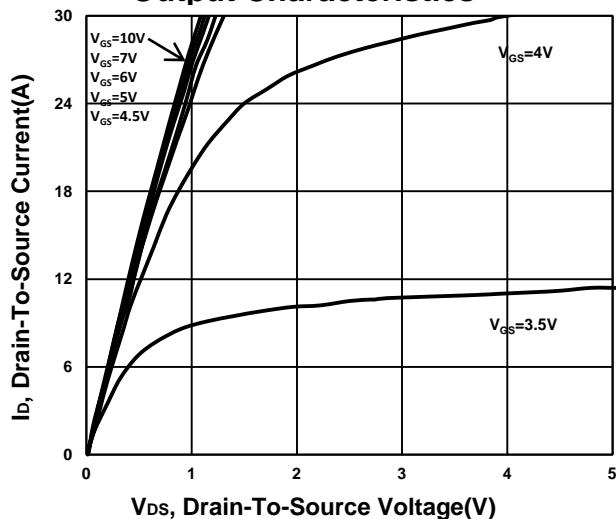
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

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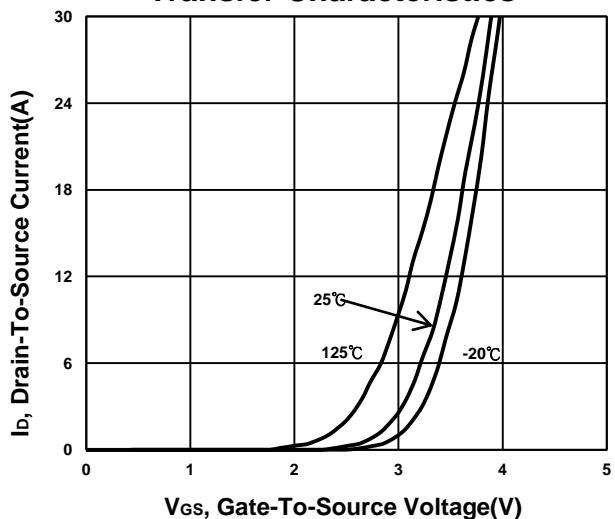
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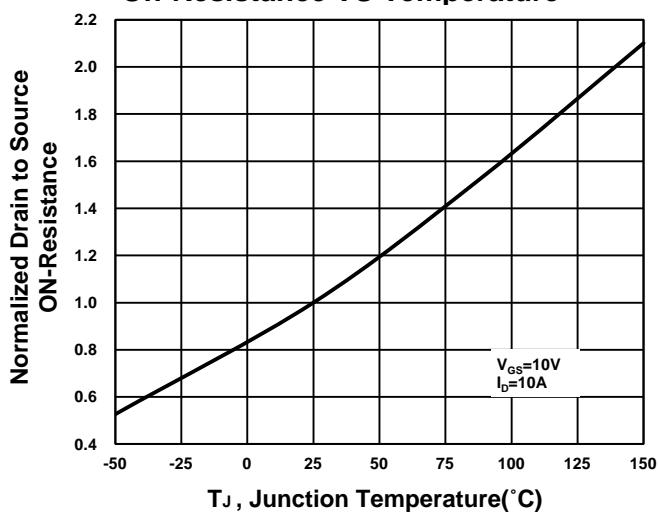
Output Characteristics



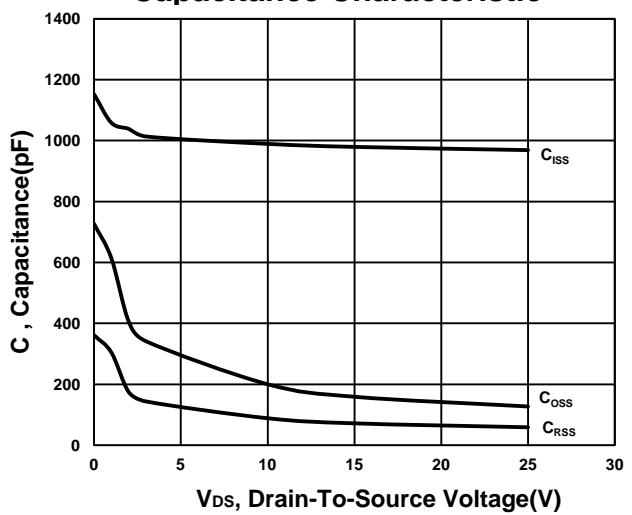
Transfer Characteristics



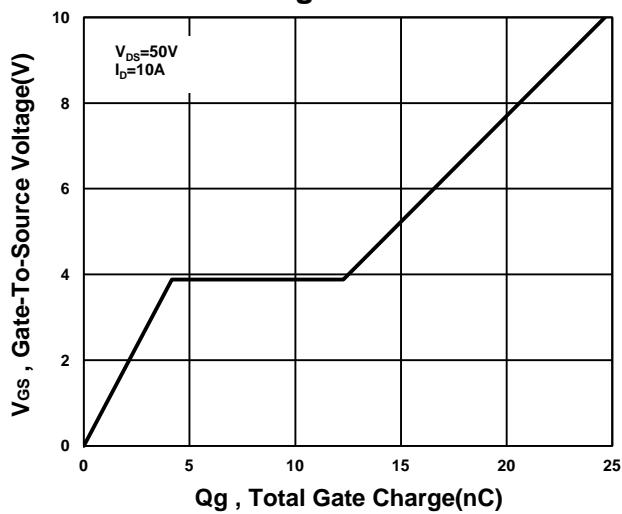
On-Resistance VS Temperature



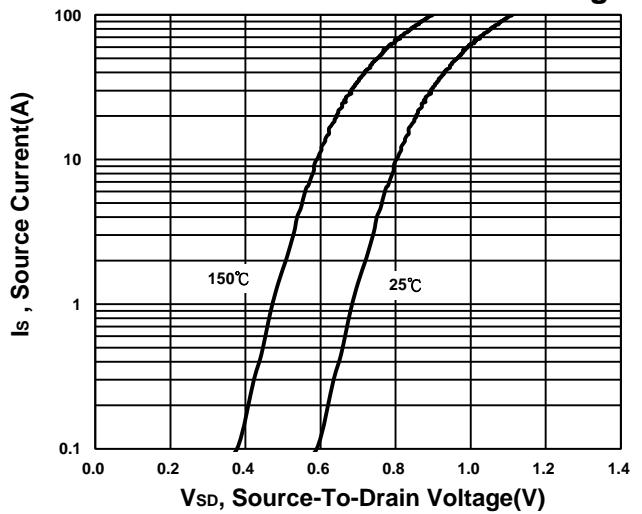
Capacitance Characteristic



Gate charge Characteristics



Source-Drain Diode Forward Voltage



NIKO-SEM

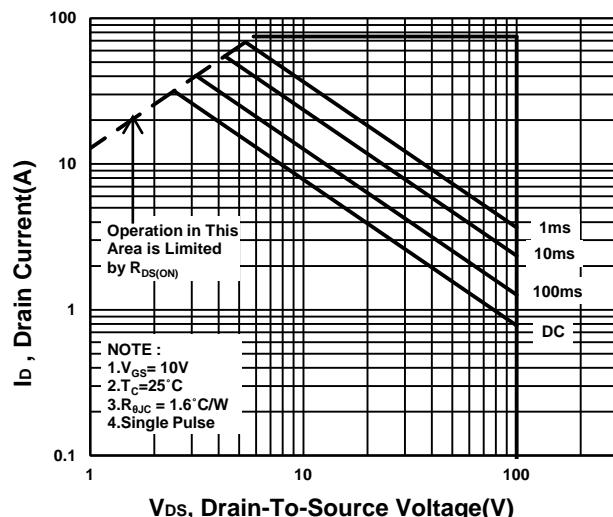
**N-Channel Enhancement Mode
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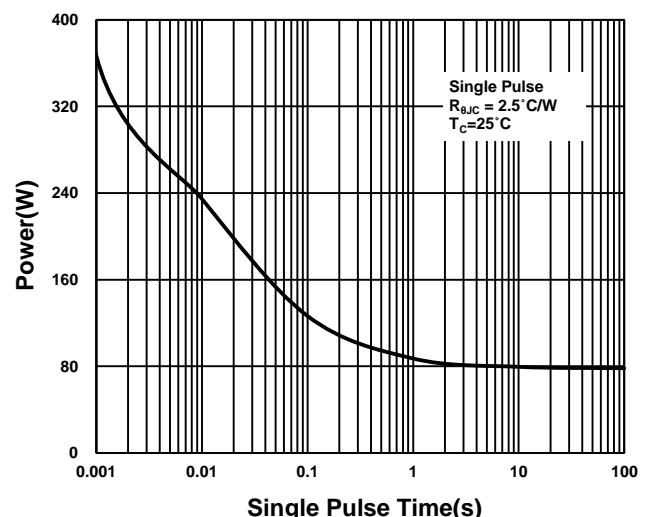
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

