

# P40B6SLK

# Power MOSFETs 60V, 40A, N-channel

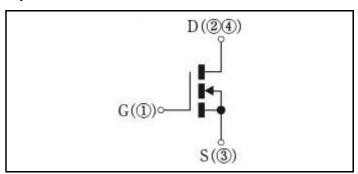
### **Feature**

- N-channel
- SMD
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- · Based on AEC-Q101
- Halogen free Pb free terminal
- RoHS:Yes

## **OUTLINE**



# **Equivalent circuit**



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

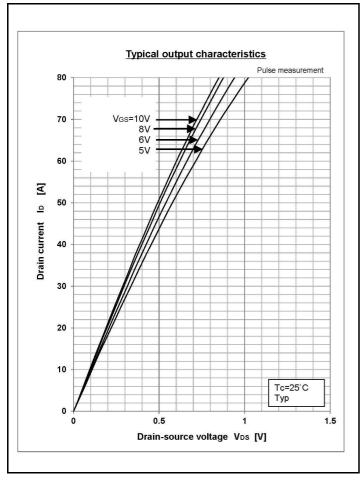
Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel tempertature	Tch		-55 to 175	°C
Drain-source voltage	$V_{DSS}$		60	V
Gate-source voltage	$V_{GSS}$		±20	V
Continuous drain current(DC)	I <sub>D</sub>		40	А
Continuous drain current(Peak)	I <sub>DP</sub>	Pulse width 10µs, duty=1/100	120	А
Continuous source current(DC)	Is		40	Α
Total power dissipation	P <sub>T</sub>	With heatsink*	46	W
Total power dissipation	P <sub>T</sub>	Measured on the 1 inch² glass epoxy substrate pattern area: 586.81mm²	3.3	W
Total power dissipation	P <sub>T</sub>	Measured on the 1 inch <sup>2</sup> glass epoxy substrate pattern area: 102.19mm <sup>2</sup>	2	W
Single avalanche current	I <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	23	Α
Single avalanche energy	E <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	54	mJ

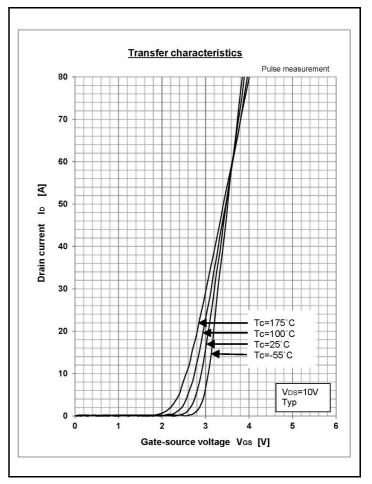
# **Electrical Characteristics** (unless otherwise specified : Tc=25°C)

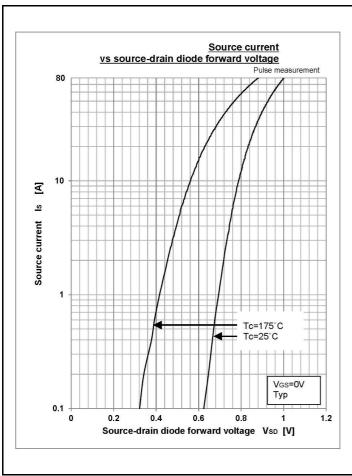
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	Unit
Drain-Source breakdown voltage	V <sub>(BR)DSS</sub>	ID=1mA, VGS=0V	60			V
Zero gate voltage drain current	I <sub>DSS</sub>	VDS=60V, VGS=0V			1	μΑ
Gate-source leakage current	I <sub>GSS</sub>	VGS=±20V, VDS=0V			±0.1	μΑ
Forward transconductance	g <sub>fs</sub>	ID=20A, VDS=10V	9			S
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=20A, VGS=10V		0.0095	0.012	Ω
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=20A, VGS=4.5V		0.012	0.016	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	$V_{SD}$	IS=40A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case, with heatsink *			3.2	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient Measured on the 1 inch <sup>2</sup> glass epoxy substrate pattern area: 586.81mm <sup>2</sup>			45	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient Measured on the 1 inch <sup>2</sup> glass epoxy substrate pattern area: 102.19mm <sup>2</sup>			75	°C/W
Total gate charge	Qg	VDD=48V, VGS=10V, ID=40A		43		nC
Gate to source charge	Qgs	VDD=48V, VGS=10V, ID=40A		8.5		nC
Gate to drain charge	Qgd	VDD=48V, VGS=10V, ID=40A		10		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		2050		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		110		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		216		pF
Turn-on delay time	td(on)	ID=20A, RL=1.5 $\Omega$ , VDD=30V, Rg=0 $\Omega$ , VGS(+)=10V, VGS(-)=0V		6		ns
Rise time	tr	ID=20A, RL=1.5 $\Omega$ , VDD=30V, Rg=0 $\Omega$ , VGS(+)=10V, VGS(-)=0V		18		ns
Turn-off delay time	td(off)	ID=20A, RL=1.5 $\Omega$ , VDD=30V, Rg=0 $\Omega$ , VGS(+)=10V, VGS(-)=0V		29		ns
Fall time	tf	ID=20A, RL=1.5 $\Omega$ , VDD=30V, Rg=0 $\Omega$ , VGS(+)=10V, VGS(-)=0V		17		ns
Diode reverse recovery time	trr	IF=40A, VGS=0V, di/dt=100A/μs		42		ns
Diode reverse recovery charge	Qrr	IF=40A, VGS=0V, di/dt=100A/μs		51		nC

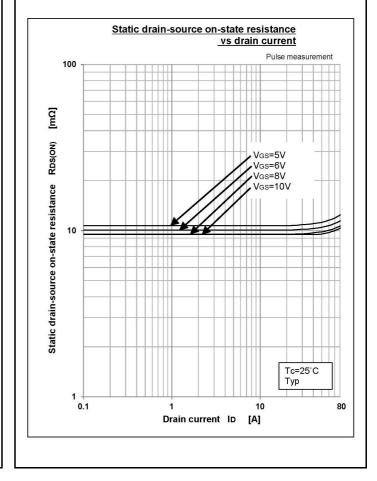
<sup>\* :</sup>See the original Specifications

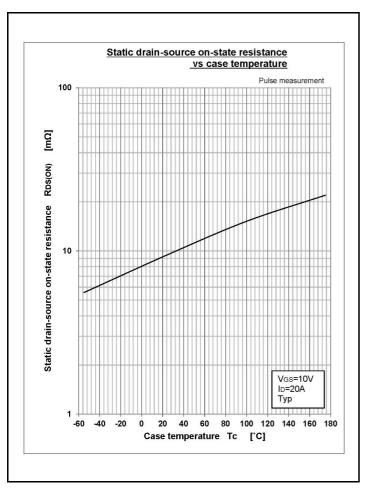
# **CHARACTERISTIC DIAGRAMS**

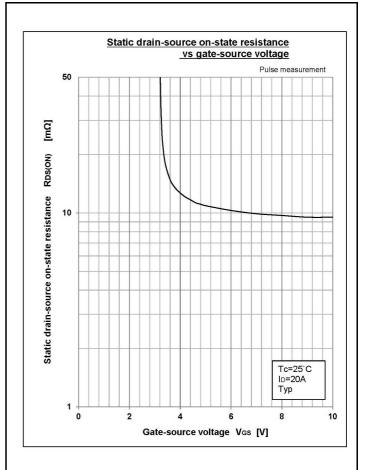


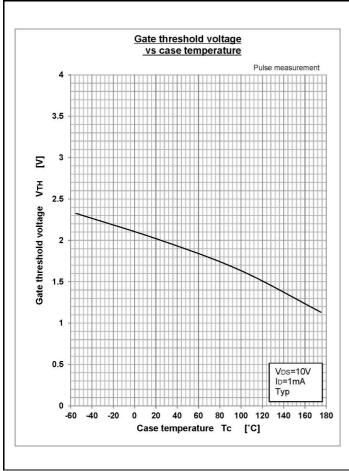


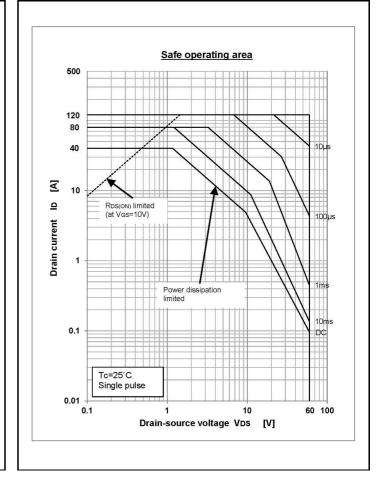


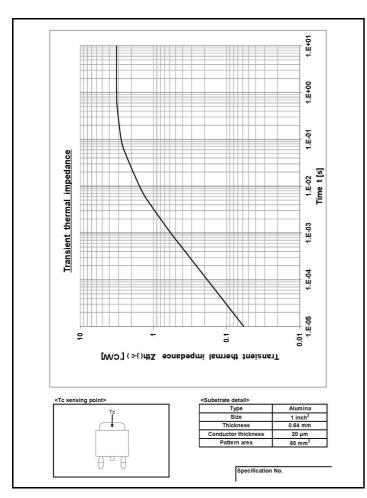


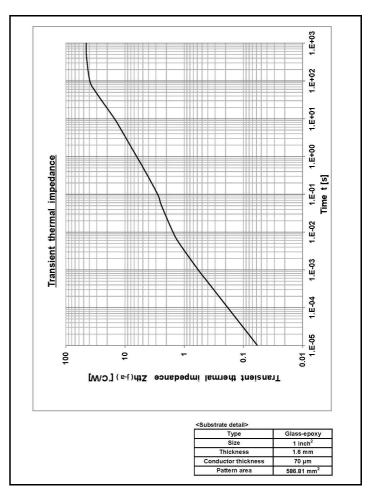


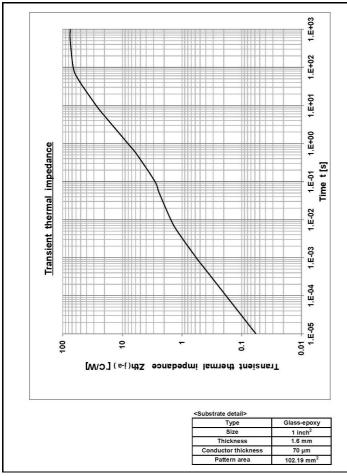


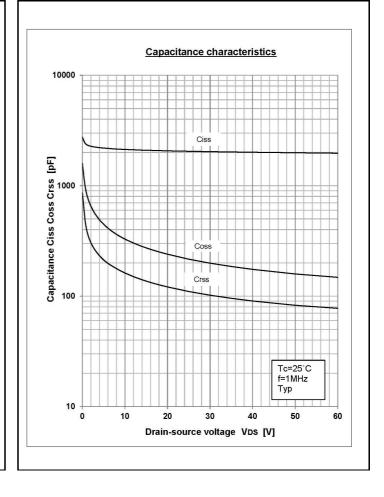


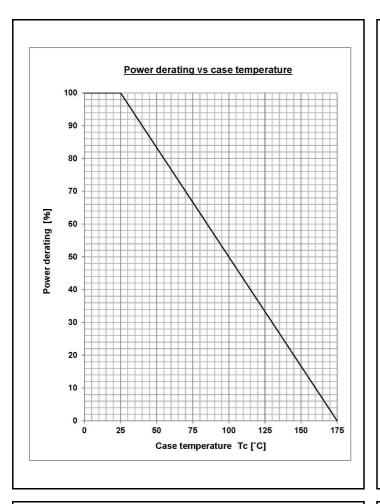


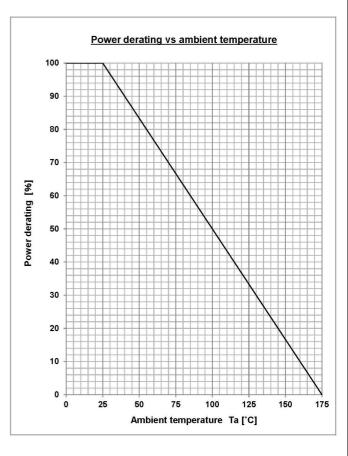


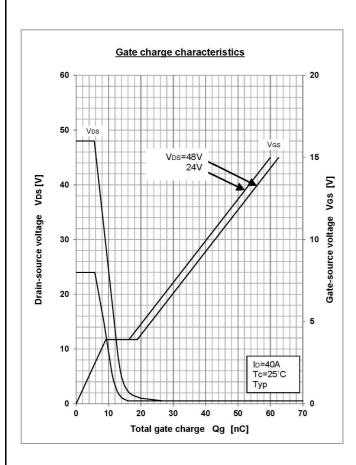


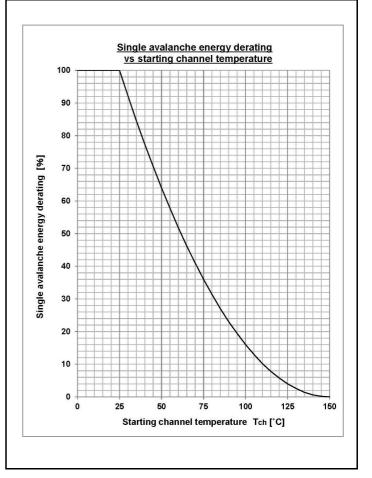






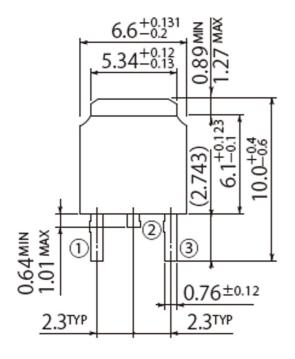


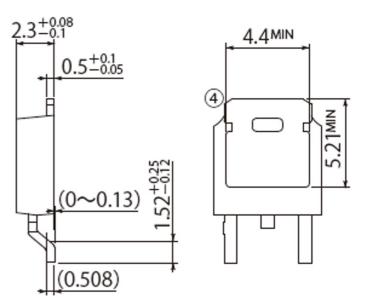


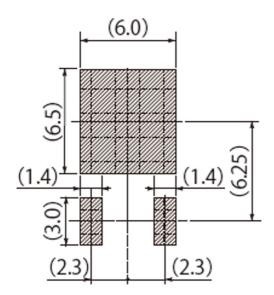


G2

JEDEC Code	TO-252AA		
JEITA Code	_		
House Name	FB		







Referential Soldering Pad

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