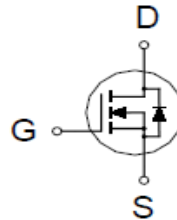
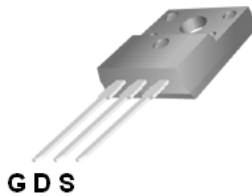


P45N03LTFG

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
25V	20m Ω @ $V_{GS} = 10V$	36A



TO-220F

ABSOLUTE MAXIMUM RATINGS ($T_C = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS	
Drain-Source Voltage	V_{DS}	25	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current	I_D	$T_C = 25\text{ }^\circ\text{C}$	36	A
		$T_C = 100\text{ }^\circ\text{C}$	22	
Pulsed Drain Current ¹	I_{DM}	140		
Avalanche Current	I_{AS}	53		
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	140	mJ
Power Dissipation	P_D	$T_C = 25\text{ }^\circ\text{C}$	42	W
		$T_C = 100\text{ }^\circ\text{C}$	17	
Operating Junction & Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		70	

¹Pulse width limited by maximum junction temperature.

P45N03LTFG

N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_C = 25 °C, Unless Otherwise Noted)

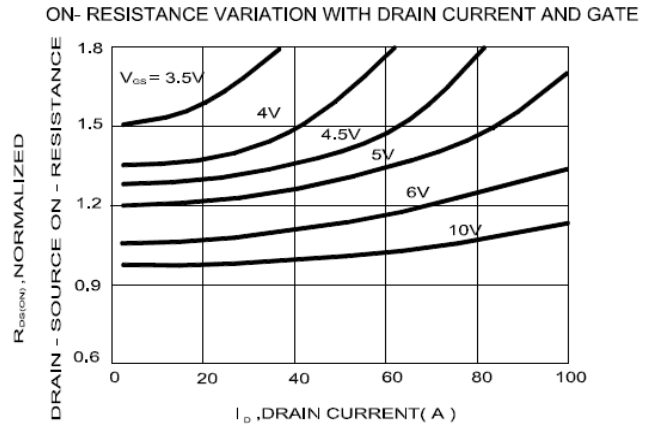
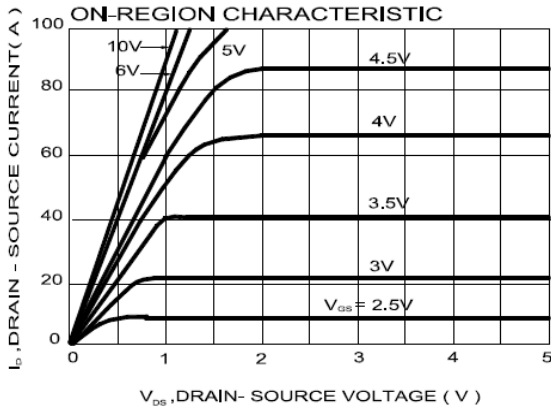
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	25			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.8	3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20V, V _{GS} = 0V			25	μA
		V _{DS} = 20V, V _{GS} = 0V, T _J = 125 °C			250	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	140			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 5V, I _D = 15A		25	30	mΩ
		V _{GS} = 10V, I _D = 15A		15	20	
Forward Transconductance ¹	g _{fs}	V _{DS} = 15V, I _D = 30A		16		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		600	864	pF
Output Capacitance	C _{oss}			290	420	
Reverse Transfer Capacitance	C _{rss}			100	150	
Total Gate Charge ²	Q _g	V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = 10V, I _D = 20A		25	30	nC
Gate-Source Charge ²	Q _{gs}			2.9	3.77	
Gate-Drain Charge ²	Q _{gd}			7	8.6	
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 15V, R _L = 1Ω, I _D ≅ 30A, V _{GS} = 10V, R _{GS} = 2.5Ω		7		nS
Rise Time ²	t _r			7		
Turn-Off Delay Time ²	t _{d(off)}			24		
Fall Time ²	t _f			6		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_C = 25 °C)						
Continuous Current	I _S					A
Forward Voltage ¹	V _{SD}	I _F = I _S , V _{GS} = 0V			1.3	V
Reverse Recovery Time	t _{rr}			37		nS
Reverse Recovery Charge	Q _{rr}			0.043		μC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

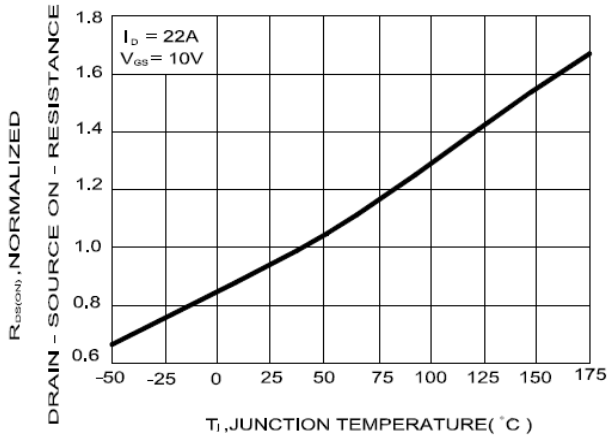
²Independent of operating temperature.

P45N03LTFG

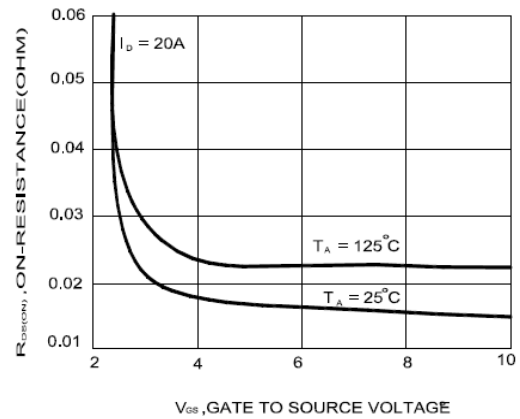
N-Channel Enhancement Mode MOSFET



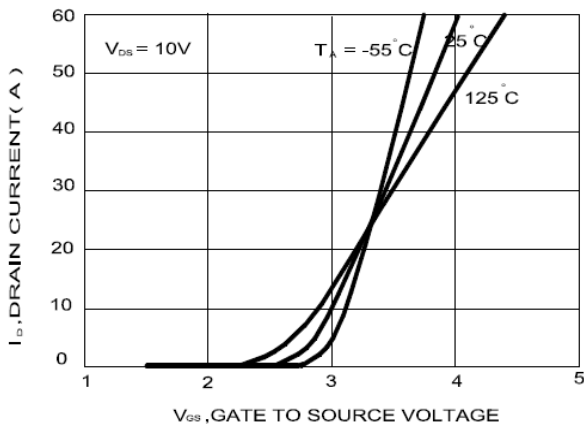
ON-RESISTANCE VARIATION WITH TEMPERATURE



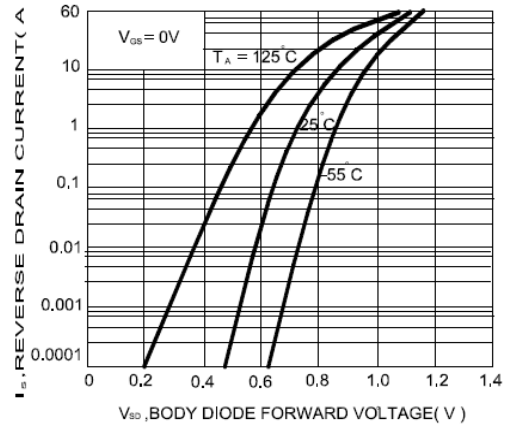
ON-RESISTANCE VARIATION WITH GATE-TO-SOURCE VOLTAGE



TRANSFER CHARACTERISTICS

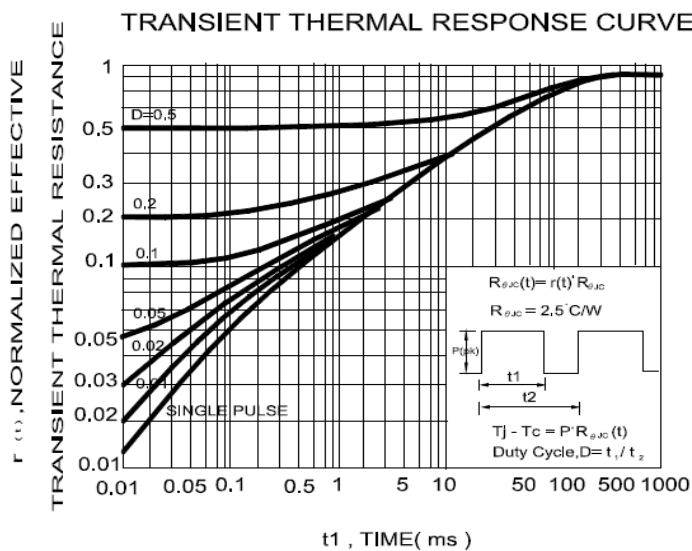
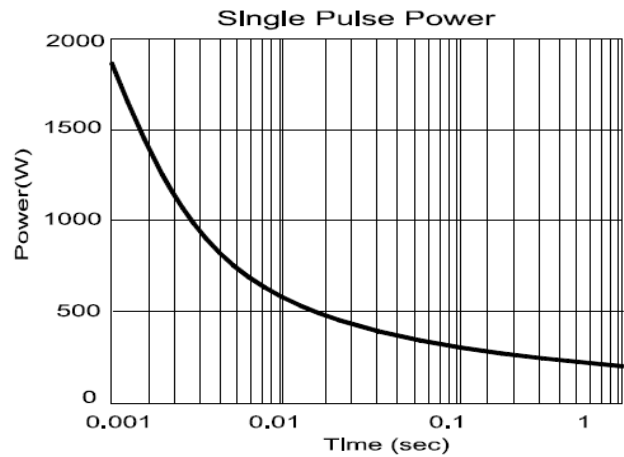
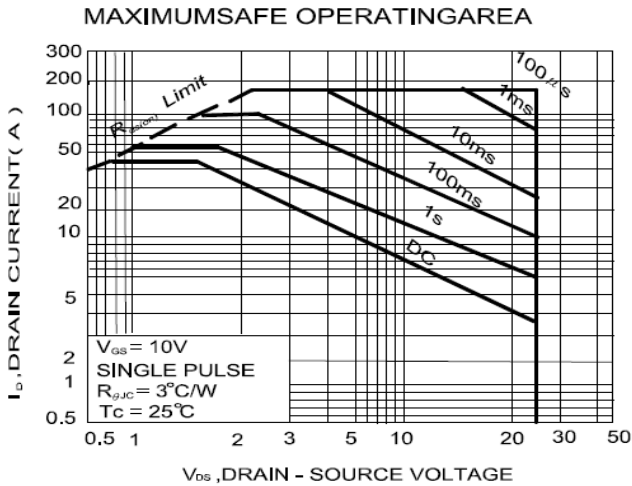
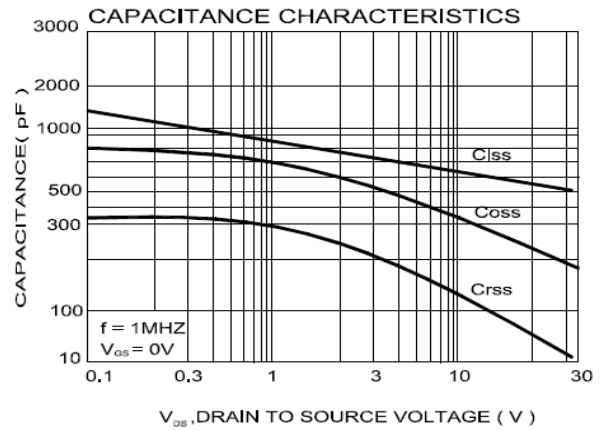
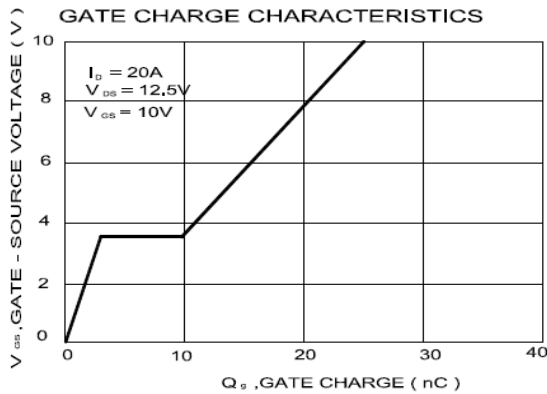


BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE



P45N03LTFG

N-Channel Enhancement Mode MOSFET



P45N03LTFG

N-Channel Enhancement Mode MOSFET

Package Dimension

TO-220F (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.93	e	2.05	2.55	3.05
A1	2.34		3.1	F	27.45		30.6
B	17.77		20.3	G	7.72		9.3
b	0.6		1.05	H	6.1		7.1
b1	0.9	1.23	1.62	L	12.5		14.5
b2	0.6		1.9	L1	1.97		3.8
c	0.4		1.0	P	2.98		3.4
D	14.7		16.4	Q	2.1		2.96
D1	6.4		7.5	q	3.0		3.8
E	9.7		10.4				

