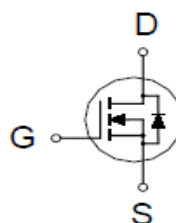
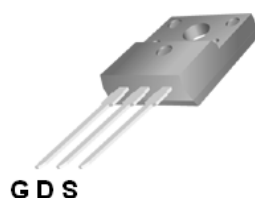


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N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	50mΩ @ $V_{GS} = 10V$	18A



TO-220F

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current	$T_c = 25\text{ }^{\circ}\text{C}$	I_D	18	A
	$T_c = 100\text{ }^{\circ}\text{C}$		11	
Pulsed Drain Current ¹		I_{DM}	80	
Avalanche Current		I_{AS}	20	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	20	mJ
Power Dissipation	$T_c = 25\text{ }^{\circ}\text{C}$	P_D	41	W
	$T_c = 100\text{ }^{\circ}\text{C}$		16	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	150			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	2	3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 120V, V _{GS} = 0V			1	μA
		V _{DS} = 100V, V _{GS} = 0V, T _J = 125 ° C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 4A		44	90	mΩ
		V _{GS} = 10V , I _D = 18A		37	50	
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 18A		30		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		4607		pF
Output Capacitance	C _{oss}			182		
Reverse Transfer Capacitance	C _{rss}			130		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		2.5		Ω
Total Gate Charge ²	Q _g (V _{GS} =10V)	V _{DS} = 75V,I _D = 18A		88		nC
	Q _g (V _{GS} =4.5V)			43		
Gate-Source Charge ²	Q _{gs}			15		
Gate-Drain Charge ²	Q _{gd}			25		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 75V , I _D ≅ 18A, V _{GS} = 10V, R _{GS} =6Ω		40		nS
Rise Time ²	t _r			230		
Turn-Off Delay Time ²	t _{d(off)}			180		
Fall Time ²	t _f			216		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				18	A
Forward Voltage ¹	V _{SD}	I _F = 18A, V _{GS} = 0V			1.4	V
Reverse Recovery Time	t _{rr}	I _F =18A,dI _F /dt = 100A / μS		71		nS
Reverse Recovery Charge	Q _{rr}			223		nC

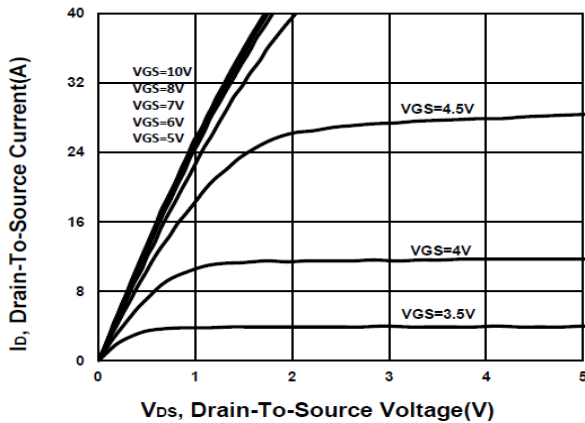
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

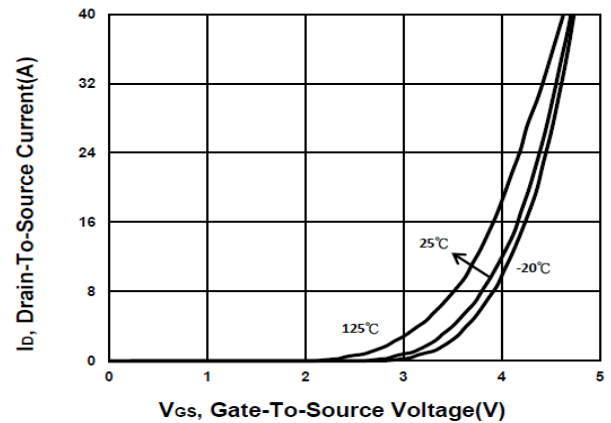
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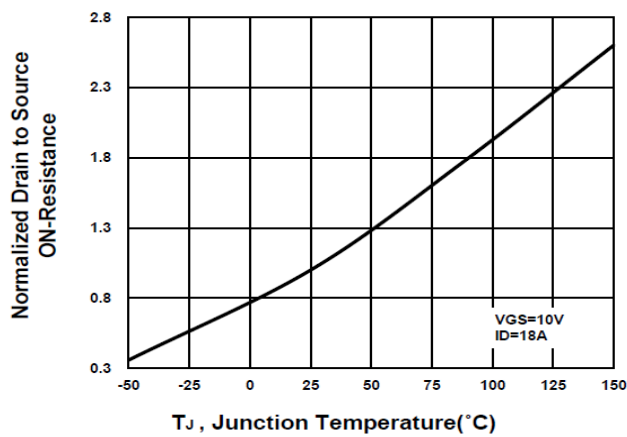
Output Characteristics



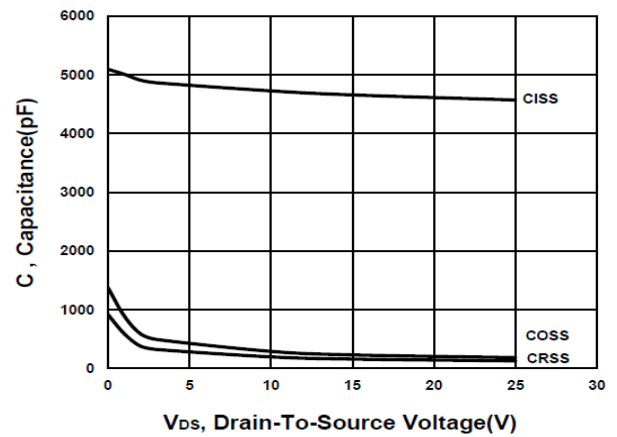
Transfer Characteristics



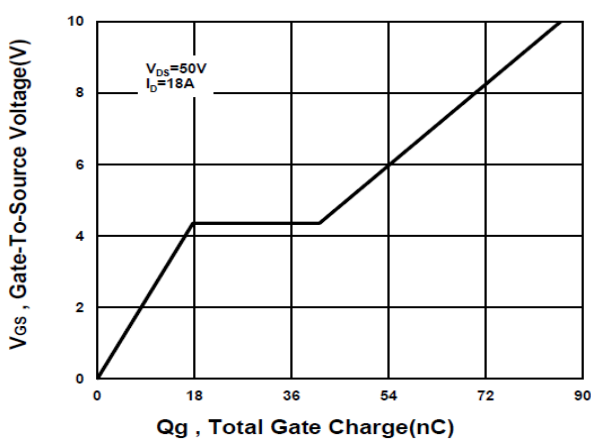
On-Resistance VS Temperature



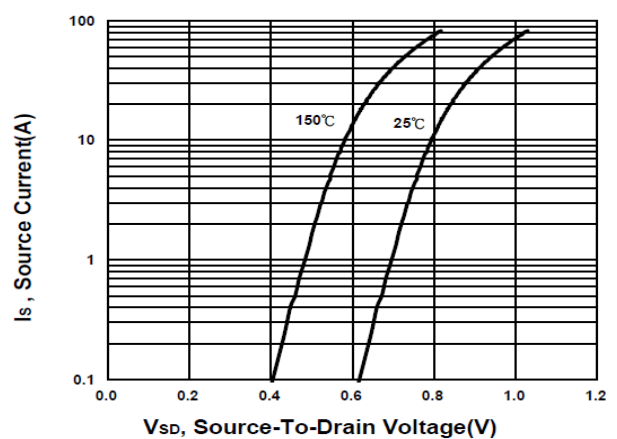
Capacitance Characteristic



Gate charge Characteristics



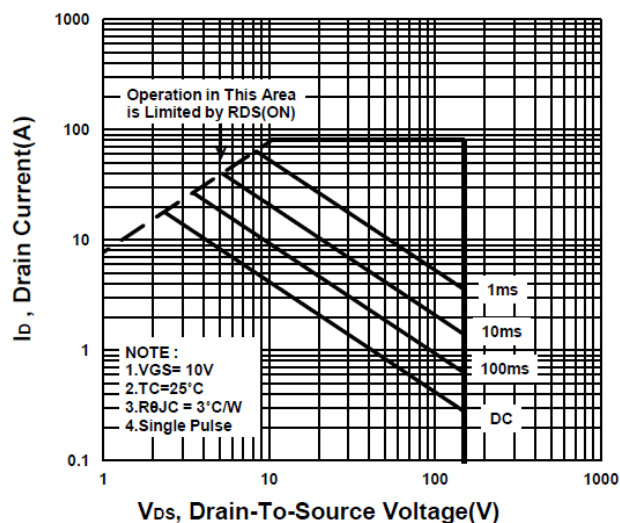
Source-Drain Diode Forward Voltage



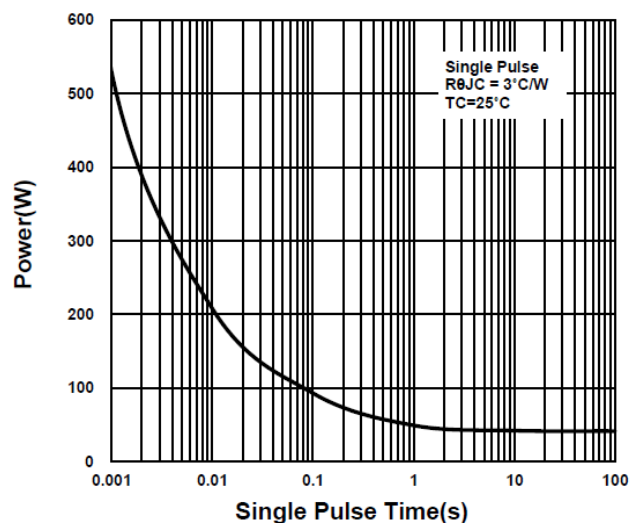
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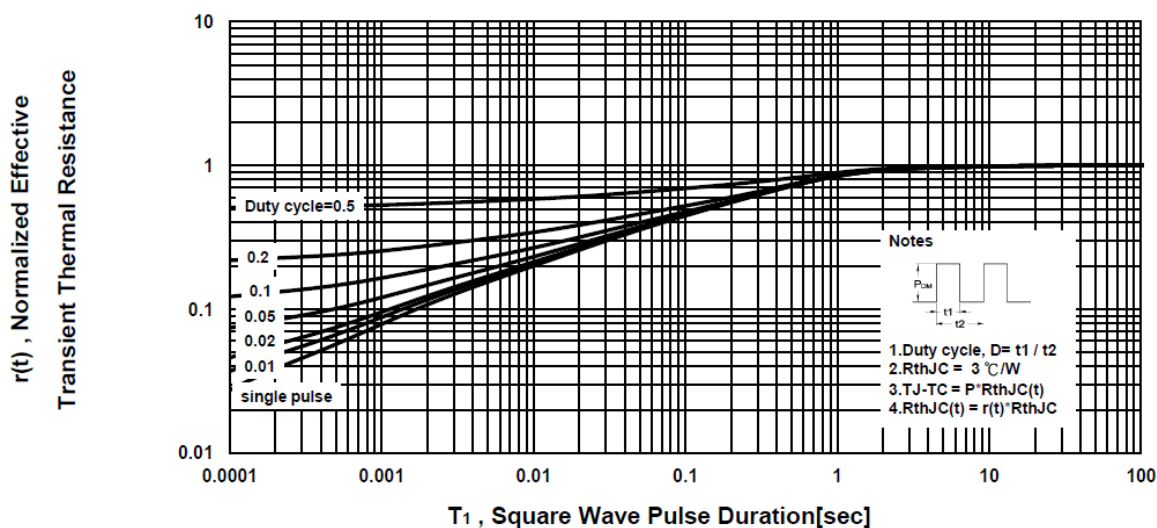
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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Package Dimension

TO-220F (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.93	e	2.05	2.55	3.05
A1	2.34		3.1	F	27.45		30.6
B	17.77		20.3	G	7.72		9.3
b	0.6		1.05	H	6.1		7.1
b1	0.9	1.23	1.62	L	12.5		14.5
b2	0.6		1.9	L1	1.97		3.8
c	0.4		1.0	P	2.98		3.4
D	14.7		16.4	Q	2.1		2.96
D1	6.4		7.5	q	3.0		3.8
E	9.7		10.4				

