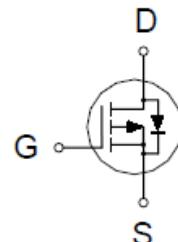
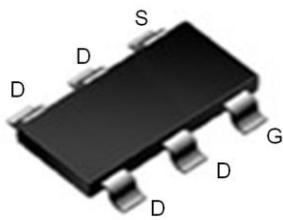


P5102FM6

P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-20V	51mΩ @ $V_{GS} = -4.5V$	-4.2A



SOT-23-6

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	± 8	
Continuous Drain Current	$T_A = 25^\circ C$	I_D	-4.2	A
	$T_A = 70^\circ C$		-3.3	
Pulsed Drain Current ¹		I_{DM}	-21	
Avalanche Current		I_{AS}	-21	
Avalanche Energy	$L=0.1mH$	E_{AS}	22	mJ
Power Dissipation	$T_A = 25^\circ C$	P_D	1.4	W
	$T_A = 70^\circ C$		0.9	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		85	°C / W

¹Limited by maximum junction temperature.

P5102FM6

P-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

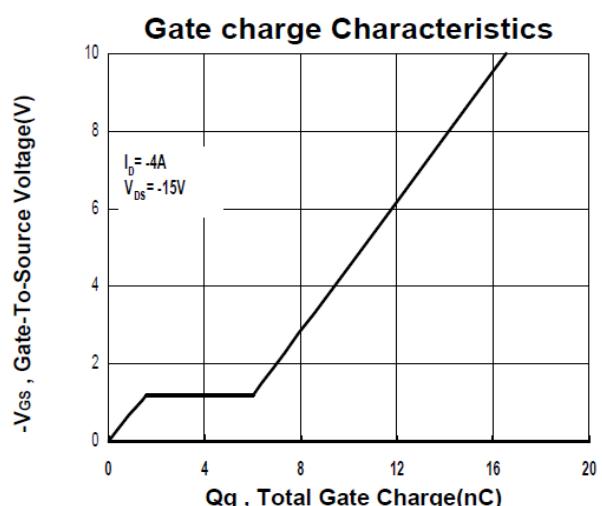
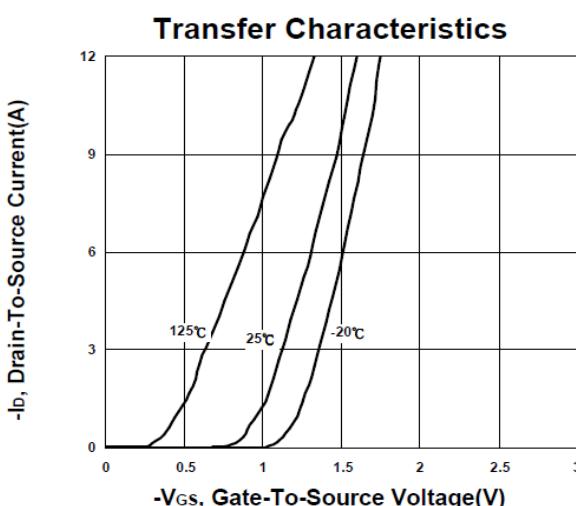
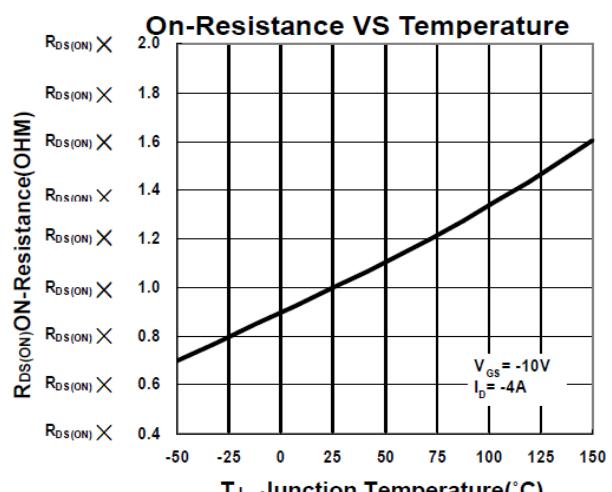
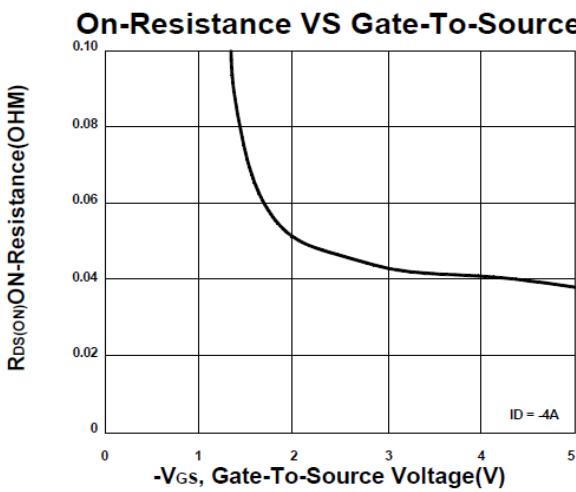
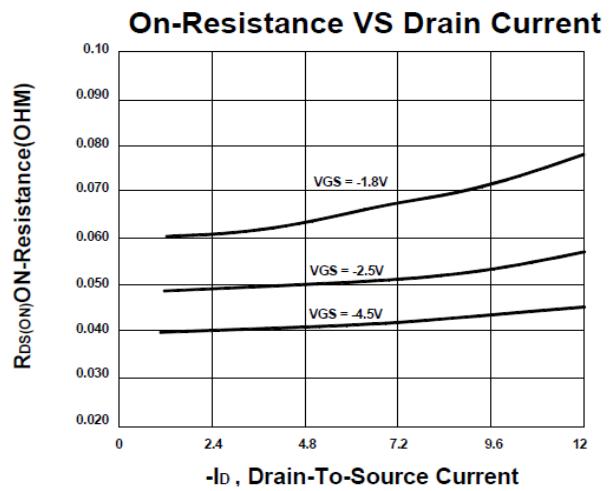
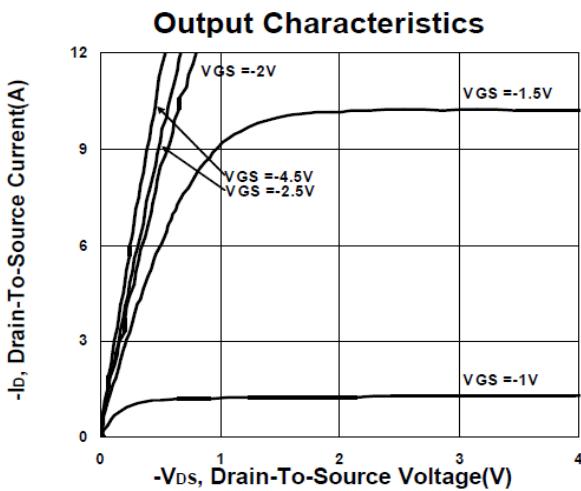
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-20			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.45	-0.6	-0.9	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 8\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -16\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
		$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 70^\circ\text{C}$			-10	
On-State Drain Current ¹	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}} = -5\text{V}, V_{\text{GS}} = -4.5\text{V}$	-21			A
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = -1.8\text{V}, I_D = -2\text{A}$		60	71	$\text{m}\Omega$
		$V_{\text{GS}} = -2.5\text{V}, I_D = -4\text{A}$		48	61	
		$V_{\text{GS}} = -4.5\text{V}, I_D = -4\text{A}$		40	51	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = -5\text{V}, I_D = -4\text{A}$		17		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -10\text{V}, f = 1\text{MHz}$		1200		pF
Output Capacitance	C_{oss}			187		
Reverse Transfer Capacitance	C_{rss}			119		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		8.2		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 0.5V_{(\text{BR})\text{DSS}}, V_{\text{GS}} = -4.5\text{V}, I_D = -4\text{A}$		16.7		nC
Gate-Source Charge ²	Q_{gs}			1.8		
Gate-Drain Charge ²	Q_{gd}			5.2		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = -10\text{V}$ $I_D \approx -4\text{A}, V_{\text{GS}} = -4.5\text{V}, R_{\text{GEN}} = 3.3\Omega$		20		nS
Rise Time ²	t_r			36		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			46		
Fall Time ²	t_f			62		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICE ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				-1	A
Forward Voltage ¹	V_{SD}	$I_F = -4\text{A}, V_{\text{GS}} = 0\text{V}$			-1.3	V
Reverse Recovery Time	t_{rr}	$I_F = -4\text{A}, dI_F/dt = 100\text{A} / \mu\text{s}$		30		nS
Reverse Recovery Charge	Q_{rr}			14		nC

¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

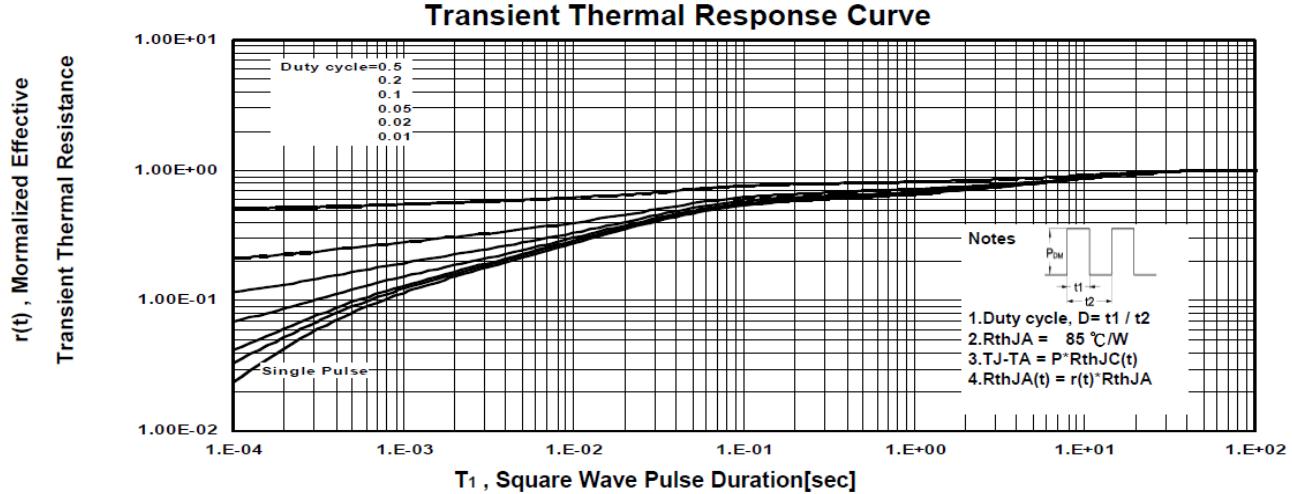
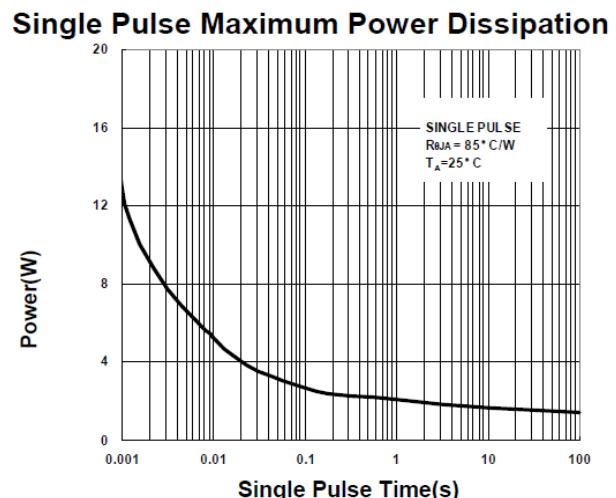
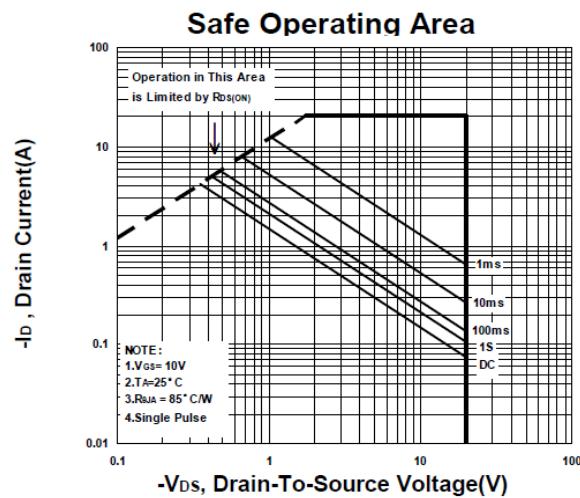
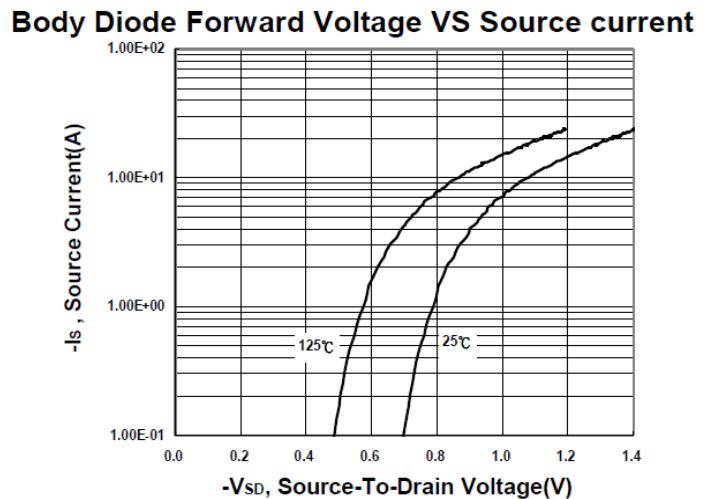
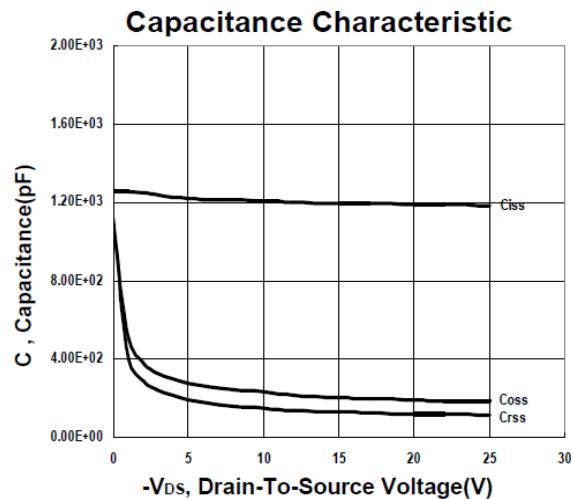
P5102FM6

P-Channel Enhancement Mode MOSFET



P5102FM6

P-Channel Enhancement Mode MOSFET



P5102FM6

P-Channel Enhancement Mode MOSFET

Package Dimension

SOT-23-6 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9	1.07	1.15	H	2.6	2.8	3.0
B	0.3	0.4	0.5	I	0		0.1
C	0.1	0.15	0.25				
D	2.8	2.9	3.1				
E	1.4	1.6	1.7				
F	1.8		2.0				
G	0.3	0.45	0.6				

