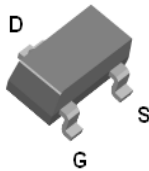


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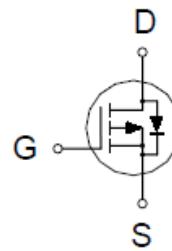
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PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-30V	51mΩ @ $V_{GS} = -10V$	-3.8A



SOT-23(S)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current	$T_A = 25\text{ °C}$	I_D	-3.8	A
	$T_A = 70\text{ °C}$		-3	
Pulsed Drain Current ¹		I_{DM}	-20	
Power Dissipation	$T_A = 25\text{ °C}$	P_D	1	W
	$T_A = 70\text{ °C}$		0.7	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		117	°C / W

¹Limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25\text{ °C}$.

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

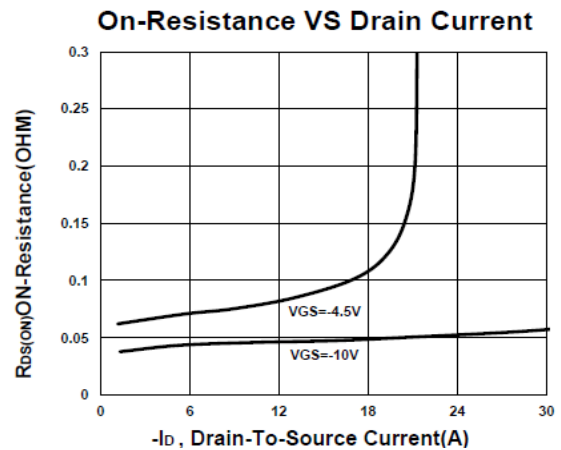
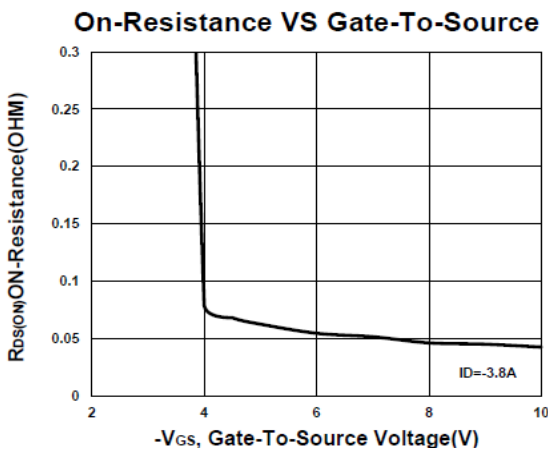
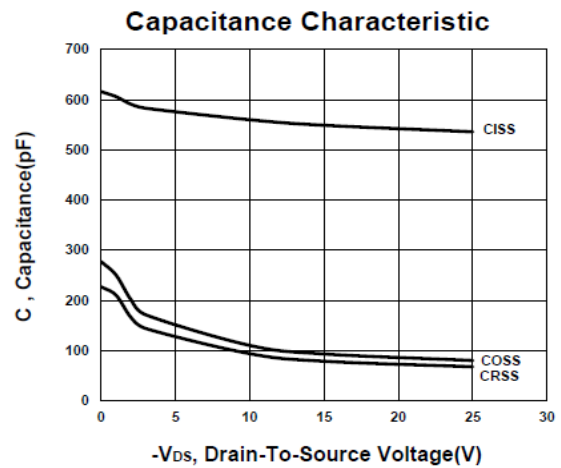
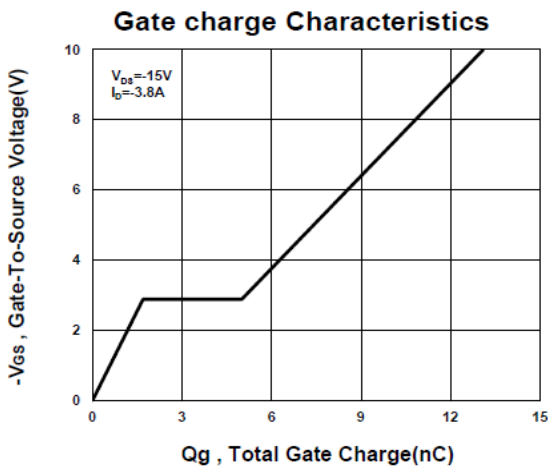
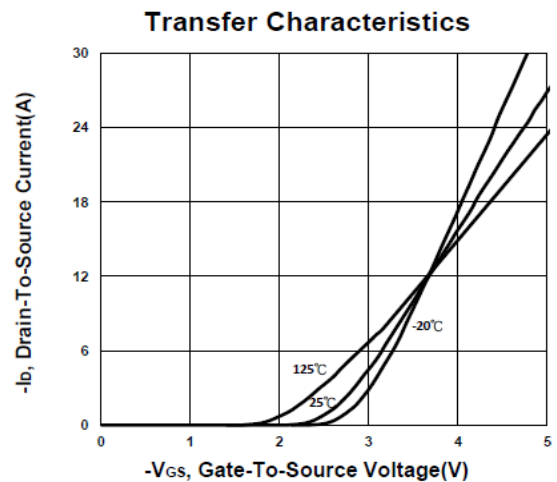
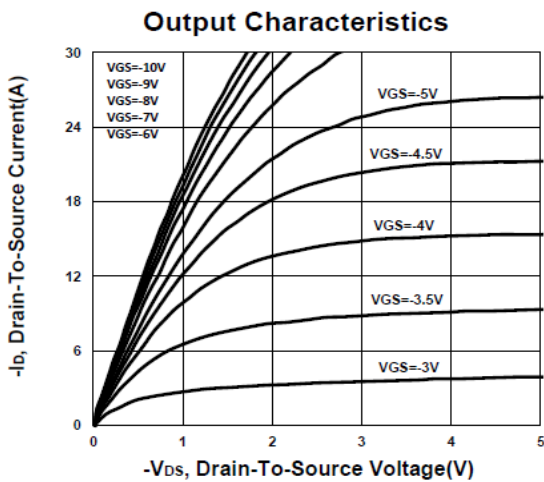
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT		
			MIN	TYP	MAX			
STATIC								
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30			V		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.8	-3	V		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -24V, V _{GS} = 0V			-1	μA		
		V _{DS} = -20V, V _{GS} = 0V, T _J = 70 °C			-10			
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = -4.5V, I _D = -3.5A		65	85	mΩ		
		V _{GS} = -10V, I _D = -3.8A		41	51			
Forward Transconductance ¹	g _{fs}	V _{DS} = -5V, I _D = -3.8A		7		S		
DYNAMIC								
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = -15V, f = 1MHz		562		pF		
Output Capacitance	C _{oss}			98				
Reverse Transfer Capacitance	C _{rss}			85				
Total Gate Charge ²	Q _{g(VGS=10V)}	V _{DS} = -15V, I _D = -3.8A		13.3		nC		
	Q _{g(VGS=4.5V)}			6.9				
Gate-Source Charge ²	Q _{gs}			1.9				
Gate-Drain Charge ²	Q _{gd}			3.5				
Turn-On Delay Time ²	t _{d(on)}		V _{DS} = -15V I _D ≅ -3.8A, V _{GS} = -10V, R _{GEN} = 6Ω		22			nS
Rise Time ²	t _r				23			
Turn-Off Delay Time ²	t _{d(off)}			110				
Fall Time ²	t _f			58				
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTIC (T_J = 25 °C)								
Continuous Current	I _S				-0.9	A		
Forward Voltage ¹	V _{SD}	I _F = -3.8A, V _{GS} = 0V			-1.1	V		
Reverse Recovery Time	t _{rr}	I _F = -3.8A, dI _F /dt = 100A /μS		13.2		nS		
Reverse Recovery Charge	Q _{rr}				6		nC	

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

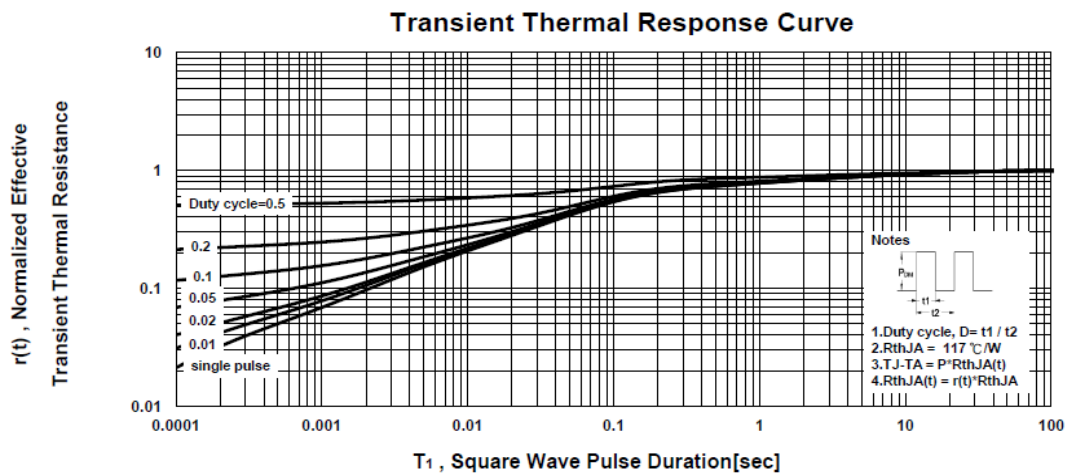
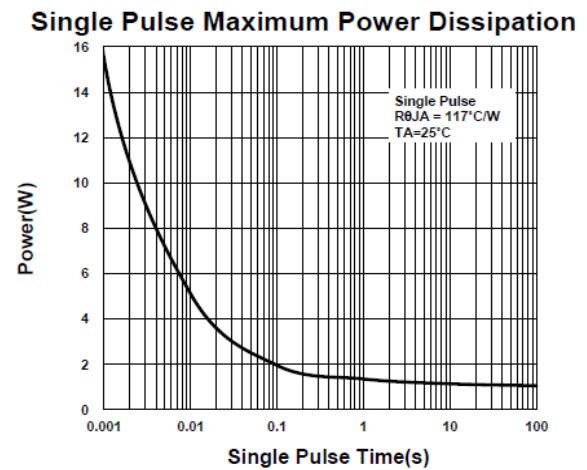
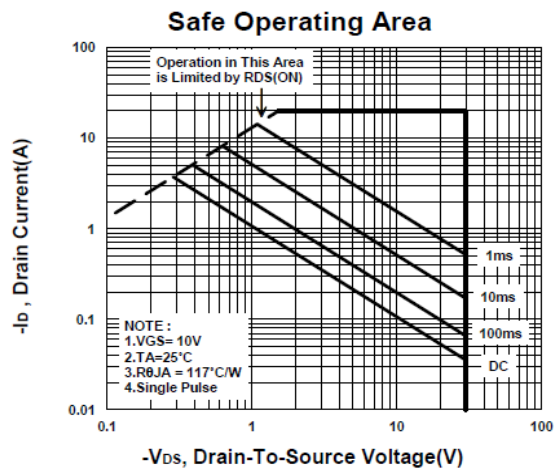
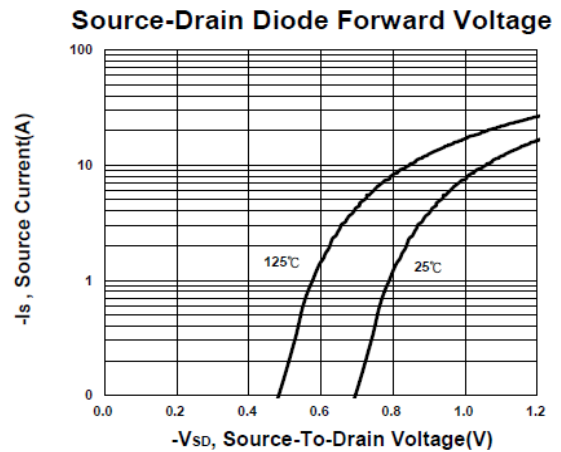
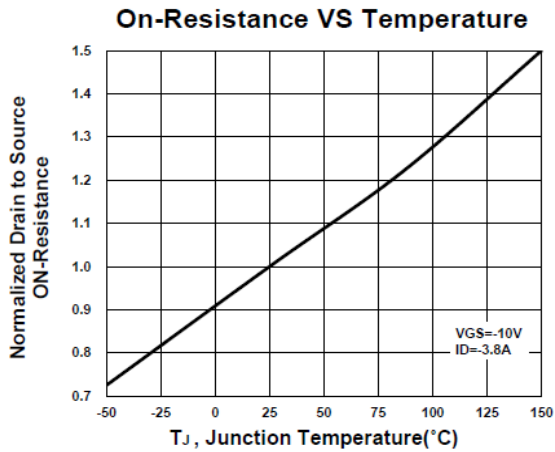
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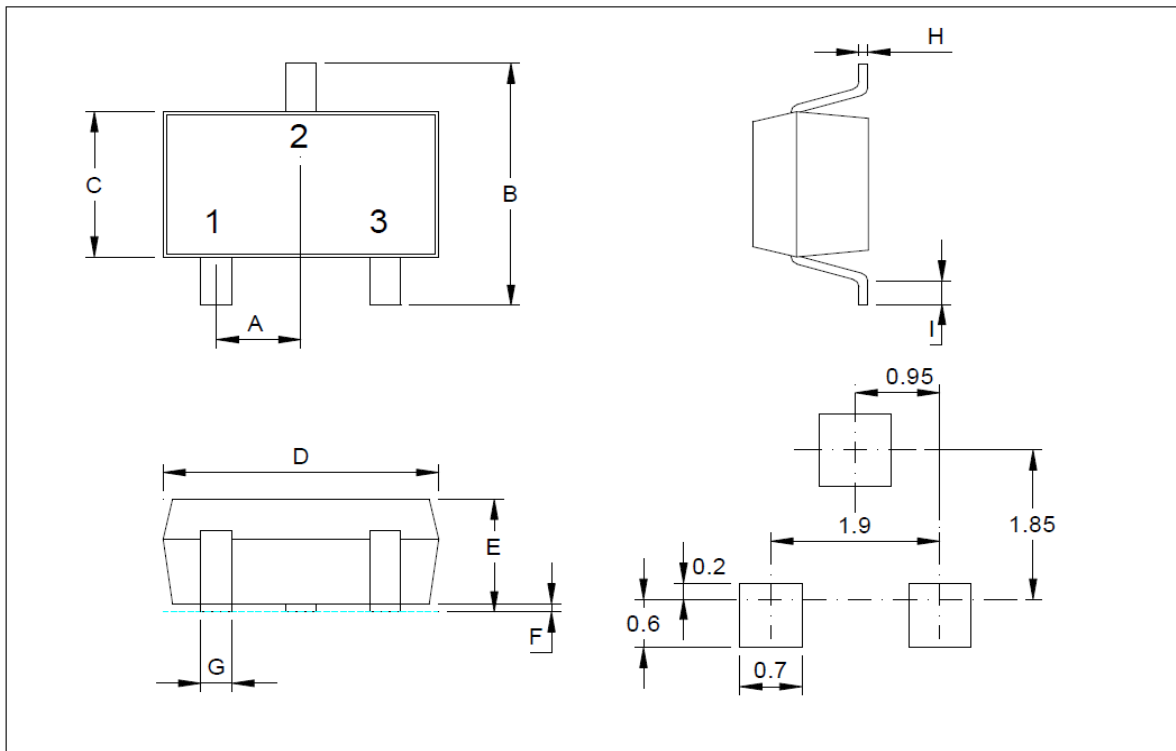
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Package Dimension

SOT-23 (S) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.25		2.85	I	0.15		0.6
C	1.2		1.4				
D	2.8		3.04				
E	0.89		1.2				
F	0		0.1				
G	0.3		0.5				



*因为各家封装模具不同而外观略有所差异，不影响电性及Layout。