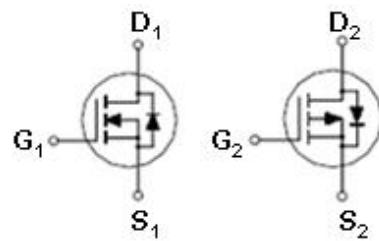


P6002OAG

N&P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D	Channel
20V	60mΩ @ $V_{GS} = 4.5V$	3.4A	N
-20V	115mΩ @ $V_{GS} = -4.5V$	-2.5A	P



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	CH.	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	N	20	V
		P	-20	
Gate-Source Voltage	V_{GS}	N	± 8	
		P	± 8	
Continuous Drain Current	I_D	N	3.4	A
		P	-2.5	
		N	2.7	
		P	-2	
Pulsed Drain Current ¹	I_{DM}	N	15	
		P	-15	
Avalanche Current ¹	I_{AS}	N	5.5	
		P	-12	
Avalanche Energy	E_{AS}	N	1.5	mJ
		P	7.4	
Power Dissipation	P_D	N	1.14	W
		P	1.14	
		N	0.72	
		P	0.72	
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150		°C

P6002OAG

N&P-Channel Enhancement Mode MOSFET

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$t \leq 10s$	$R_{\theta JA}$	$^{\circ}\text{C} / \text{W}$	110	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	Steady-State	$R_{\theta JA}$		150	
Junction-to-Lead	Steady-State	$R_{\theta JC}$		80	

¹Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	CH.	LIMITS			UNITS
				MIN	TYP	MAX	
STATIC							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	N	20			V
		$V_{GS} = 0V, I_D = -250\mu\text{A}$	P	-20			
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	N	0.4	0.75	1.3	
		$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	P	-0.4	-0.75	-1.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 8V$	N			± 100	nA
		$V_{DS} = 0V, V_{GS} = \pm 8V$	P			± 100	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16V, V_{GS} = 0V$	N			1	μA
		$V_{DS} = -16V, V_{GS} = 0V$	P			-1	
		$V_{DS} = 10V, V_{GS} = 0V, T_J = 55^{\circ}\text{C}$	N			10	
		$V_{DS} = -10V, V_{GS} = 0V, T_J = 55^{\circ}\text{C}$	P			-10	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = 5V, V_{GS} = 10V$	N	15			A
		$V_{DS} = -5V, V_{GS} = -10V$	P	-15			
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 1.8V, I_D = 2A$	N		90	140	$\text{m}\Omega$
		$V_{GS} = -1.8V, I_D = -1A$	P		171	300	
		$V_{GS} = 2.5V, I_D = 3A$	N		63	85	
		$V_{GS} = -2.5V, I_D = -2A$	P		118	180	
		$V_{GS} = 4.5V, I_D = 3.6A$	N		47	60	
		$V_{GS} = -4.5V, I_D = -3.1A$	P		85	115	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 3.6A$	N		6		S
		$V_{DS} = -5V, I_D = -3.1A$	P		11		

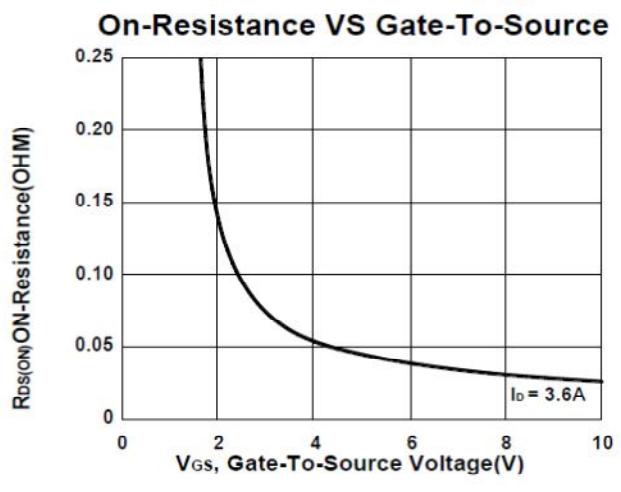
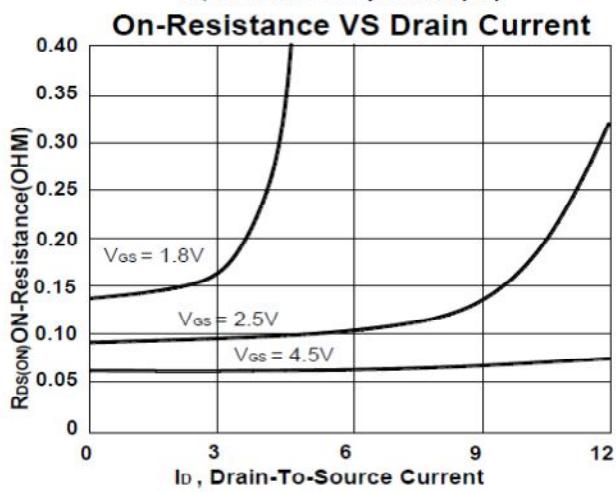
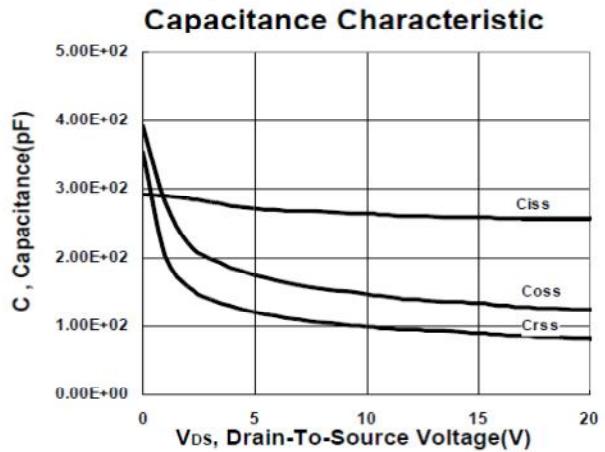
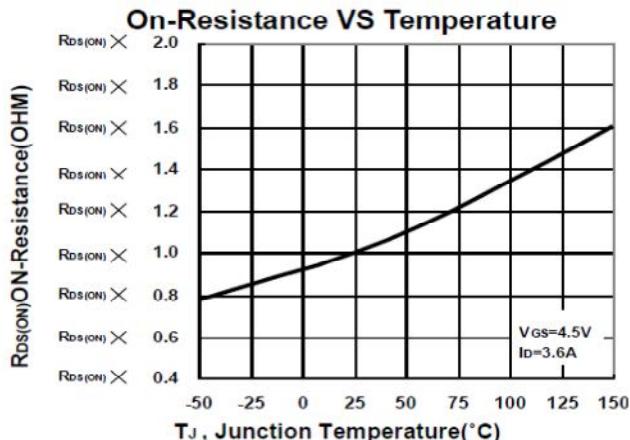
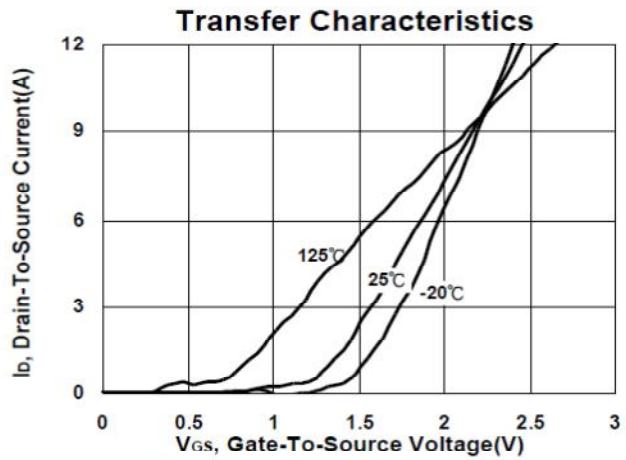
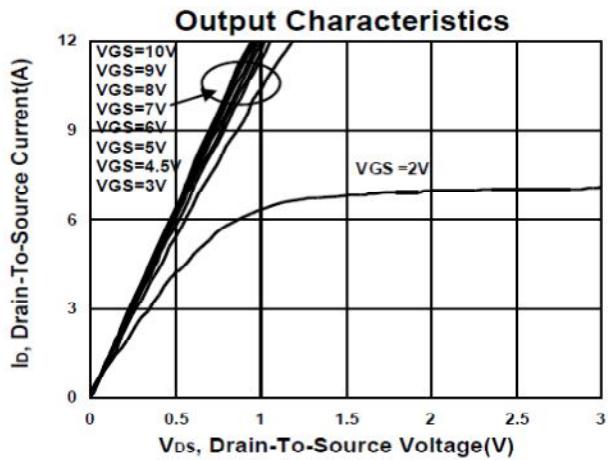
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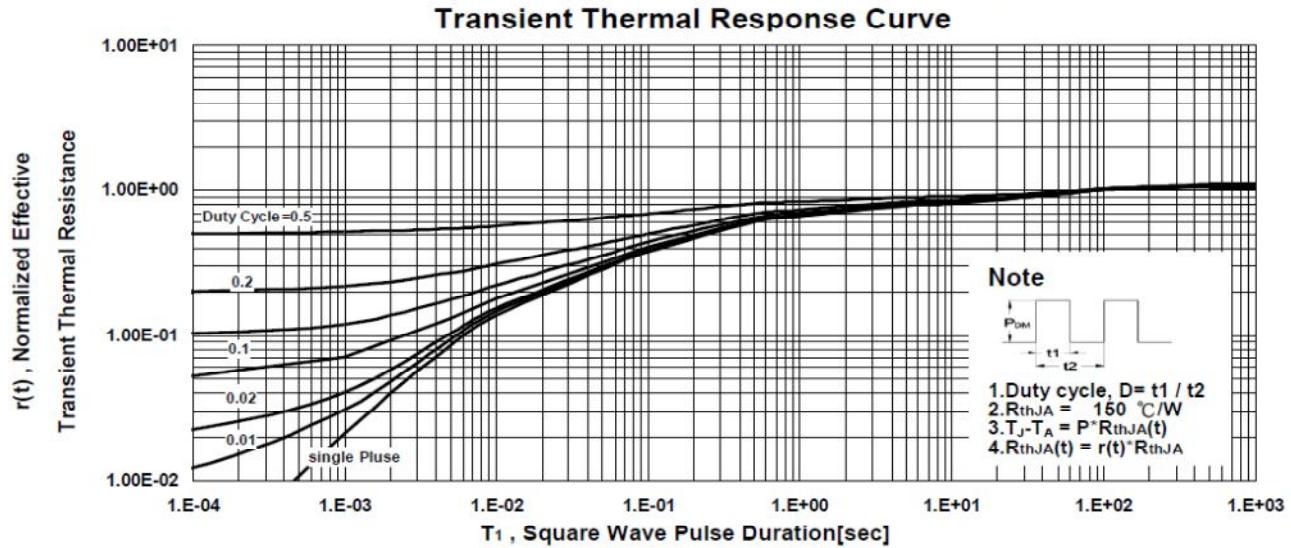
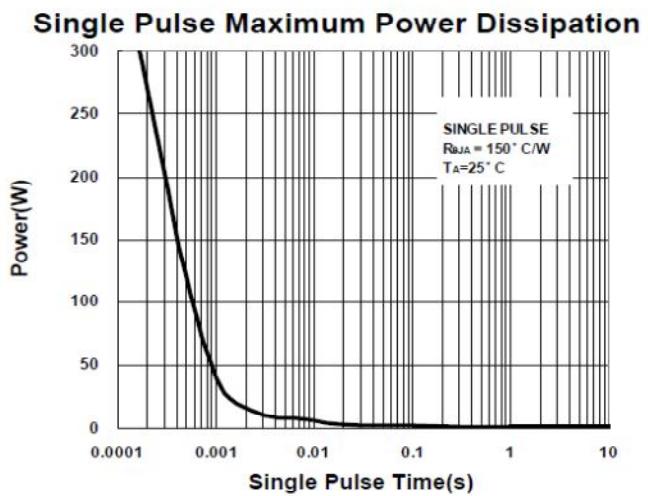
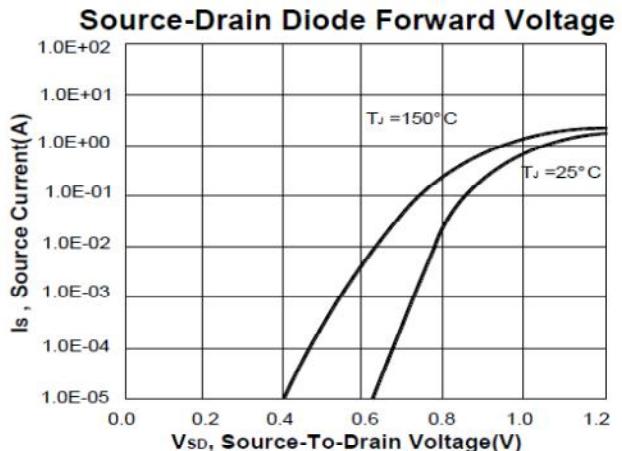
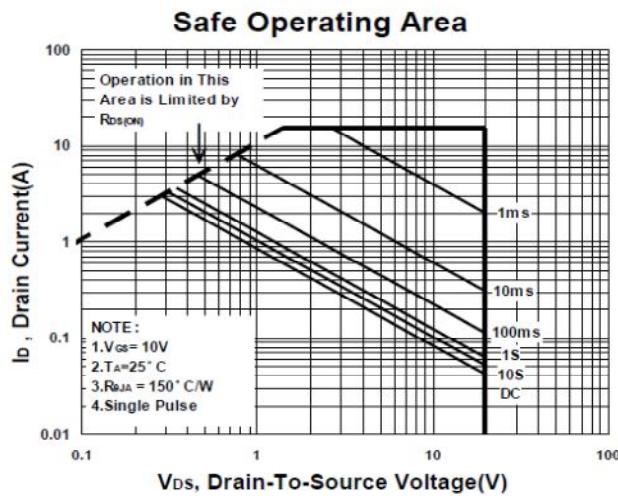
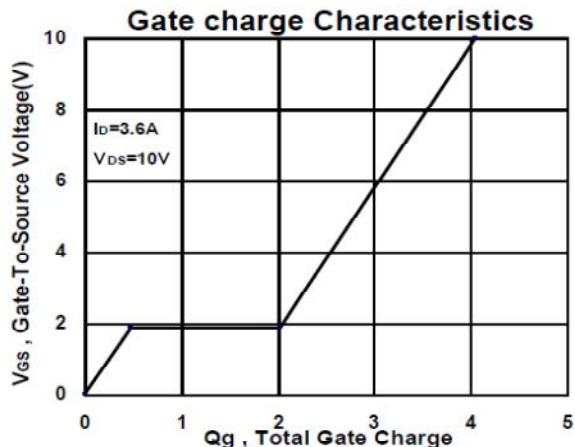
N&P-Channel Enhancement Mode MOSFET

DYNAMIC							
Input Capacitance	C_{iss}	N-Channel $V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$ P-Channel $V_{GS} = 0V, V_{DS} = -15V, f = 1MHz$	N		263		
Output Capacitance			P		415		
Reverse Transfer Capacitance	C_{rss}		N		128		
			P		126		
Gate Resistance	R_g		N		87		
Total Gate Charge ²			P		78		
Gate-Source Charge ²	Q_{gs}	N-Channel $V_{DS} = 0.5V_{(BR)DSS}, V_{GS} = 4.5V, I_D = 3.6A$ P-Channel $V_{DS} = 0.5V_{(BR)DSS}, V_{GS} = 4.5V, I_D = -3.1A$	N		1.65		
Gate-Drain Charge ²			P		6.1	Ω	
Turn-On Delay Time ²	$t_{d(on)}$		N		4		
Rise Time ²			P		4		
Turn-Off Delay Time ²	$t_{d(off)}$		N		0.5		
Fall Time ²			P		1		
			N		1.6		
			P		1.1		
			N		6		
			P		10		
			N		7		
			P		12		
			N		40		
			P		44		
			N		13		
			P		22		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)							
Continuous Current	I_S	$I_D \approx 3.6A, V_{GS} = 10V, R_{GEN} = 6\Omega$ P-Channel $V_{DS} = -15V, R_L = 1\Omega$ $I_D \approx -3.1A, V_{GS} = -10V, R_{GEN} = 6\Omega$	N		0.95		
			P		-0.95	A	
Forward Voltage ¹	V_{SD}	$I_F = 3.6A, V_{GS} = 0V$	N		1.2		
		$I_F = -3.1A, V_{GS} = 0V$	P		-1.2	V	
Reverse Recovery Time	t_{rr}	$I_F = 3.6A, dI_F/dt = 100A/\mu S$	N		14		
		$I_F = -3.1A, dI_F/dt = 100A/\mu S$	P		25	nS	
Reverse Recovery Charge	Q_{rr}		N		4		
			P		8	nC	

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

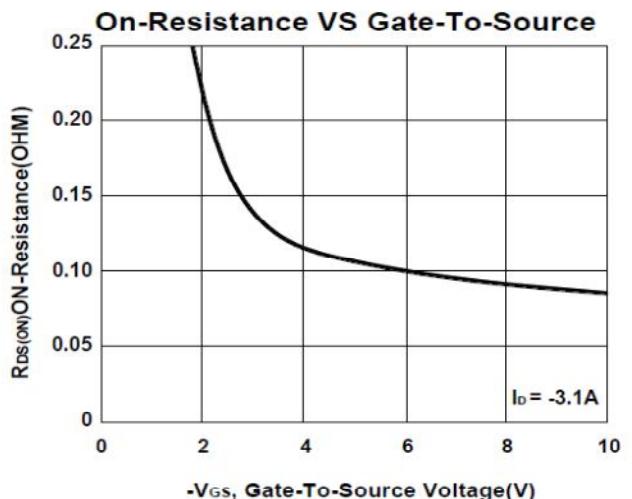
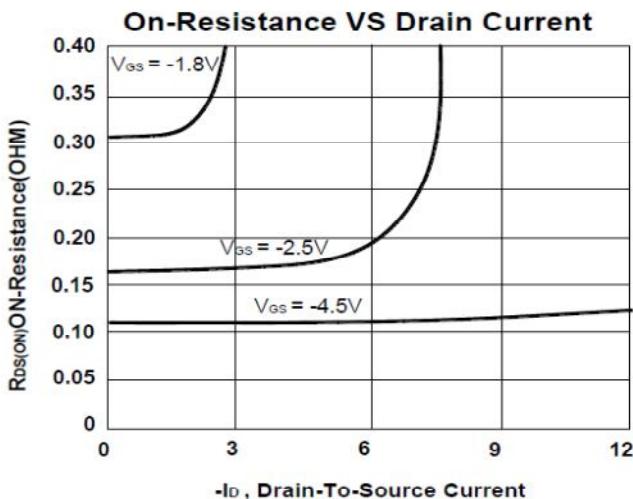
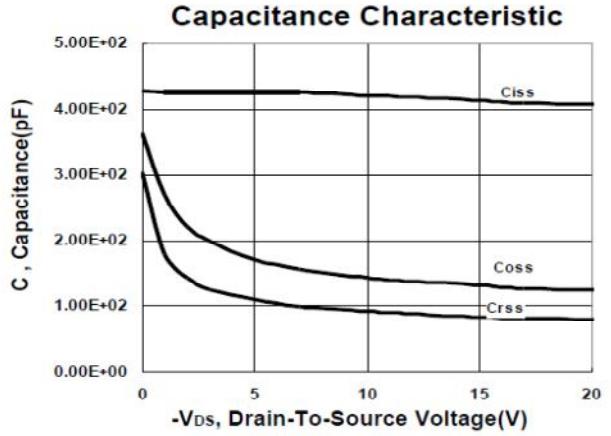
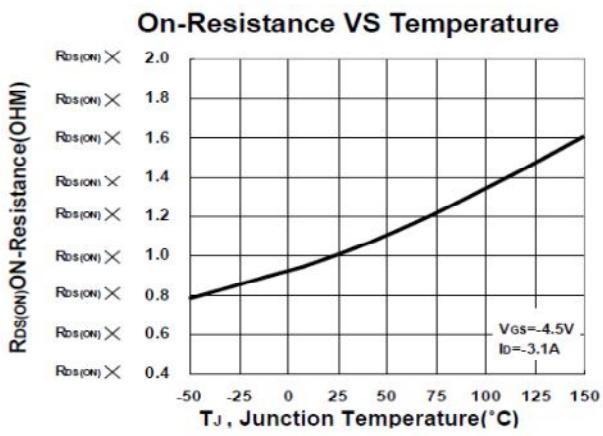
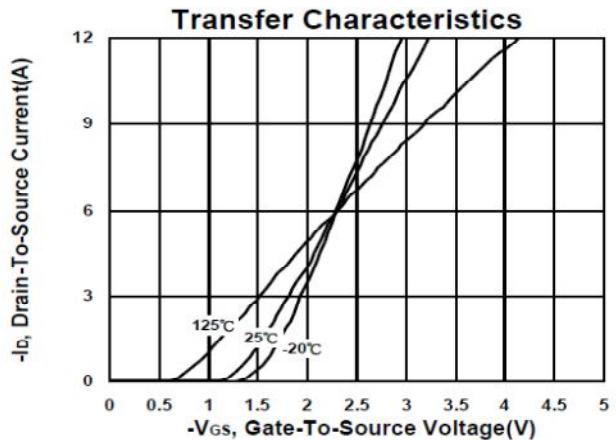
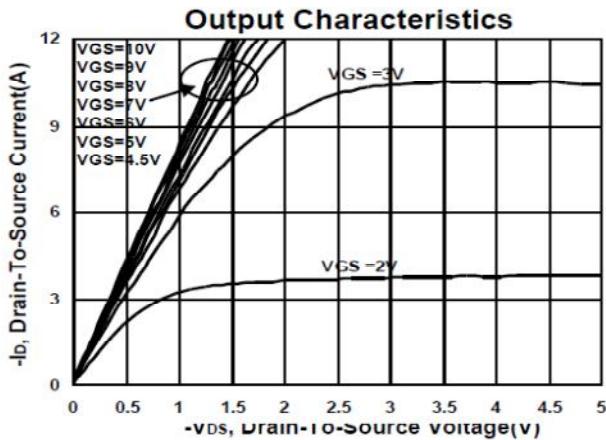
**P6002OAG****N&P-Channel Enhancement Mode MOSFET**
**TYPICAL PERFORMANCE CHARACTERISTICS
N-CHANNEL**


P6002OAG**N&P-Channel Enhancement Mode MOSFET**

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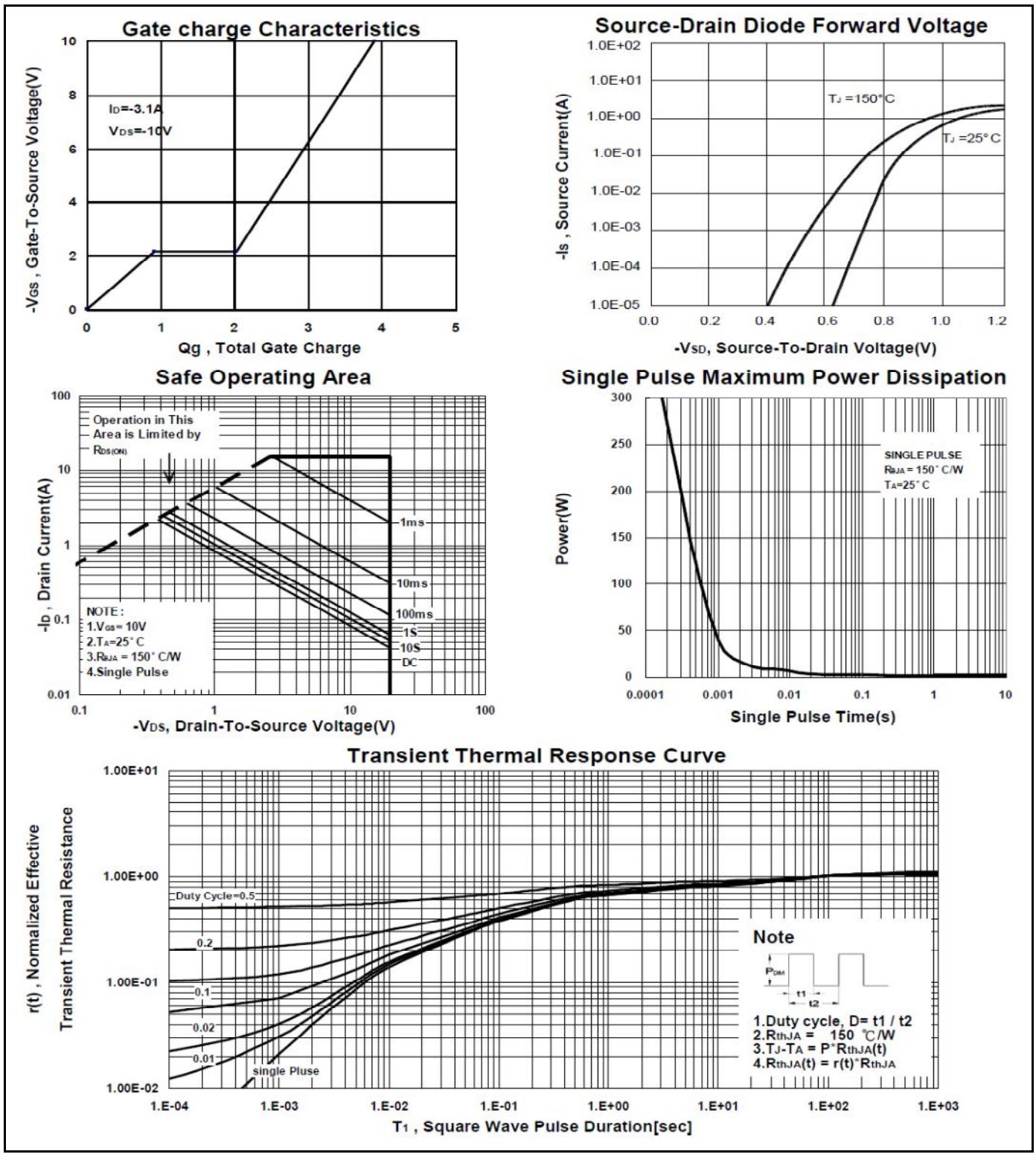
N&P-Channel Enhancement Mode MOSFET

TYPICAL PERFORMANCE CHARACTERISTICS P-CHANNEL



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N&P-Channel Enhancement Mode MOSFET



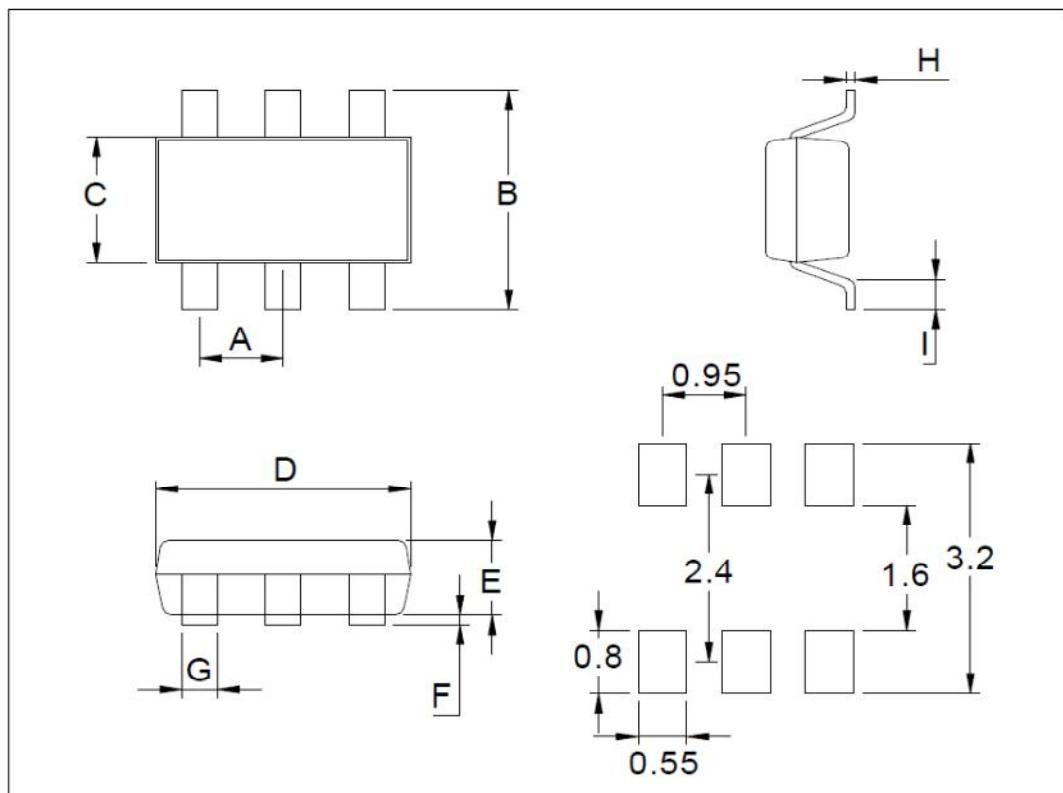
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N&P-Channel Enhancement Mode MOSFET

Package Dimension

TSOP- 6 MECHANICAL DATA

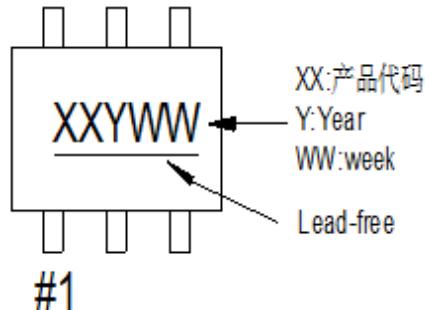
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.6		3	I	0.33		0.57
C	1.5		1.7	J			
D	2.8		3.02	K			
E	0.7		0.85	L			
F	0		0.1	M			
G	0.35		0.5	N			



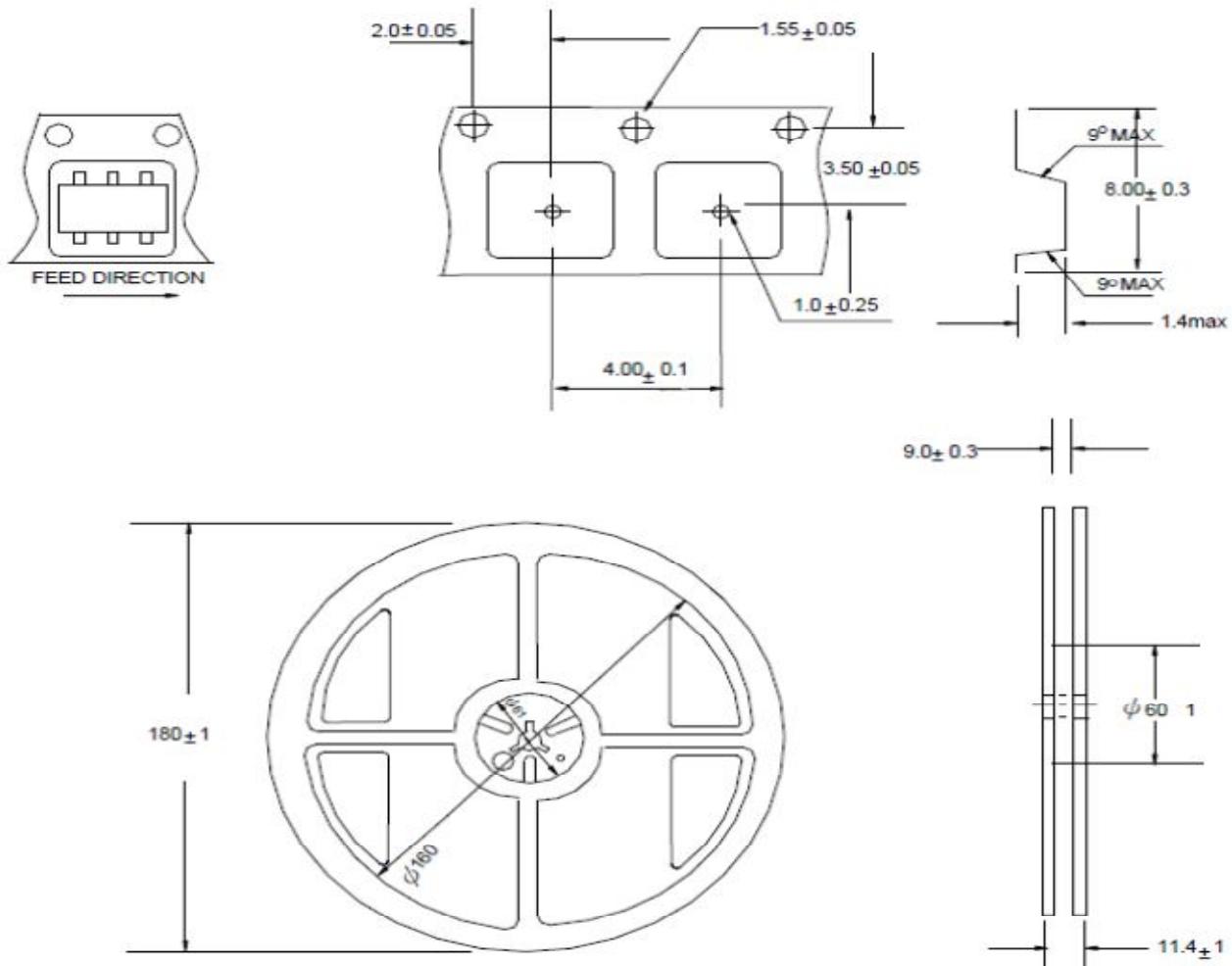
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N&P-Channel Enhancement Mode MOSFET

A. Marking Information(产品代码为: 52)



B. Tape&Reel Information: 3000pcs/Reel

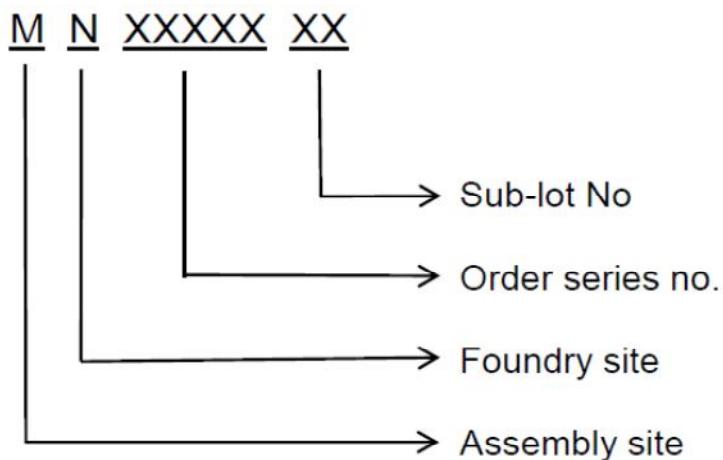


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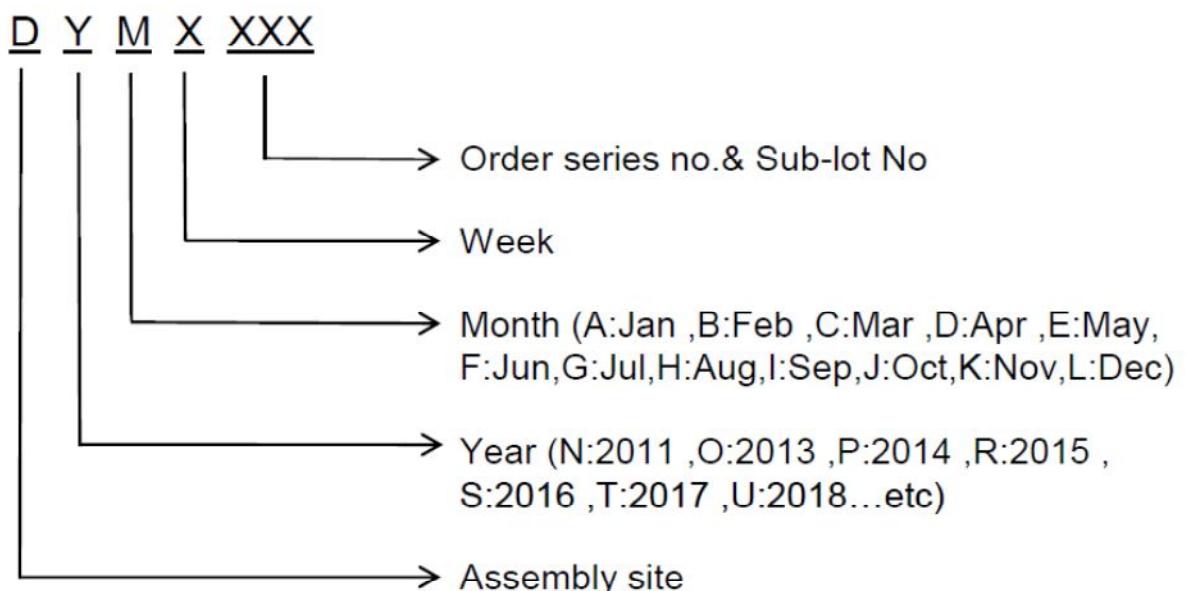
N&P-Channel Enhancement Mode MOSFET

C. Lot No.&Date Code rule

1. Lot No.



2. Date Code



P6002OAG

N&P-Channel Enhancement Mode MOSFET

D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert “ / ” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least