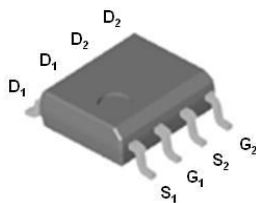


# P8008HV

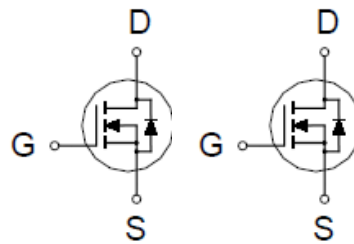
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
80V	80m $\Omega$ @ $V_{GS} = 10V$	4A



SOP- 08



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	80	V
Gate-Source Voltage		$V_{GS}$	$\pm 25$	
Continuous Drain Current	$T_A = 25\text{ }^\circ\text{C}$	$I_D$	4	A
	$T_A = 70\text{ }^\circ\text{C}$		3	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	20	
Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	$P_D$	1.9	W
	$T_A = 70\text{ }^\circ\text{C}$		1.2	
Operating Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$
Lead Temperature ( $1/16$ " from case for 10 sec.)		$T_L$	275	

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		65	$^\circ\text{C} / \text{W}$
Junction-to-Lead	$R_{\theta JL}$		25	

<sup>1</sup>Pulse width limited by maximum junction temperature.

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## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	80			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.0	2.0	3.0	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±25V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 64V, V <sub>GS</sub> = 0V			1	μA
		V <sub>DS</sub> = 64V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 70 °C			10	
On-State Drain Current <sup>1</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 10V	20			A
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1A		70	90	mΩ
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A		60	80	
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A		7.5		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz		1165	1400	pF
Output Capacitance	C <sub>oss</sub>			104		
Reverse Transfer Capacitance	C <sub>rss</sub>			57		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz		1.5	1.8	Ω
Total Gate Charge <sup>2</sup> (10V)	Q <sub>g</sub>	V <sub>DS</sub> = 0.5V <sub>(BR)DSS</sub> , V <sub>GS</sub> = 10V, I <sub>D</sub> = 4A		29.0		nC
Total Gate Charge <sup>2</sup> (4.5V)	Q <sub>g</sub>			12.0		
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>			2.0		
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>			6.8		
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>	V <sub>DS</sub> = 0.5V <sub>(BR)DSS</sub> , R <sub>L</sub> = 40Ω I <sub>D</sub> ≅ 4A, V <sub>GS</sub> = 10V, R <sub>G</sub> = 3.3Ω		6.0		nS
Rise Time <sup>2</sup>	t <sub>r</sub>			3.8		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>			21.0		
Fall Time <sup>2</sup>	t <sub>f</sub>			5.0		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current	I <sub>S</sub>				4	A
Pulsed Current <sup>3</sup>	I <sub>SM</sub>				20	
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 1A, V <sub>GS</sub> = 0V		1	1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = I <sub>S</sub> , dI <sub>F</sub> /dt = 100A / μS		30		nS
Reverse Recovery Charge	Q <sub>rr</sub>				40	

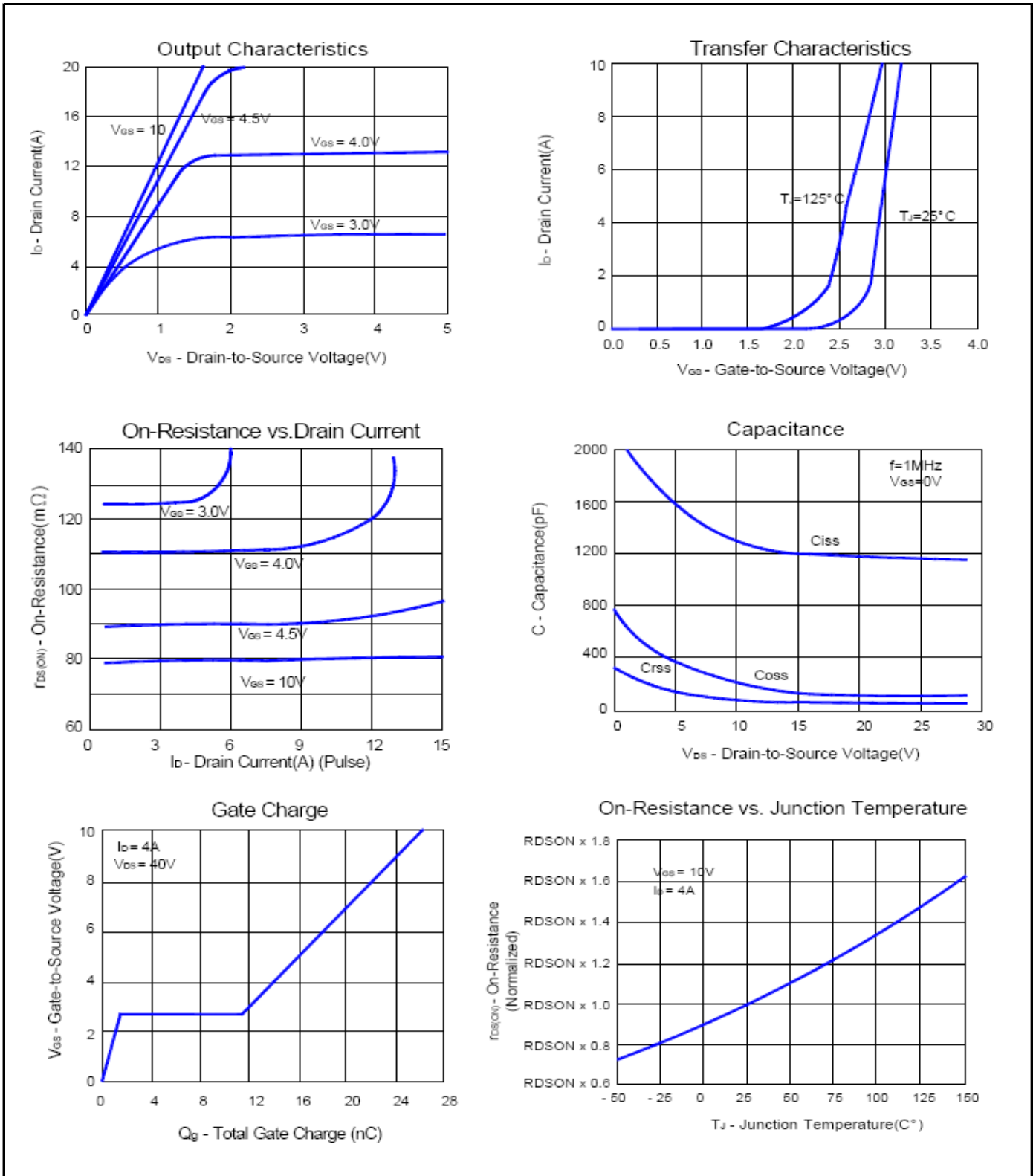
<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

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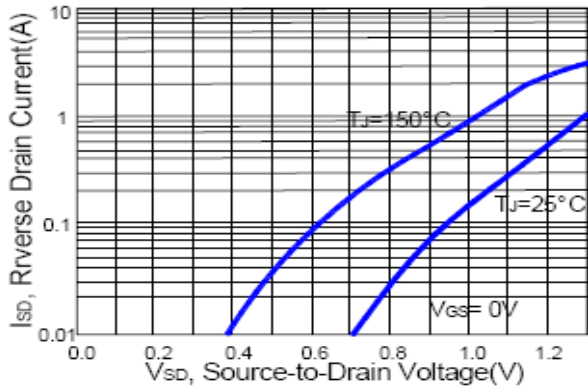
## N-Channel Enhancement Mode MOSFET



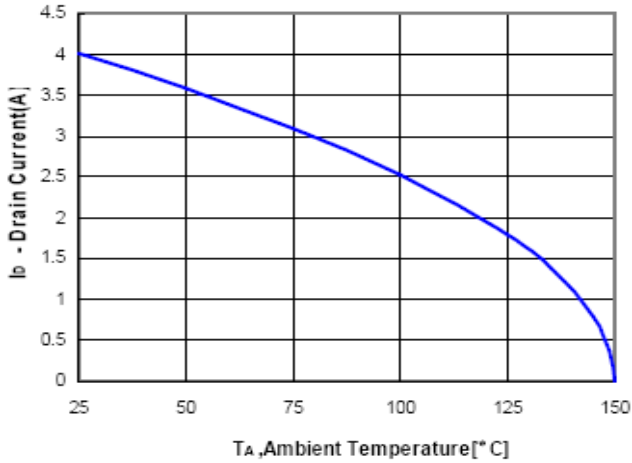
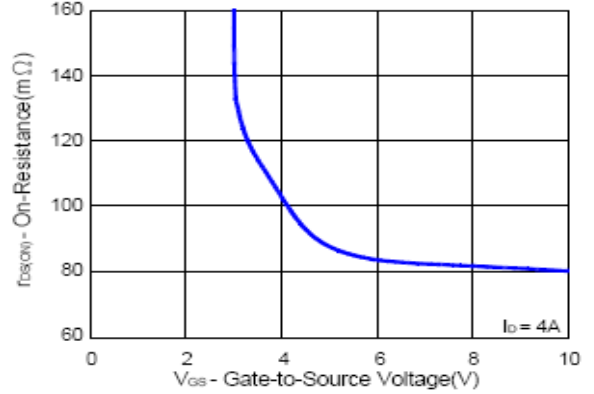
# P8008HV

## N-Channel Enhancement Mode MOSFET

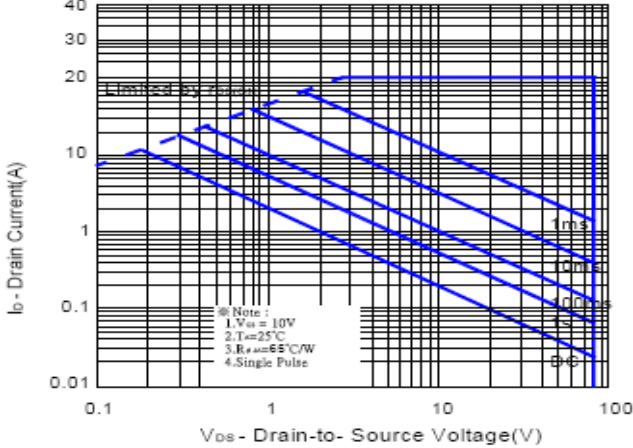
Typical Source-Drain Diode Forward Voltage



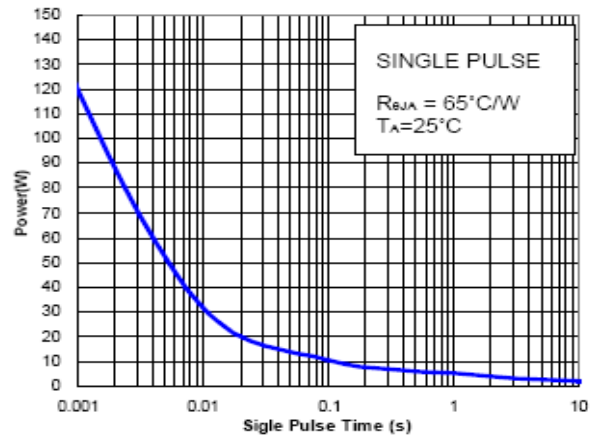
On-Resistance vs. Gate-to-Source Voltage



Safe Operating Area, Junction-to-Case

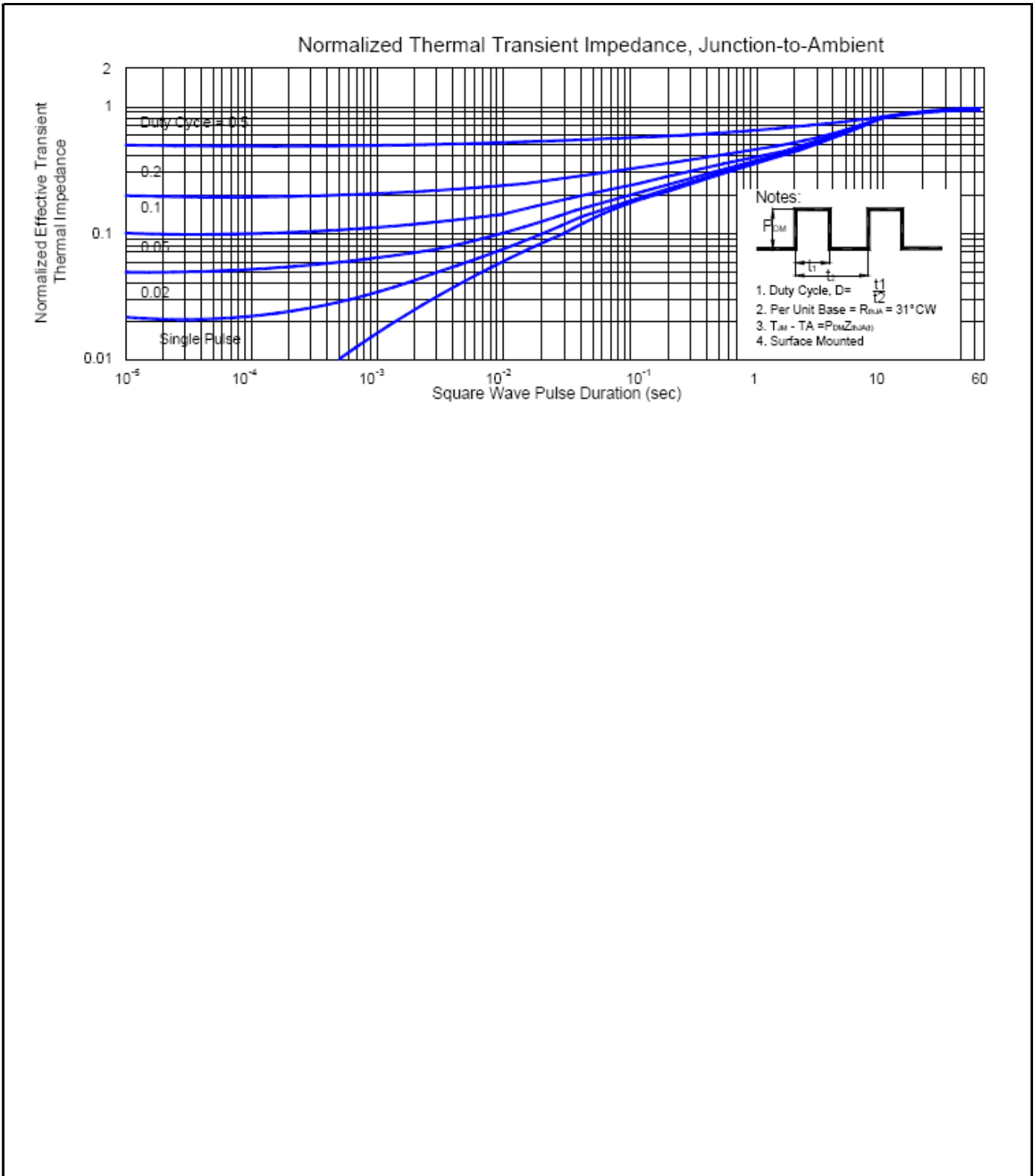


Single Pulse Maximum Power dissipation



# P8008HV

## N-Channel Enhancement Mode MOSFET



# P8008HV

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

