

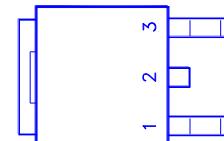
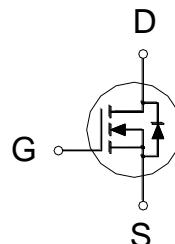
**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****P8010BD**

TO-252

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
100V	85mΩ	15A

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS		UNITS
Drain-Source Voltage		$V_{DS}$	100		V
Gate-Source Voltage		$V_{GS}$	$\pm 20$		V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	15		A
	$T_C = 100^\circ\text{C}$		9		
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	35		
Avalanche Current		$I_{AS}$	12		
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	7.2		mJ
MOSFET dV/dt Ruggedness		dV/dt	16.2		V/nS
Peak Diode Recovery dV/dt <sup>2</sup>			4.1		
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	46		W
	$T_C = 100^\circ\text{C}$		18		
Junction & Storage Temperature Range		$T_J, T_{stg}$	-55 to 150		°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.7	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>ID=15A,di/dt=100A/uS,VDD<BVdss,Starting Tj=25°C**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$			1	
		$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	μA

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On-State Drain Current <sup>1</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 10V	35			A
Drain-Source On-State Resistance <sub>1</sub>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		67	95	mΩ
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		61	85	
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 15A		25		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz		527		pF
Output Capacitance	C <sub>oss</sub>			68		
Reverse Transfer Capacitance	C <sub>rss</sub>			37		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz		1.5		Ω
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 10 V , V <sub>DS</sub> = 0.5V <sub>(BR)DSS</sub> , I <sub>D</sub> = 15A		18.5		nC
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>			2.7		
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>			5.1		
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>	V <sub>DS</sub> = 40V  I <sub>D</sub> ≈ 15A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 6Ω		11		nS
Rise Time <sup>2</sup>	t <sub>r</sub>			48		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>			80		
Fall Time <sup>2</sup>	t <sub>f</sub>			73		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current	I <sub>S</sub>				15	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 15A, V <sub>GS</sub> = 0V			1.1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 15A, dI <sub>F</sub> /dt = 100A / μS		33		nS
Reverse Recovery Charge	Q <sub>rr</sub>			35		nC

<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.<sup>2</sup>Independent of operating temperature.

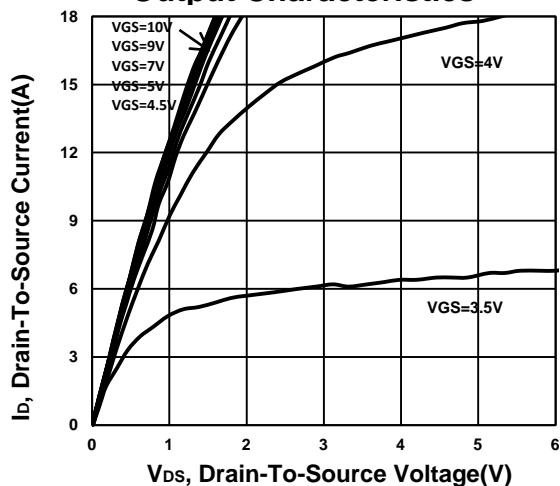
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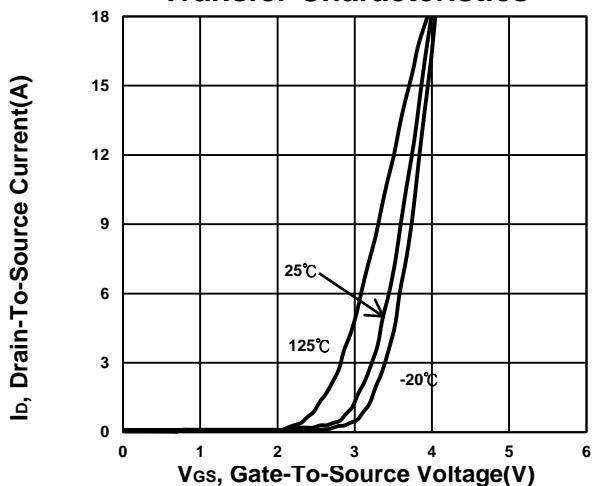
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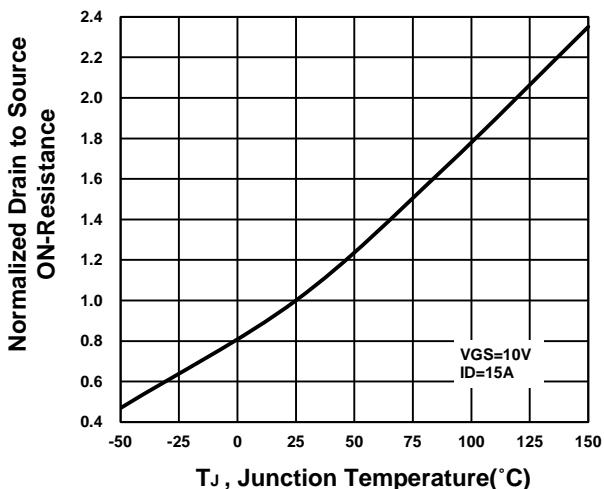
**Output Characteristics**



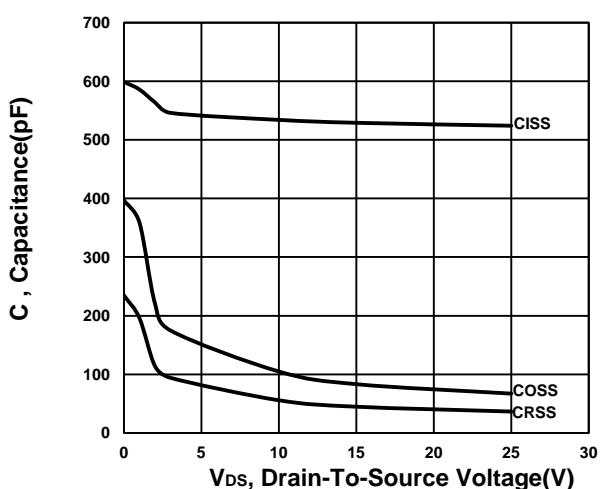
**Transfer Characteristics**



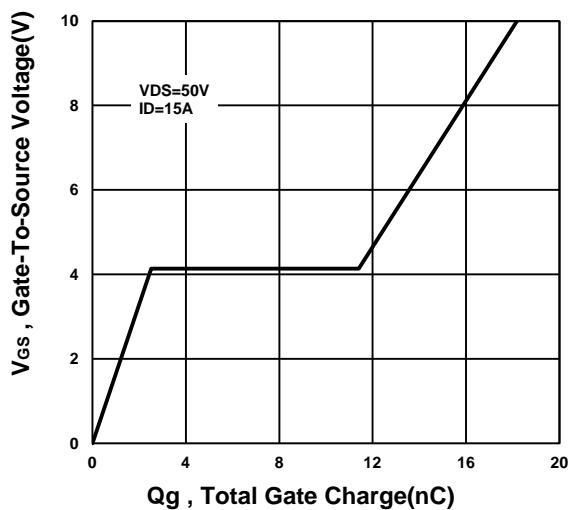
**On-Resistance VS Temperature**



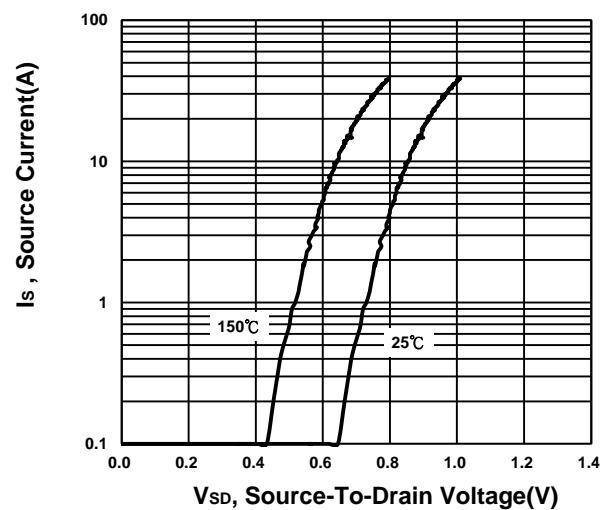
**Capacitance Characteristic**



**Gate charge Characteristics**



**Source-Drain Diode Forward Voltage**



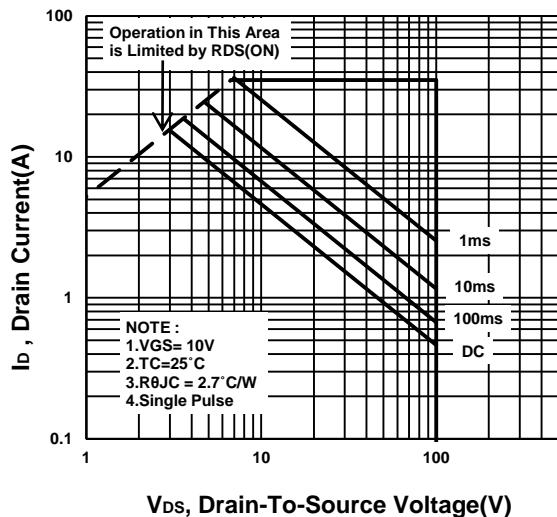
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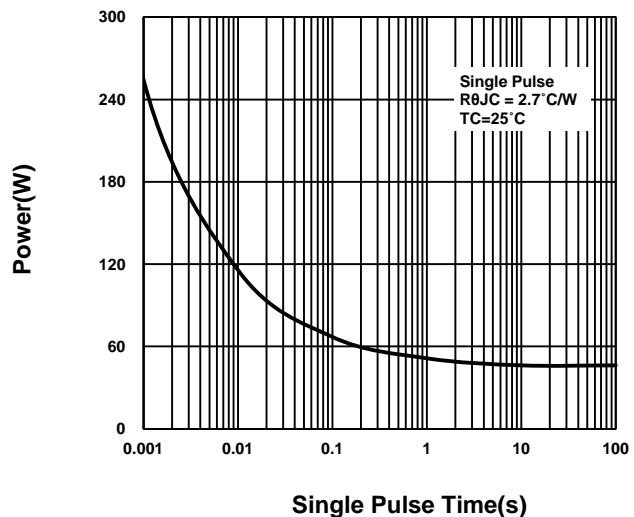
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**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**

