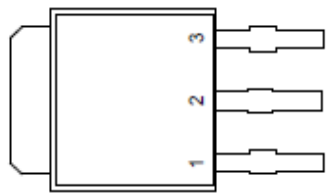


P8010BIS

N-Channel Enhancement Mode MOSFET

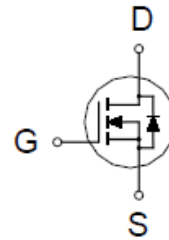
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	85mΩ @ $V_{GS} = 10V$	15A



TO-251(S)

- 1.GATE
- 2.DRAIN
- 3.SOURCE



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current	$T_C = 25\text{ °C}$	I_D	15	A
	$T_C = 100\text{ °C}$		9	
Pulsed Drain Current ¹		I_{DM}	35	
Avalanche Current		I_{AS}	12	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	7.2	mJ
Power Dissipation	$T_C = 25\text{ °C}$	P_D	46	W
	$T_C = 100\text{ °C}$		18	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.7	°C / W

¹Pulse width limited by maximum junction temperature.

P8010BIS

N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

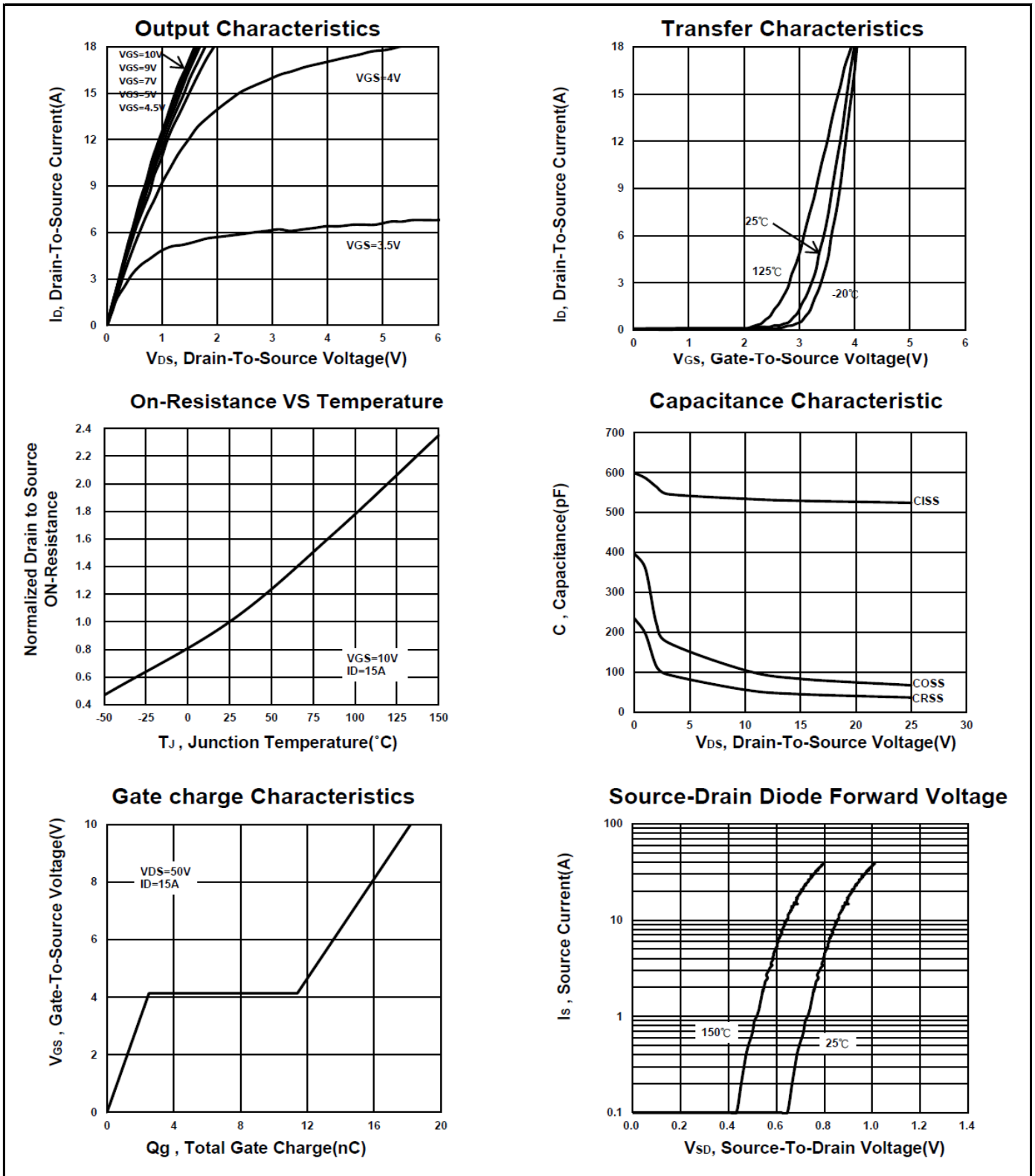
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.3	1.8	2.3	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V			1	μA
		V _{DS} = 80V, V _{GS} = 0V, T _J = 125 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 10V	35			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 10A		67	95	mΩ
		V _{GS} = 10V, I _D = 15A		61	85	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 15A		25		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		527		pF
Output Capacitance	C _{oss}			68		
Reverse Transfer Capacitance	C _{rss}			37		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.5		Ω
Total Gate Charge ²	Q _g	V _{GS} = 10V, V _{DS} = 0.5V _{(BR)DSS} , I _D = 15A		18.5		nC
Gate-Source Charge ²	Q _{gs}			2.7		
Gate-Drain Charge ²	Q _{gd}			5.1		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 40V, I _D ≅ 15A, V _{GS} = 10V, R _{GEN} = 6Ω		11		nS
Rise Time ²	t _r			48		
Turn-Off Delay Time ²	t _{d(off)}			80		
Fall Time ²	t _f			73		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				15	A
Forward Voltage ¹	V _{SD}	I _F = 15A, V _{GS} = 0V			1.1	V
Reverse Recovery Time	t _{rr}	I _F = 15A, di _F /dt = 100A / μS		33		nS
Reverse Recovery Charge	Q _{rr}				35	

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

P8010BIS

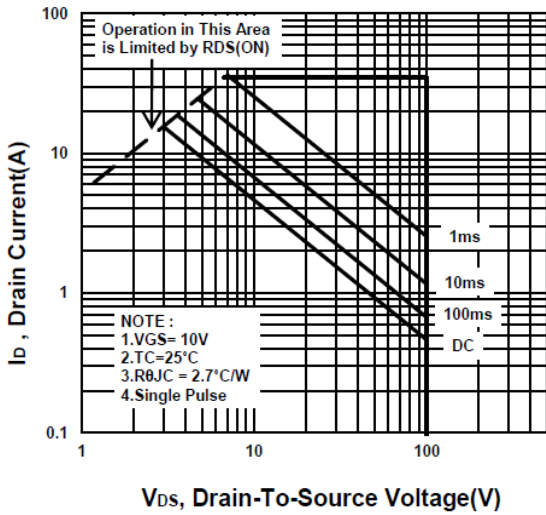
N-Channel Enhancement Mode MOSFET



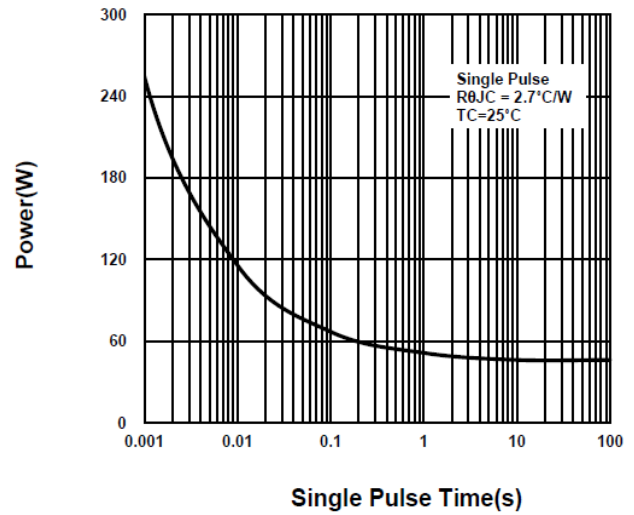
P8010BIS

N-Channel Enhancement Mode MOSFET

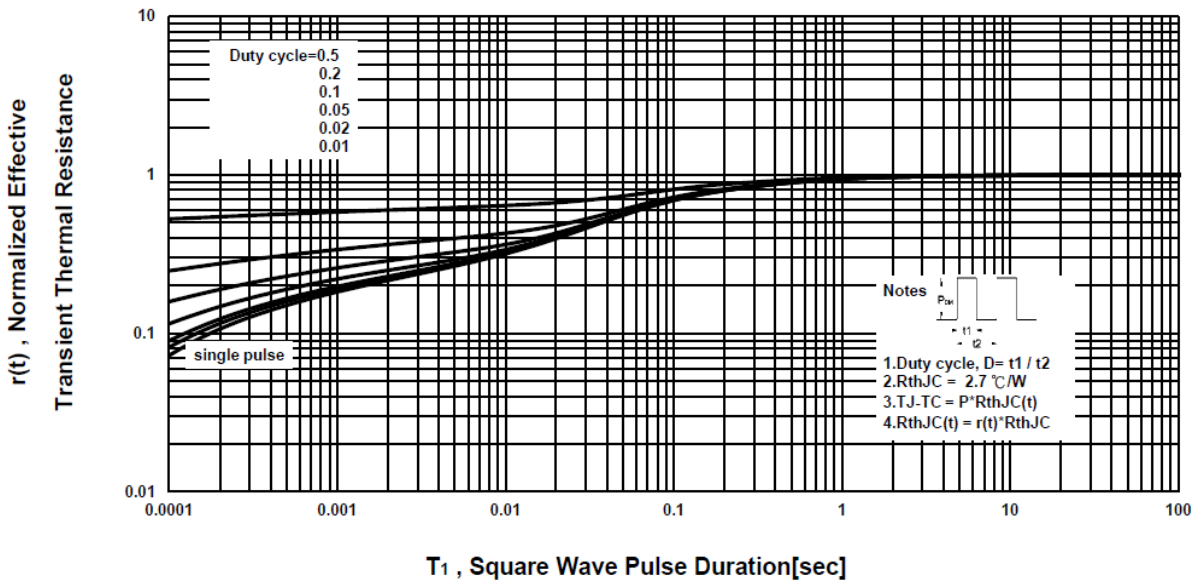
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



P8010BIS

N-Channel Enhancement Mode MOSFET

Package Dimension

TO-251 (IS) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	6.3	6.6	6.8	H	2.1	2.3	2.5
B	4.8	5.3	5.5	J	0.4	0.5	0.6
C	6.7		7.57	K	0.35	0.5	0.65
D	3	3.5	4.5	L	0.9		1.5
E		2.3		M	5.3		6.22
F	0.6	0.9	1.1	N	1.4	1.6	2.1
G	0.4		0.89				

