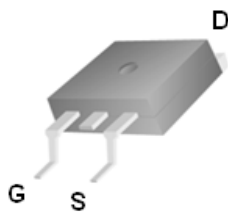


# P9006ESG

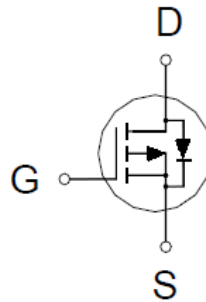
## P-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
-60V	90mΩ @ $V_{GS} = 10V$	-18A



TO-263



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	-60	V
Gate-Source Voltage		$V_{GS}$	±20	
Continuous Drain Current	$T_C = 25\text{ °C}$	$I_D$	-18	A
	$T_C = 100\text{ °C}$		-12	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	-48	
Avalanche Current		$I_{AS}$	-22	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	24	mJ
Power Dissipation	$T_C = 25\text{ °C}$	$P_D$	54	W
	$T_C = 100\text{ °C}$		22	
Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.3	°C / W

<sup>1</sup>limited by maximum junction temperature.

# P9006ESG

## P-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1	-1.7	-3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -48V, V <sub>GS</sub> = 0V			-1	μA
		V <sub>DS</sub> = -40V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C			-10	
On-State Drain Current <sup>1</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = -5V, V <sub>GS</sub> = -10V	-48			A
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3.5A		70	135	mΩ
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -4.5A		54	90	
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -4.5A		12		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = -25V, f = 1MHz		1070		pF
Output Capacitance	C <sub>oss</sub>			99		
Reverse Transfer Capacitance	C <sub>rss</sub>			64		
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 0.5V <sub>(BR)DSS</sub> , V <sub>GS</sub> = -10V, I <sub>D</sub> = -4.5A		22		nC
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>			3		
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>			6		
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>	V <sub>DS</sub> = -20V, I <sub>D</sub> ≅ -5A, V <sub>GS</sub> = -10V, R <sub>GEN</sub> = 6Ω		9		nS
Rise Time <sup>2</sup>	t <sub>r</sub>			15		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>			36		
Fall Time <sup>2</sup>	t <sub>f</sub>			11		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current	I <sub>S</sub>				-18	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = -4.5A, V <sub>GS</sub> = 0V			-1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -4.5A, di <sub>F</sub> /dt = 100A / μS		19		nS
Reverse Recovery Charge	Q <sub>rr</sub>			14		nC

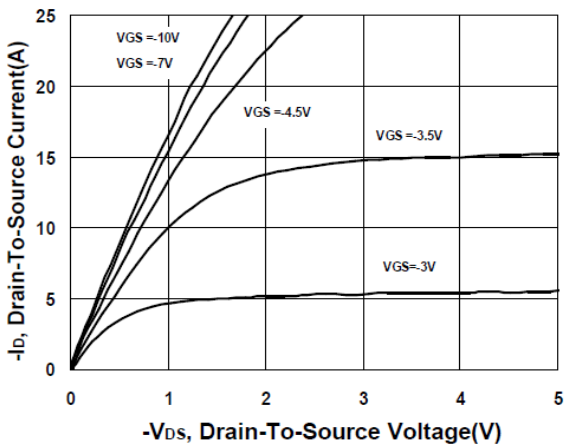
<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

<sup>2</sup>Independent of operating temperature.

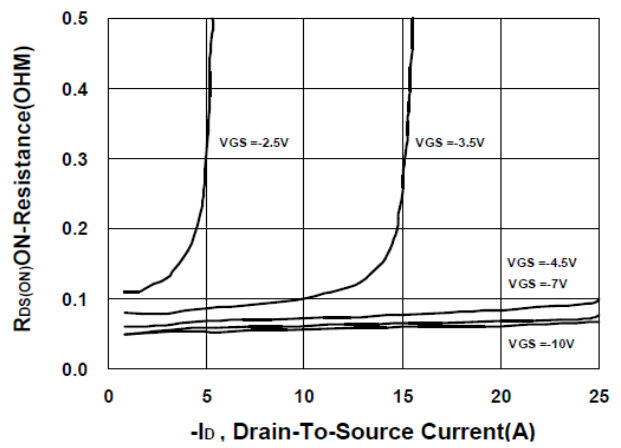
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## P-Channel Enhancement Mode MOSFET

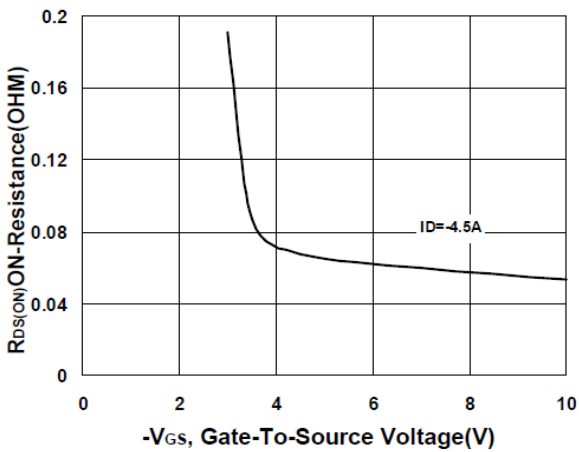
**Output Characteristics**



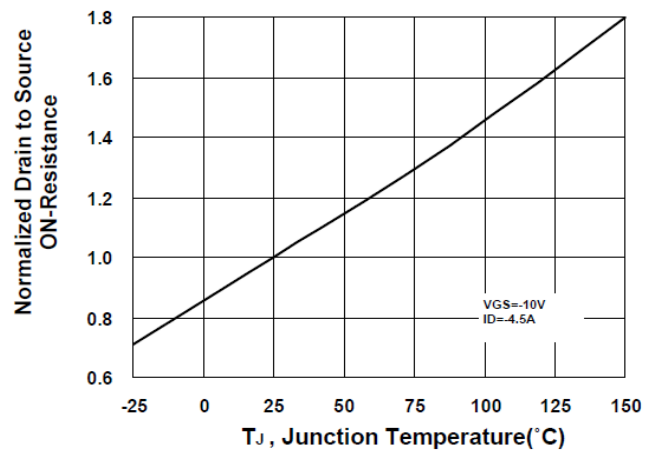
**On-Resistance VS Drain Current**



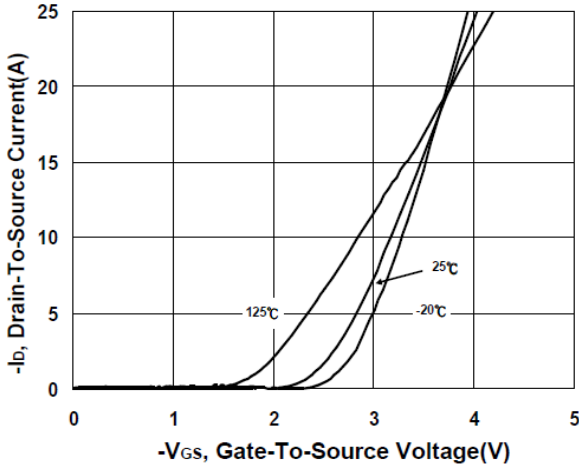
**On-Resistance VS Gate-To-Source**



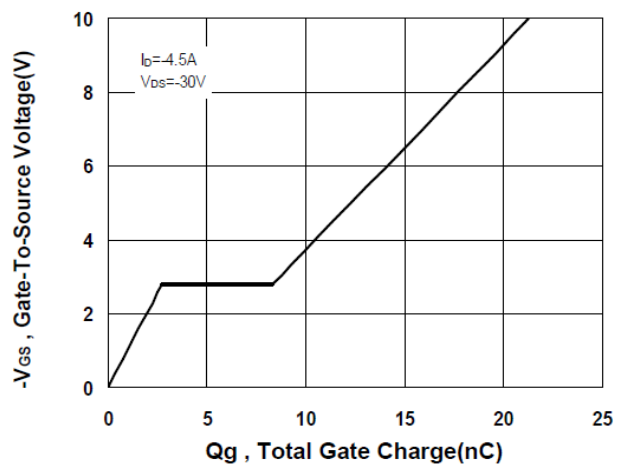
**On-Resistance VS Temperature**



**Transfer Characteristics**

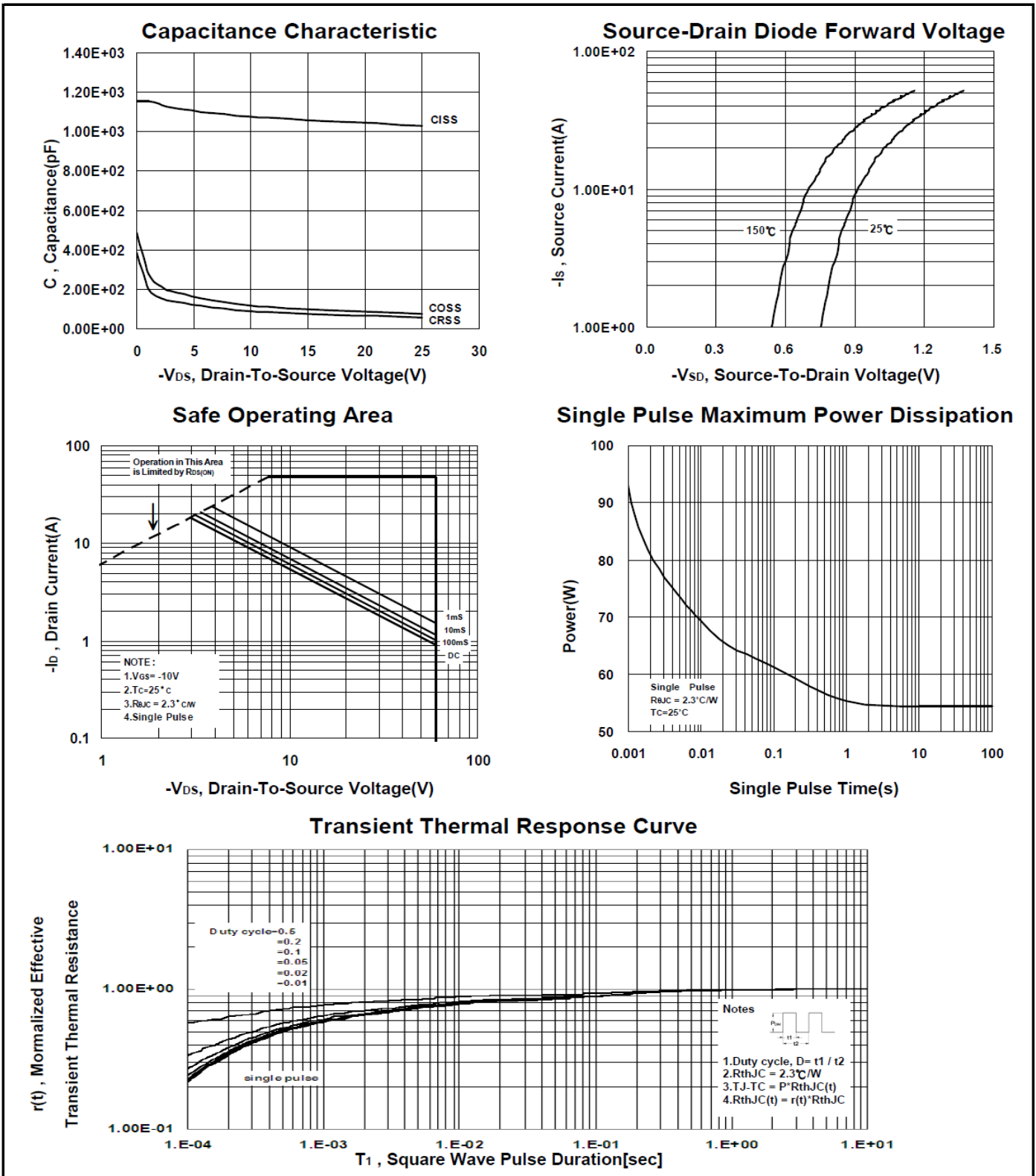


**Gate charge Characteristics**



# P9006ESG

## P-Channel Enhancement Mode MOSFET



# P9006ESG

## P-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-263 (D<sup>2</sup>PAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.8	e	4.08	5.08	6.08
A1	0		0.3	E	9.8		10.55
b	0.71		1.06	E1	6.9		8.7
b2	1.07		1.47	H	14.2		15.8
C	0.3		0.69	L	1.2		2.79
C2	1.15		1.45	L1	1		1.65
D	8.3		9.4	L2	1.2		1.78
D1	6.37		8.23				

